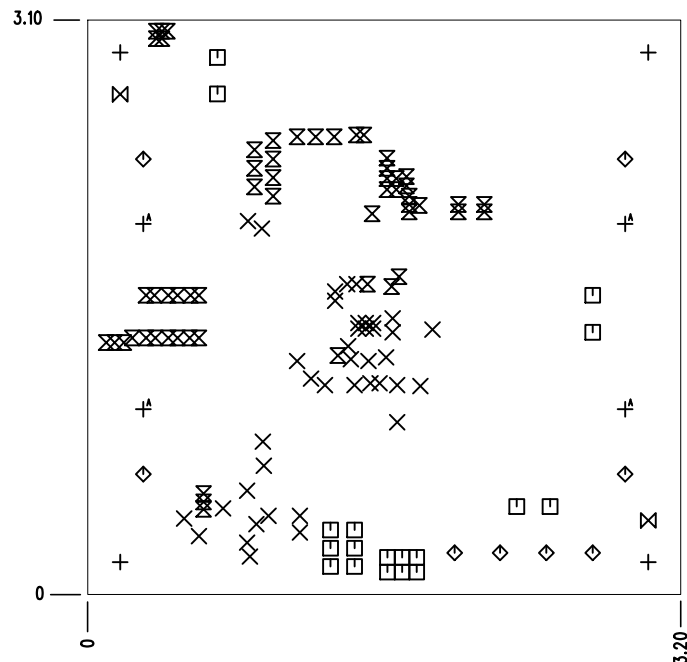
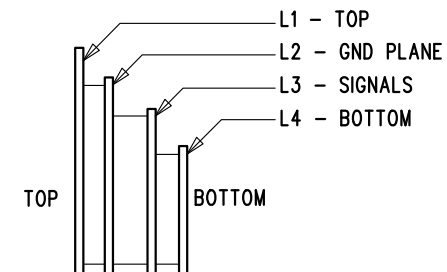


REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	1	PRODUCTION	HT00 K. 10-03-17

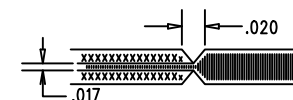



SIZE	QTY	SYM	PLATED	TOL
0.01	40	×	YES	+/-0.003
0.015	57	⊗	YES	+/-0.003
0.035	18	□	YES	+/-0.003
0.07	2	⊠	NO	+/-0.003
0.094	8	◇	YES	+/-0.003
0.187	4	+	NO	+/-0.003
0.207	4	⊕	YES	+/-0.003



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED		APPROVALS				1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES TOLERANCES: XX = ± 0.01 XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	LT			TITLE: FABRICATION DRAWING	
		APP ENG.	HT00 K.			FAST 60V PROTECTED HIGH-SIDE NMOS STATIC SWITCH DRIVER	
						SIZE	IC NO.
						N/A	LTC7003EMSE
						DEMO CIRCUIT 2749A	
						REV	1
		SCALE = NONE		FILENAME: DC2749A-1.PCB		SHT 1 OF 1	