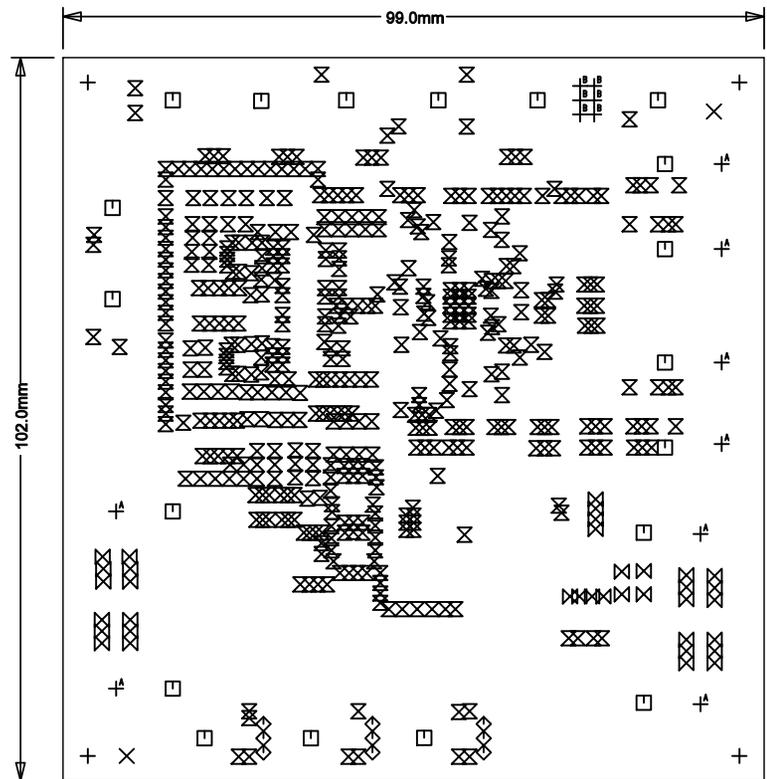
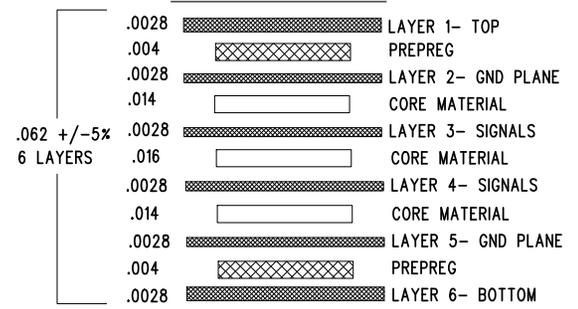


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PROTOTYPE	LJ	10-14-19



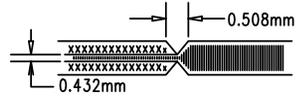
COMPONENT SIDE SHOWN

LAYER STRUCTURE (INCHES)

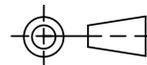


NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 1.6 +/- 0.1mm
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON ALL LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.025mm THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.075mm IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



SIZE	QTY	SYM	PLATED	TOL
0.25	497	⊗	YES	+/-0.008
0.51	36	⊗	YES	+/-0.008
0.76	9	◇	YES	+/-0.008
0.9	6	⊕ ^B	YES	+/-0.008
1.78	2	⊗	NO	+/-0.008
2.4	19	□	YES	+/-0.008
4.75	4	⊕	NO	+/-0.008
5.21	8	⊕ ^A	YES	+/-0.008

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN METRIC TOLERANCES: 0.XX = ±0.25mm 0.XXX = ±0.125mm INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION 	APPROVALS		  <small>www.analog.com FOR ADI CUSTOMER USE ONLY</small>			
	PCB DES.	LT		TITLE: FABRICATION DRAWING LOW IQ HIGH FREQUENCY SYNCHRONOUS STEP-UP AND DUAL STEP-DOWN SUPPLIES		
	APP ENG.	LJ	SIZE		IC NO.	REV
	SCALE = NONE		N/A		LTC7817EUHF DC3033A	1
FILENAME: DC3033A-1.PCB			SHT 1 OF 1			