


DATE: 11-4-12  
ISOLATED 100MHz ADC - SPI INTERFACE  
DC502A-5 \* LTM2893IY  
LINEAR TECHNOLOGY  
BOTTOM SILKSCREEN LAYER

| REVISION HISTORY |     |               |          |         |
|------------------|-----|---------------|----------|---------|
| ECO              | REV | DESCRIPTION   | APPROVED | DATE    |
| -                | 2   | 2ND PROTOTYPE | KWB      | 11-4-15 |

|             |     |   |                                 |   |  |  |      |
|-------------|-----|---|---------------------------------|---|--|--|------|
| APPROVALS   |     |  <b>LINEAR</b><br>TECHNOLOGY |                                 | 1630 McCarthy Blvd.<br>Milpitas, CA 95035<br>Phone: (408) 432-1900<br>Fax: (408) 434-0507<br>www.linear.com |  | LTC CONFIDENTIAL<br>FOR CUSTOMER<br>USE ONLY |      |
| PCB DES.    | KWB |   |                                 |   |  |  |      |
| APP ENG.    | KWB |   |                                 |   |  |  |      |
|             |     | TITLE: BOTTOM ASSEMBLY DRAWING<br>ISOLATED 100MHz ADC - SPI INTERFACE   |                                 |   |  |  |      |
|             |     | SIZE  | IC NO.                          |   |  |  | REV. |
|             |     | N/A   | LTM2893IY<br>DEMO CIRCUIT 2405A |   |  |  | 2    |
| SCALE = 1:1 |     | DATE: Wednesday, November 4th, 2015   |                                 |   |  | SHEET 1 OF 1                                 |      |