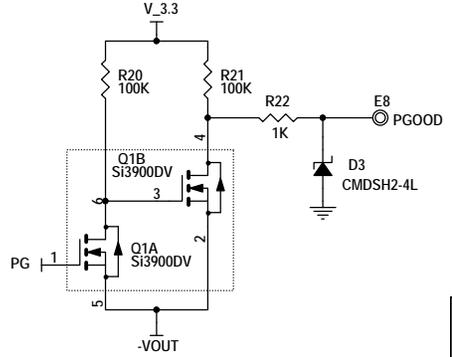
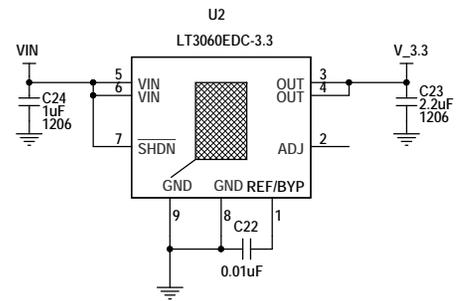
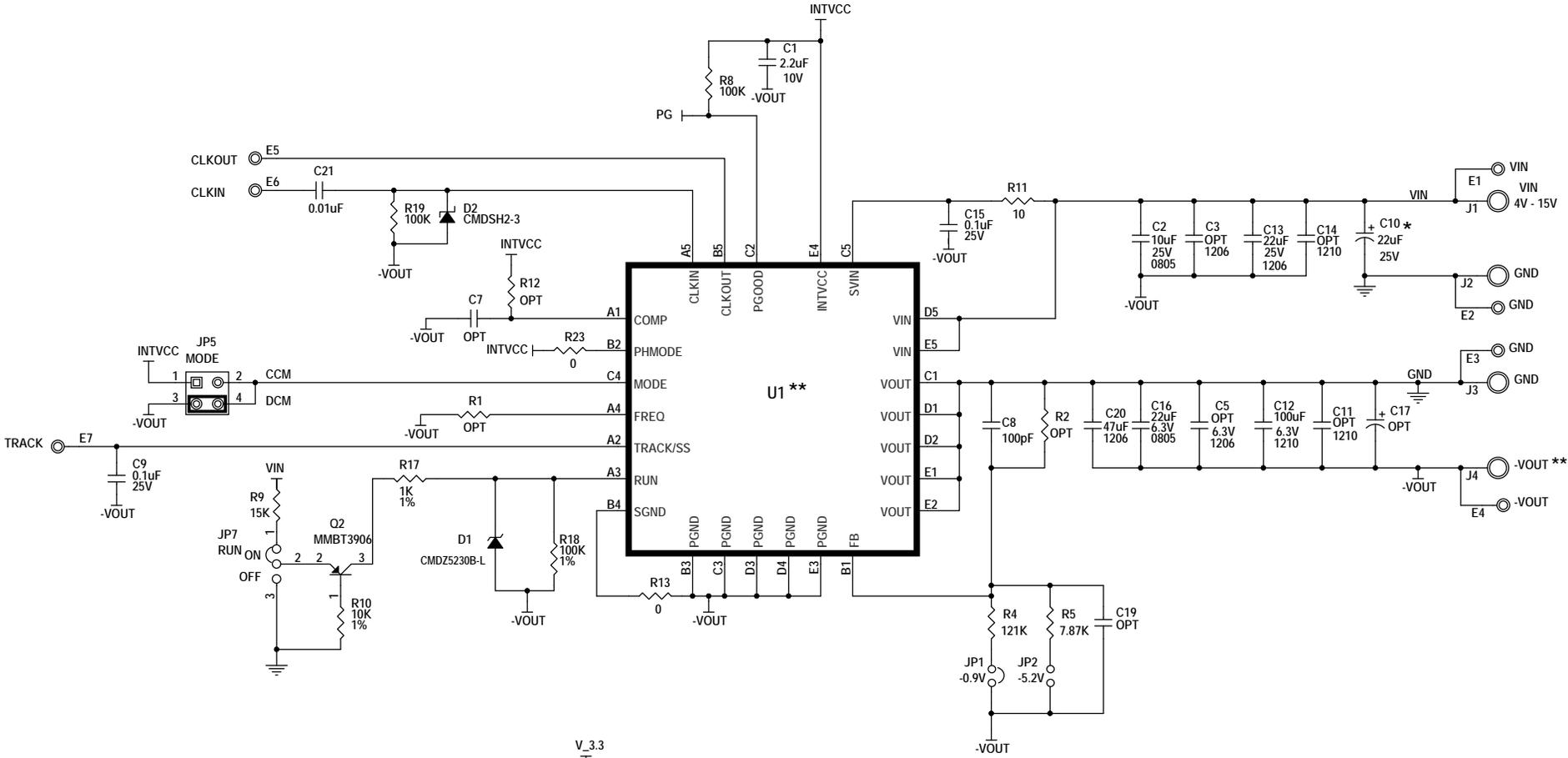


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION	SAM Y.	8-25-2017



** C10 IS AN OPTIONAL CAPACITOR. IT IS INSERTED TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, THE CAPACITOR IS NOT NEEDED.
 FOR $|V_{OUT}| > 3.3V$, 2MHz SWITCHING FREQUENCY IS RECOMMENDED
 CLKOUT, TRACK SIGNALS ARE NOT LEVEL SHIFTED (REFERENCED TO -VOUT)

**		
VERSION	U1	Iout
- A	LTM4623EV	2A
- B	LTM4625EY	3A

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL CAPACITORS AND RESISTORS ARE 0603
2. VIN + |VOUT| SHOULD NOT EXCEED 20V

CUSTOMER NOTICE		APPROVALS			1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only	
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	LT		TITLE: SCHEMATIC	
		APP ENG.	SAM Y.	TINY, HIGH EFFICIENCY, 2A INVERTING BUCK-BOOST DC/DC μ MODULE REGULATOR		
				SIZE	IC NO.	REV.
				N/A	LTM4623EY / LTM4625EY	2
				DEMO CIRCUIT 2721A(A,B)		
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SCALE = NONE		DATE:	Friday, August 25, 2017	SHEET 1 OF 1