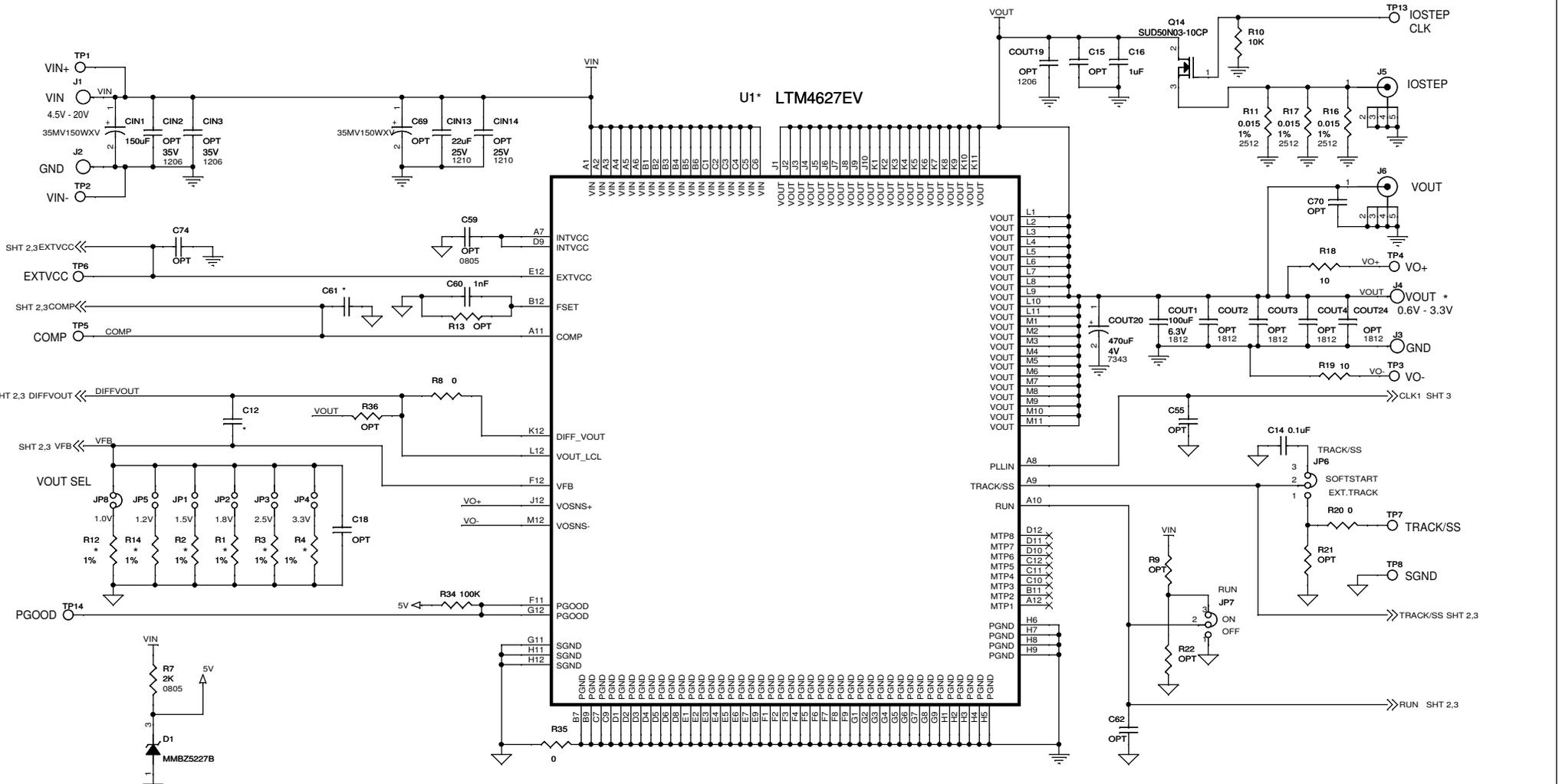


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	1	PRODUCTION	SAM Y.	07/12/11



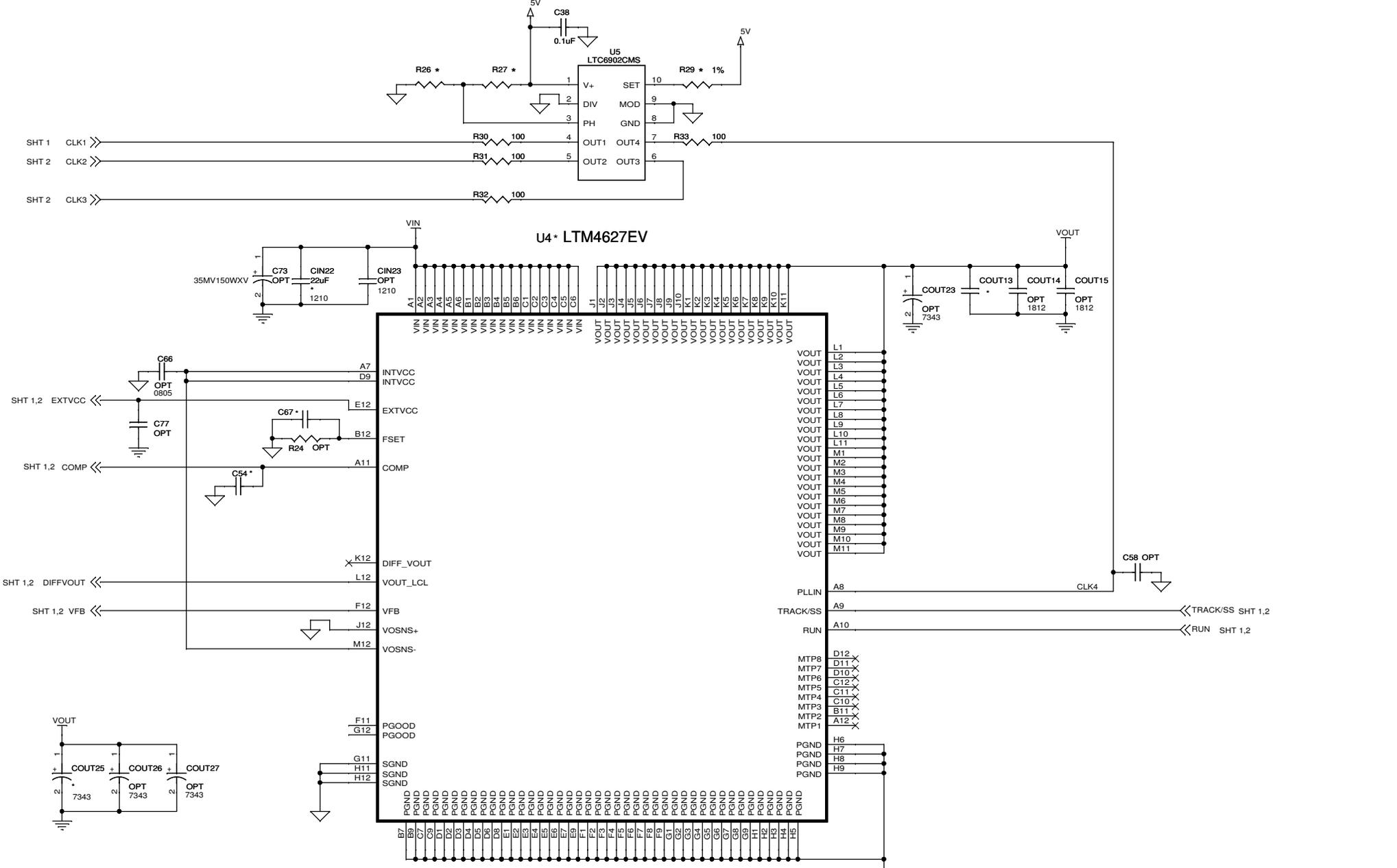
* ASSY	IC	IOUT	R12	R14	R2	R1	R3	R4	R26	R27	R29	PHASE No.	CIN20	CIN22	COUT9	COUT13	COUT25	COUT22	C12	C61	C52	C67	C54	C53	C65
- A	U1,U2	30A	45.3k	30.1k	20k	15k	9.53k	6.65k	0	OPT	402K	2	OPT	OPT	OPT	OPT	OPT	OPT	47pF	47pF	47pF	OPT	OPT	OPT	OPT
- B	U1-U3	40A	30.1k	20k	13.3k	10k	6.34k	4.42k	OPT	OPT	133K	3	22uF	OPT	100uF	OPT	OPT	Poscap 4TPF470ML	100pF	68pF	68pF	OPT	OPT	68pF	1nF
- C	U1-U4	50A	22.6k	15k	10k	7.5k	4.75k	3.32k	OPT	0	100K	4	22uF	22uF	100uF	100uF	100uF	Poscap 4TPF470ML	100pF	180pF	180pF	1nF	180pF	180pF	1nF

\* FSW = 500KHz

NOTES: UNLESS OTHERWISE SPECIFIED,  
1. ALL RESISTORS AND CAPACITORS ARE 0603.

CUSTOMER NOTICE		APPROVALS		 1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	MI	
		APP ENG.	SAM Y.	
		TITLE: SCHEMATIC HIGH EFFICIENCY POLYPHASE STEP-DOWN POWER $\mu$ MODULE <sup>®</sup>		
		SIZE	IC NO.	REV.
		N/A	LTM4627EV DEMO CIRCUIT 1668B	1
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SCALE = NONE		DATE: Tuesday, July 12, 2011
				SHEET 1 OF 3





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THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		APP ENG.	SAM Y.	SIZE	IC NO.	1
		SCALE = NONE		N/A	<b>LTM4627EV</b> <b>DEMO CIRCUIT 1668B</b>	1
				DATE: Tuesday, July 12, 2011		SHEET 3 OF 3