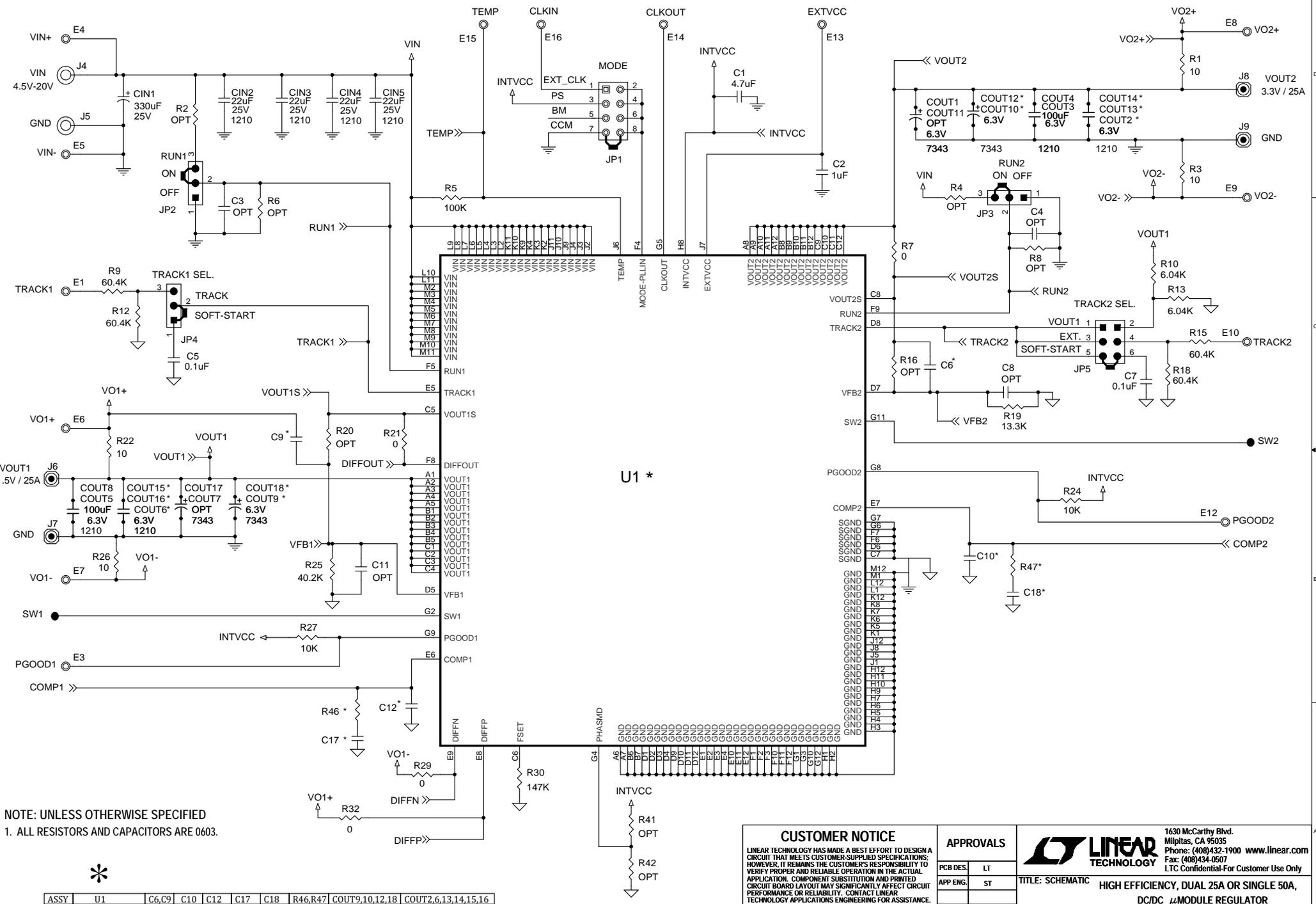


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	PROTOTYPE	ST	03-28-17



NOTE: UNLESS OTHERWISE SPECIFIED
 1. ALL RESISTORS AND CAPACITORS ARE 0603.

*

ASSY	U1	C6,C9	C10	C12	C17	C18	R46,R47	COU9,10,12,18	COU2,6,13,14,15,16
-A	LTM4650AEY	OPT	OPT	OPT	OPT	OPT	OPT	330uF (7343)	100uF (1210)
-B	LTM4650AEY-1	33pF	220pF	47pF	3.3nF	4.7nF	10K	OPT	330uF (1210)

CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS	
PCB DES.	LT
APP ENG.	ST

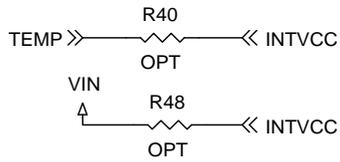
LINEAR TECHNOLOGY
 1630 McCarthy Blvd.
 Milpitas, CA 95035
 Phone: (408)432-1900 www.linear.com
 Fax: (408)434-0507
 LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC
HIGH EFFICIENCY, DUAL 25A OR SINGLE 50A, DC/DC μMODULE REGULATOR

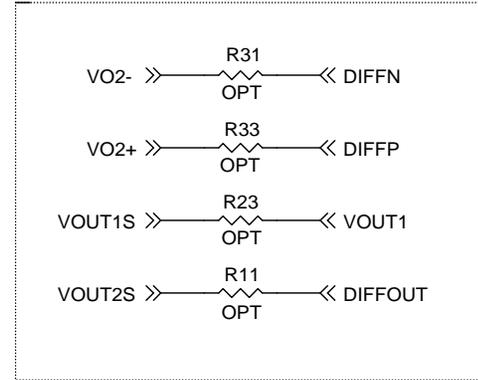
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

SCALE = NONE

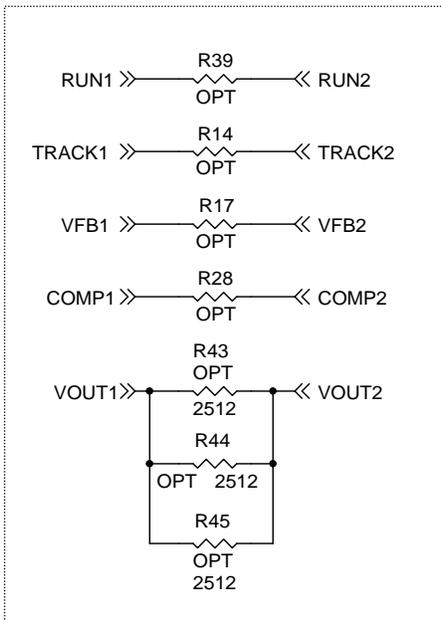
SIZE	IC NO.	REV.
N/A	LTM4650AEY / LTM4650AEY-1 DEMO CIRCUIT 2603A(A,B)	1
DATE:	Tuesday, March 28, 2017	SHEET 1 OF 2



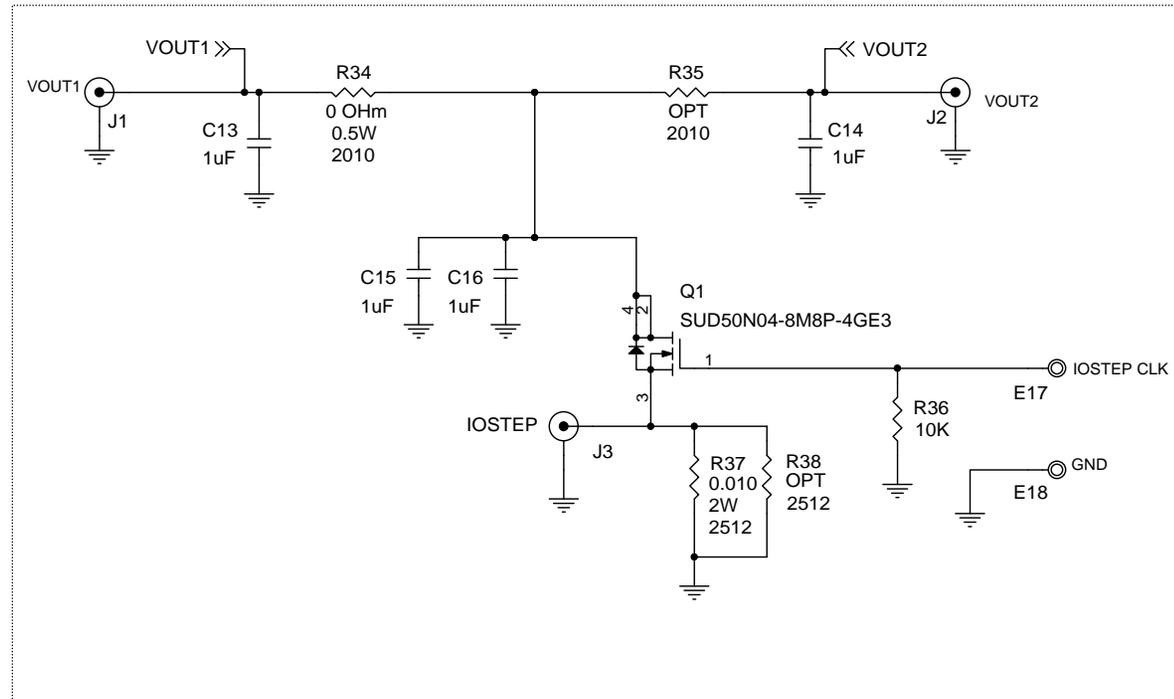
DIFF SENSING SELECTION



OPTIONAL JUMPER FOR SINGLE OUTPUT CONFIGURATION



Load Transient Circuit



CUSTOMER NOTICE

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THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	LT
APP ENG.	ST

SCALE = NONE



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TITLE: SCHEMATIC HIGH EFFICIENCY, DUAL 25A OR SINGLE 50A, DC/DC μ MODULE REGULATOR

SIZE	IC NO.	LTM4650AEY / LTM4650AEY-1	REV.
N/A		DEMO CIRCUIT 2603A(A,B)	1

DATE: Tuesday, March 28, 2017

SHEET 2 OF 2