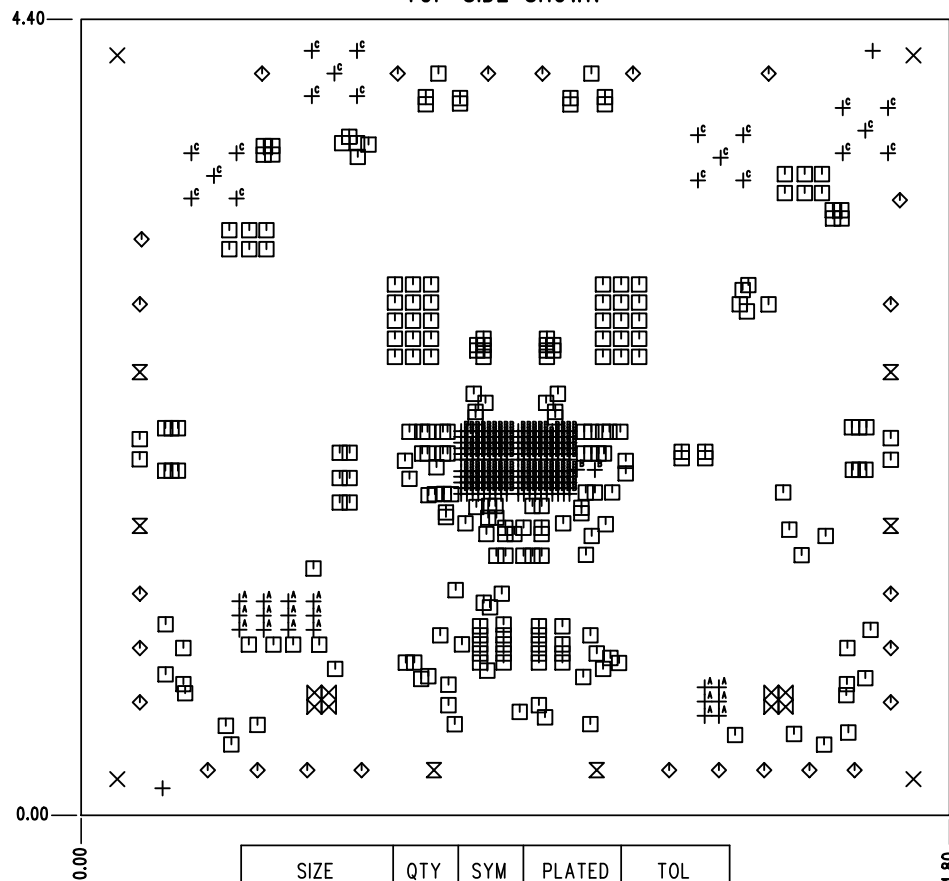


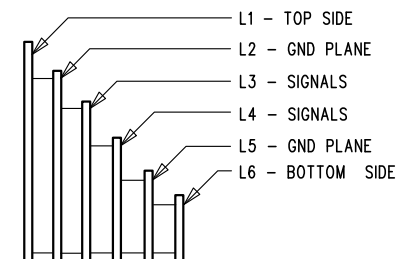
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	4	PRODUCTION	SAM Y.	06-06-18

TOP SIDE SHOWN



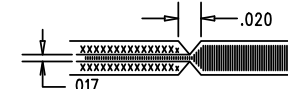
SIZE	QTY	SYM	PLATED	TOL
0.006	209	\pm^B	YES	+/-0.003
0.008	244	\square	YES	+/-0.003
0.036	18	\pm^A	YES	+/-0.003
0.04	8	\boxtimes	YES	+/-0.003
0.056	20	\pm^C	YES	+/-0.003
0.07	2	\pm	NO	+/-0.003
0.094	25	\diamond	YES	+/-0.003
0.188	4	\times	YES	+/-0.003
0.21	6	\boxtimes	YES	+/-0.003

LAYER STRUCTURE



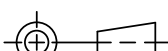


NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



- DESIGN HAS SOLDER MASK DEFINED PADS ON U1 WITH 0.019 PAD, 0.015 SOLDER MASK, AND 0.014 SOLDER PASTE.
DO NOT CHANGE ANY SIZE ON THIS COMPONENT.

UNLESS OTHERWISE SPECIFIED		APPROVALS		 ANALOG DEVICES  POWER BY LINEAR TM		FOR ADI CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION							
		PCB DES.	LT	TITLE: FABRICATION DRAWING QUAD OUTPUT STEP DOWN μMODULE REGULATOR-DUAL12A, DUAL5A			
		APP ENG.	SY				
				SIZE	IC NO.	LTM4671EY	REV
				N/A		DEMO CIRCUIT 2659A	4
SCALE = NONE		FILENAME: DC2659A-4.PCB		SHT 1 OF 1			