

LTM9013 -

DC1931B -

DPD Receiver Module
Demo Board

RF in

EN I/Q Enable
JP6

EN IP2 Adjust
JP7

EN Amp Run
JP5

SER PAR I/O
JP1

RUN ADC Sleep
JP3

3.5mA 1.75mA
JP4 LVDS Current

EN CLK Stabilizer
JP2

R15 IP2 Adjust (Q ch)
R12

R7 IP2 Adjust (I ch)
R6
Demod Ref

E6 C18
L0 in R34
E1 R9 R10
Amp Mute U1

R25 Gain Adjust (I ch)
R23
Vcc2

R31 Gain Adjust (Q ch)
R30
Vcc1
Vdd
R35
R36
T2
C25
R38
OVdd

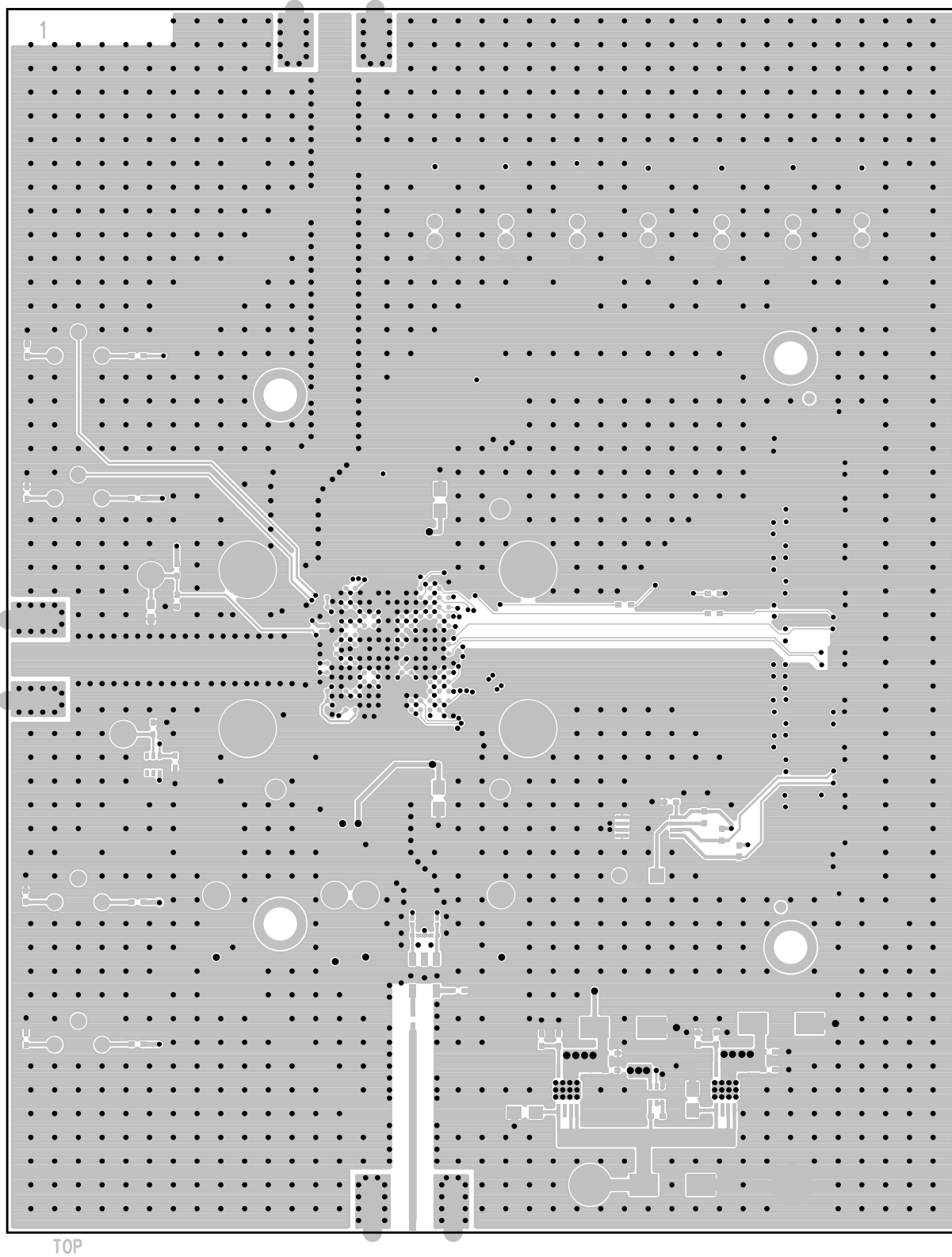
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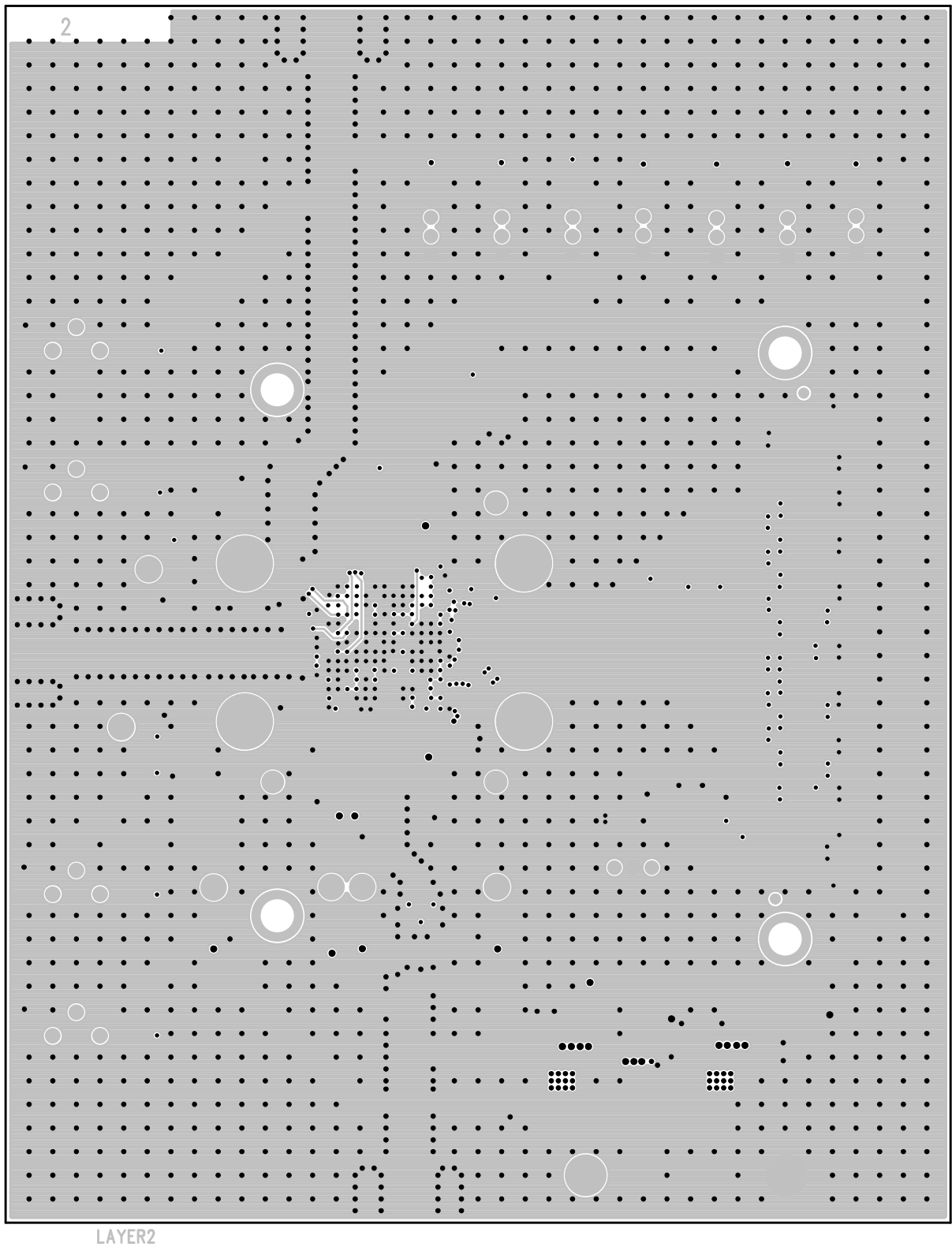
ENC in

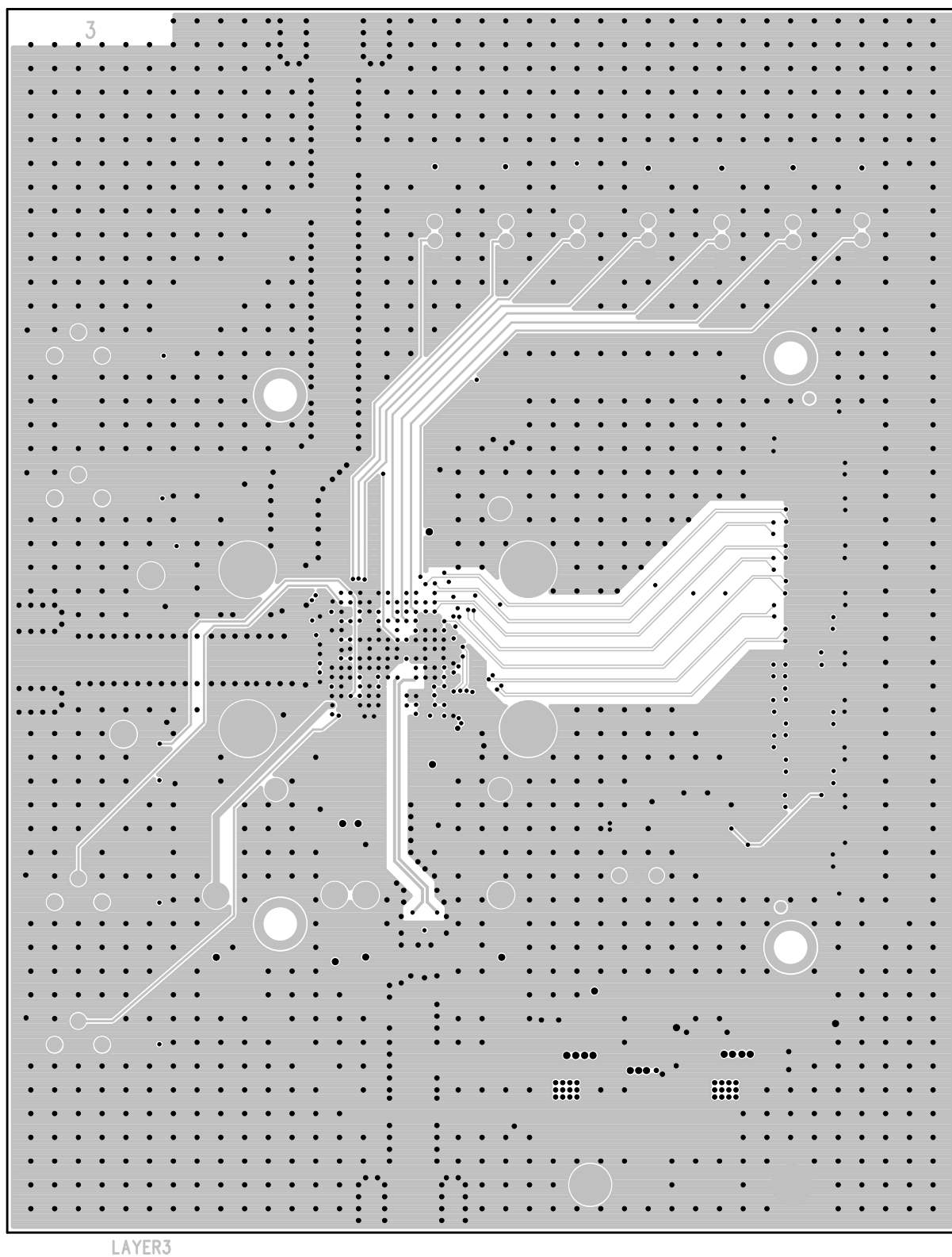
JP8 Write Enable
U5 C38
R44 R45 R46 R47

C31 R43 C37 C35 C36 R42 U4 U7 R47 C30 C29 E7 E8 GND
C32 C33 R40 C34 R41 C39

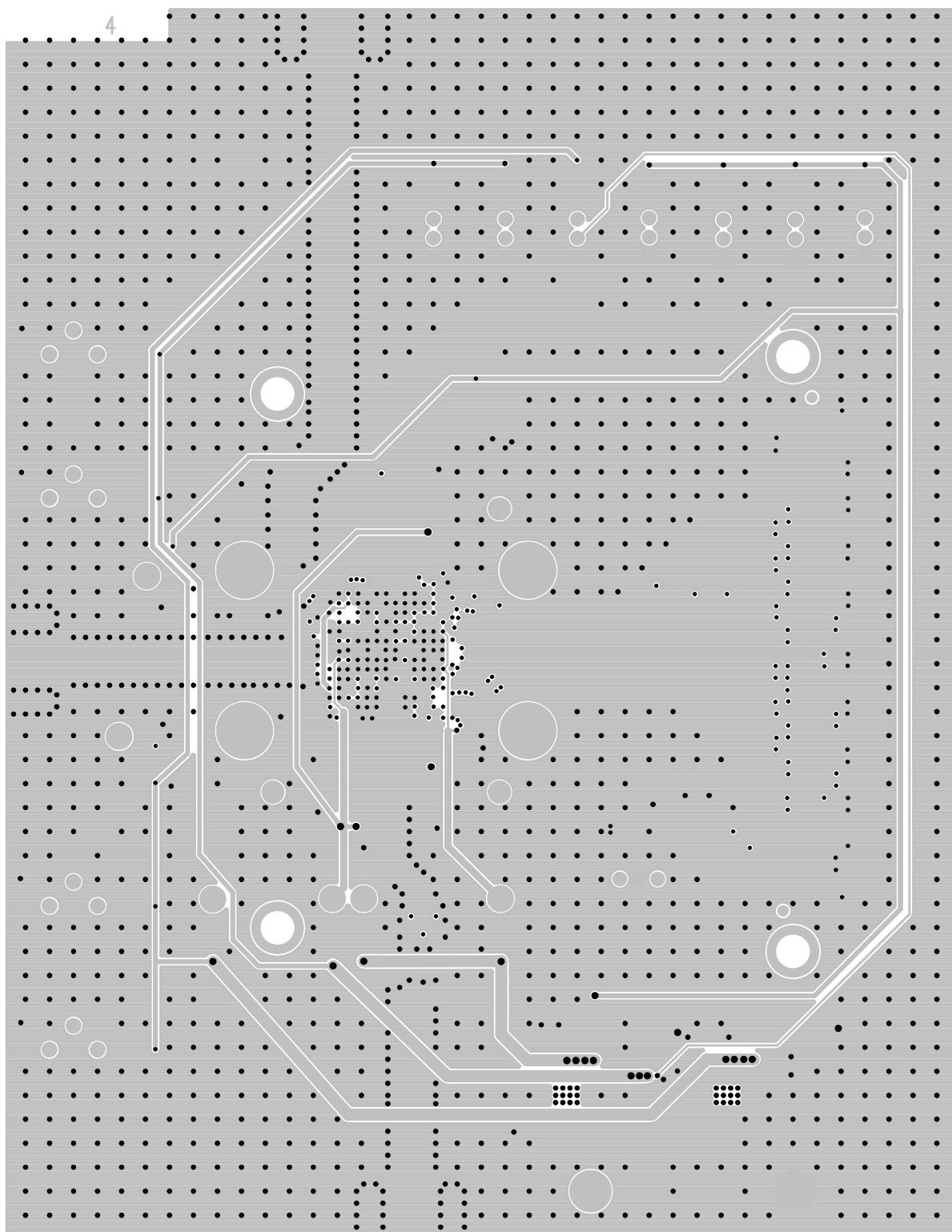
TOP SILK



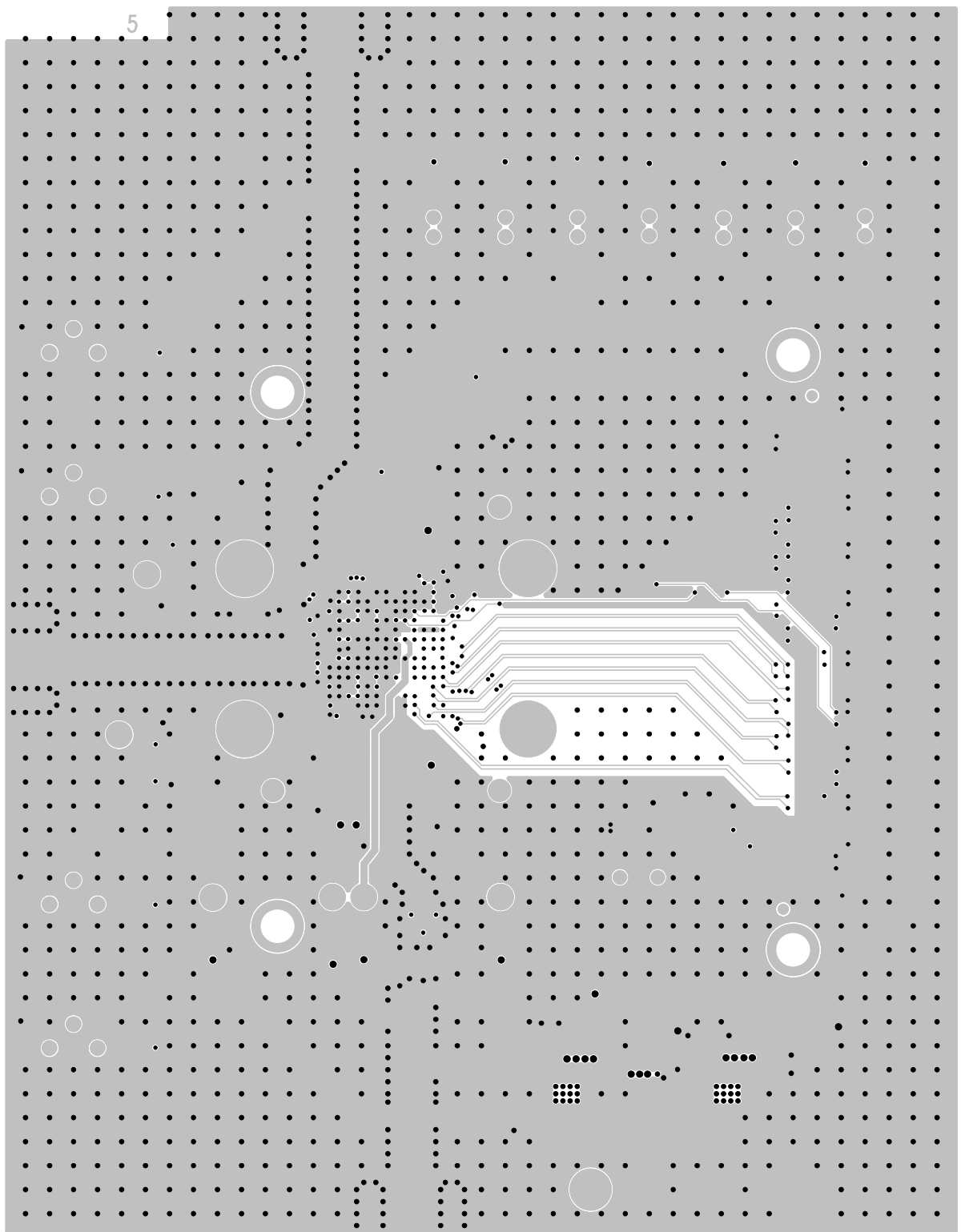




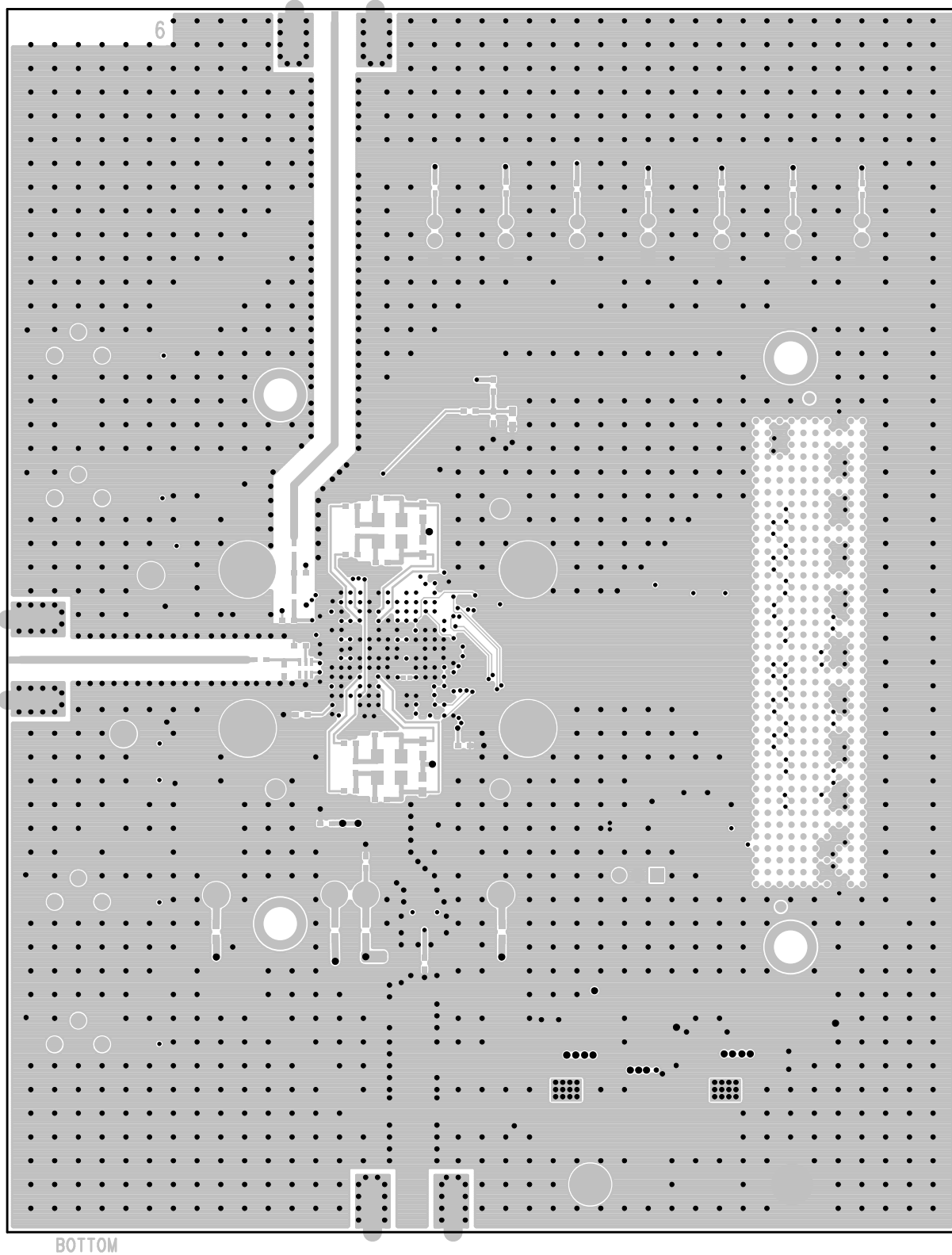
LAYER3

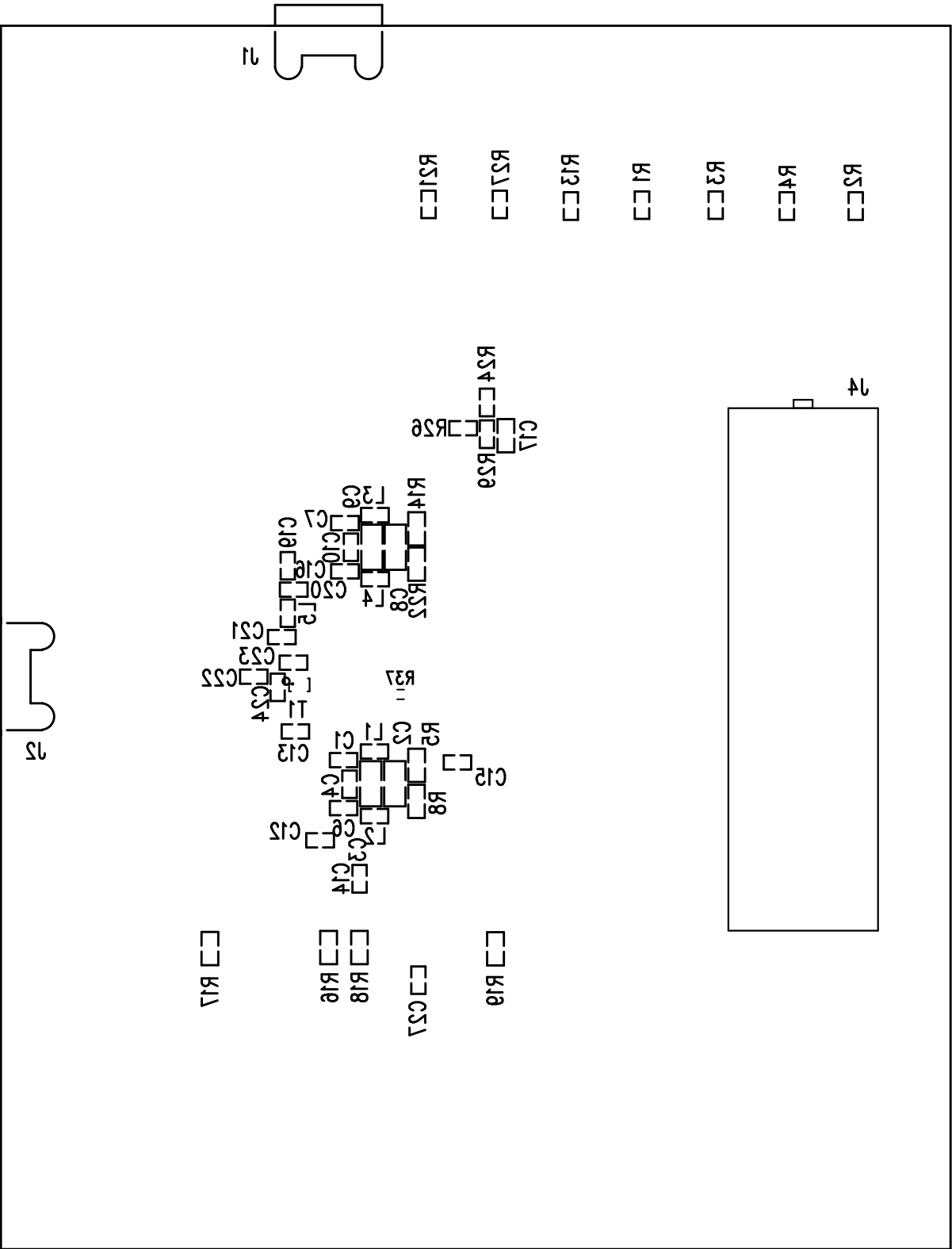


LAYER4

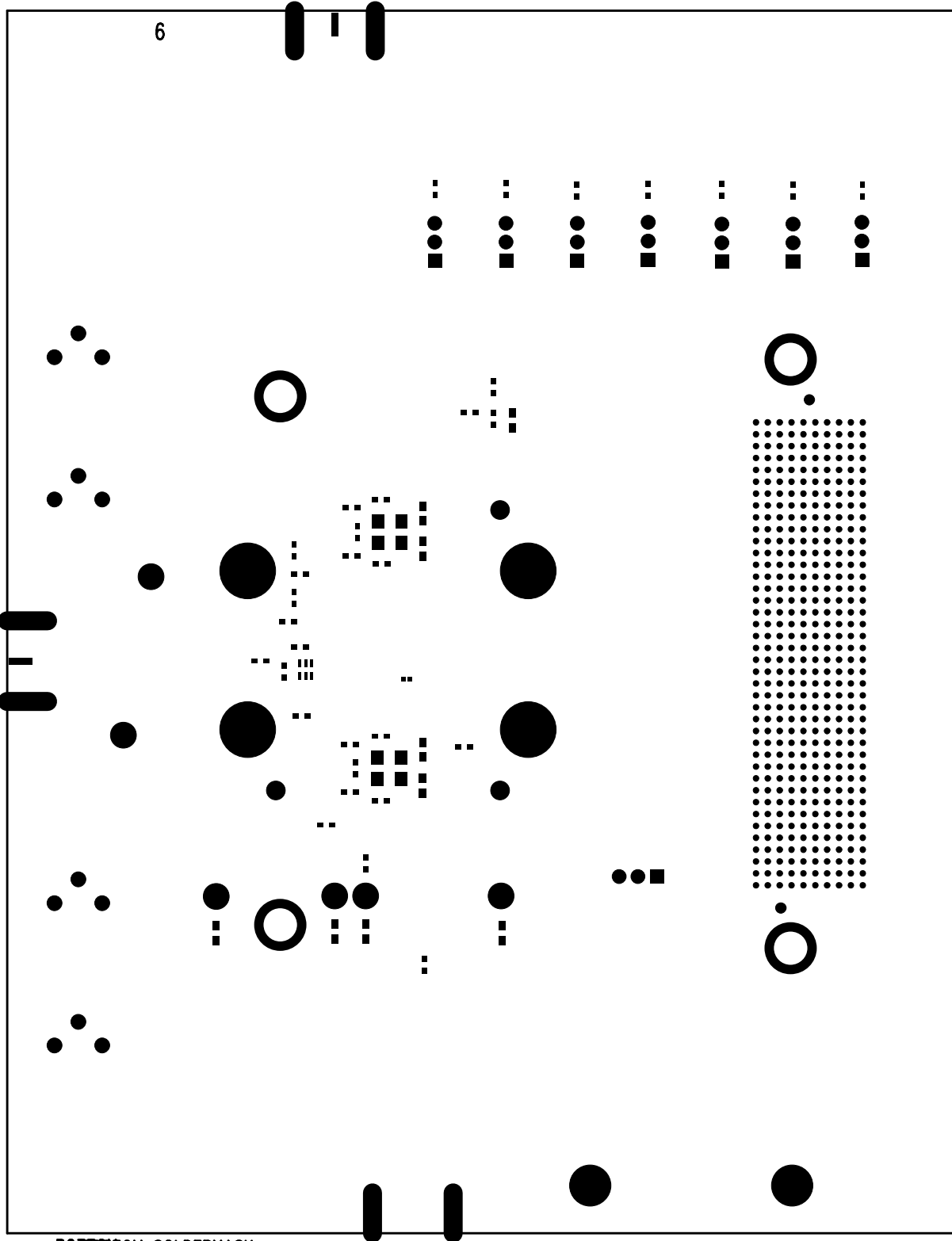


LAYER5

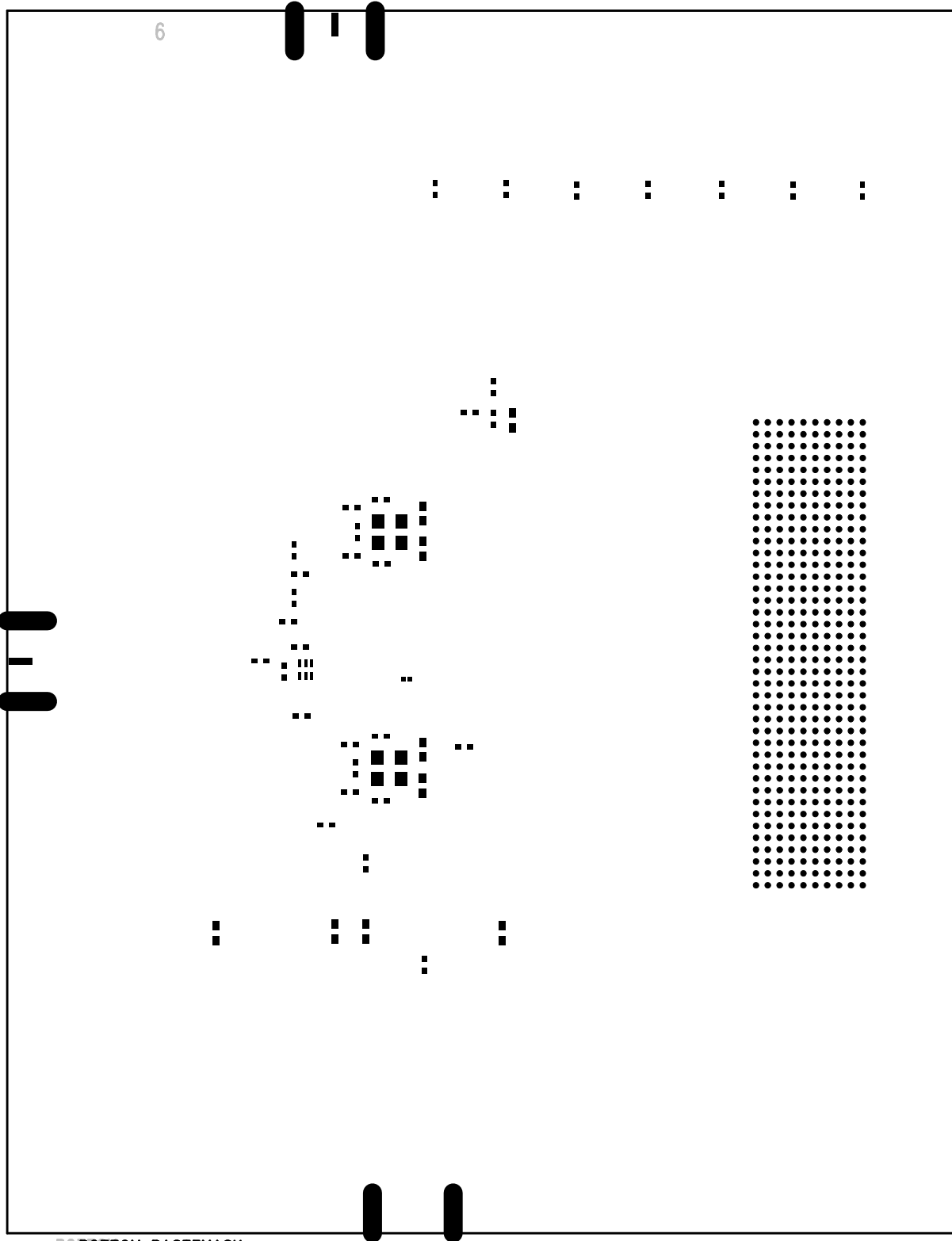




BOTTOM SILK



BOTTOM SOLDERMASK



6

BOTTOM PASTEMASK