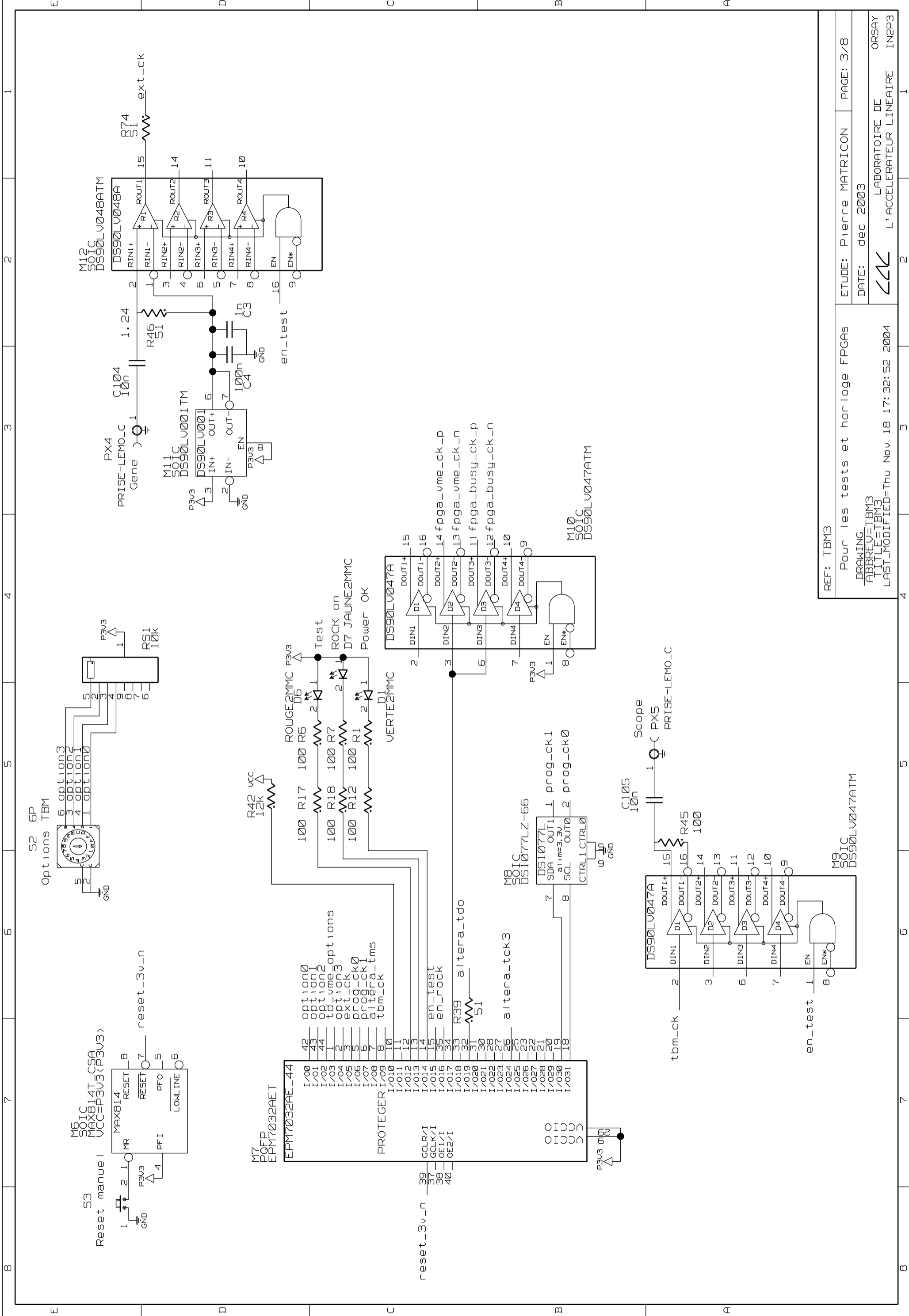
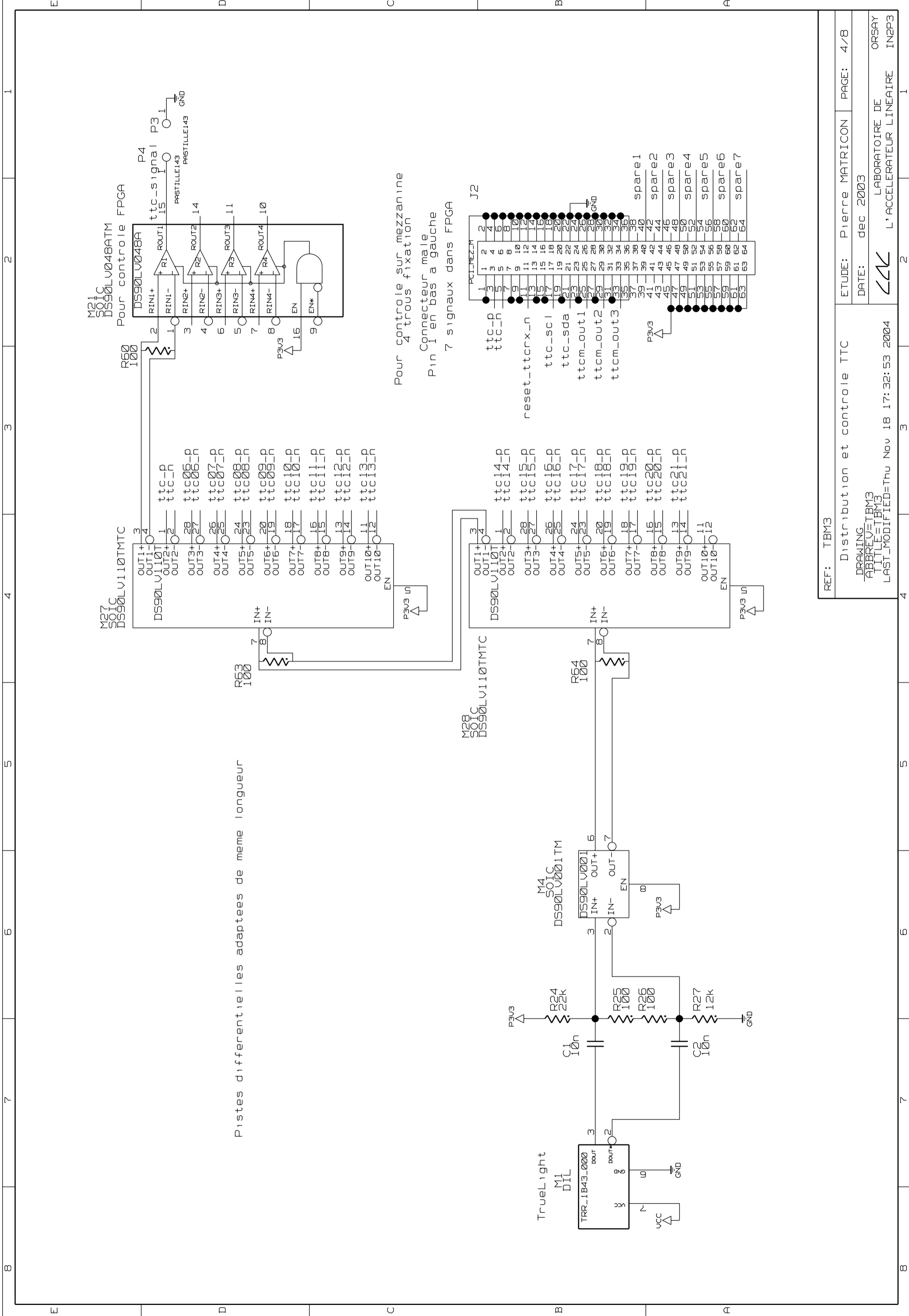


REF: TBM3	Decouplages, ESD et JTAG Commentaires	ETUDE: Pierre MATRICON	PAGE: 2/8
DRAWING ABBREU=TBM3 FILE=TBM3 LAST_MODIFIED=Thu Nov 18 17:32:51 2004		DATE: dec 2003	
		L'ACCELERATEUR LINEAIRE	ORSAY

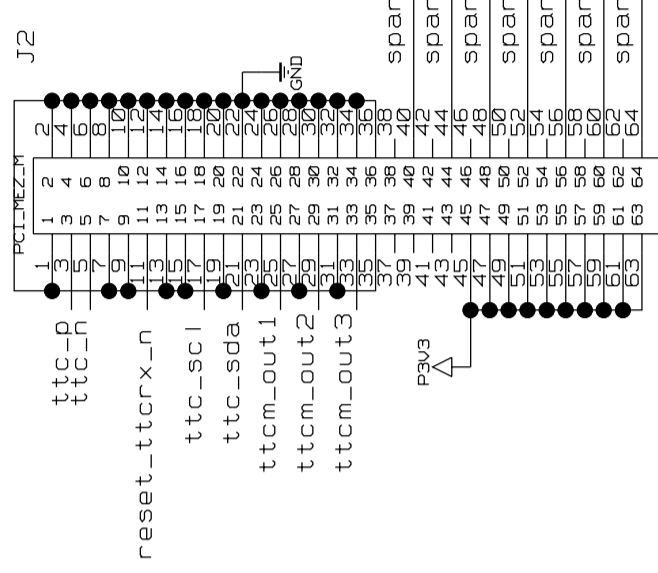


REF: TBM3	ETUDE: Pierre MATRICON	PAGE: 3/8
Pour les tests et horloge FPGAs		
DRAWING: ABBREV=TBM3		
TITLE=TBM3		
LAST_MODIFIED=Thu Nov 18 17:32:52 2004		
LABORATOIRE DE L'ACCELERATEUR LINEAIRE		ORSAY
DATE: dec 2003		IN2P3

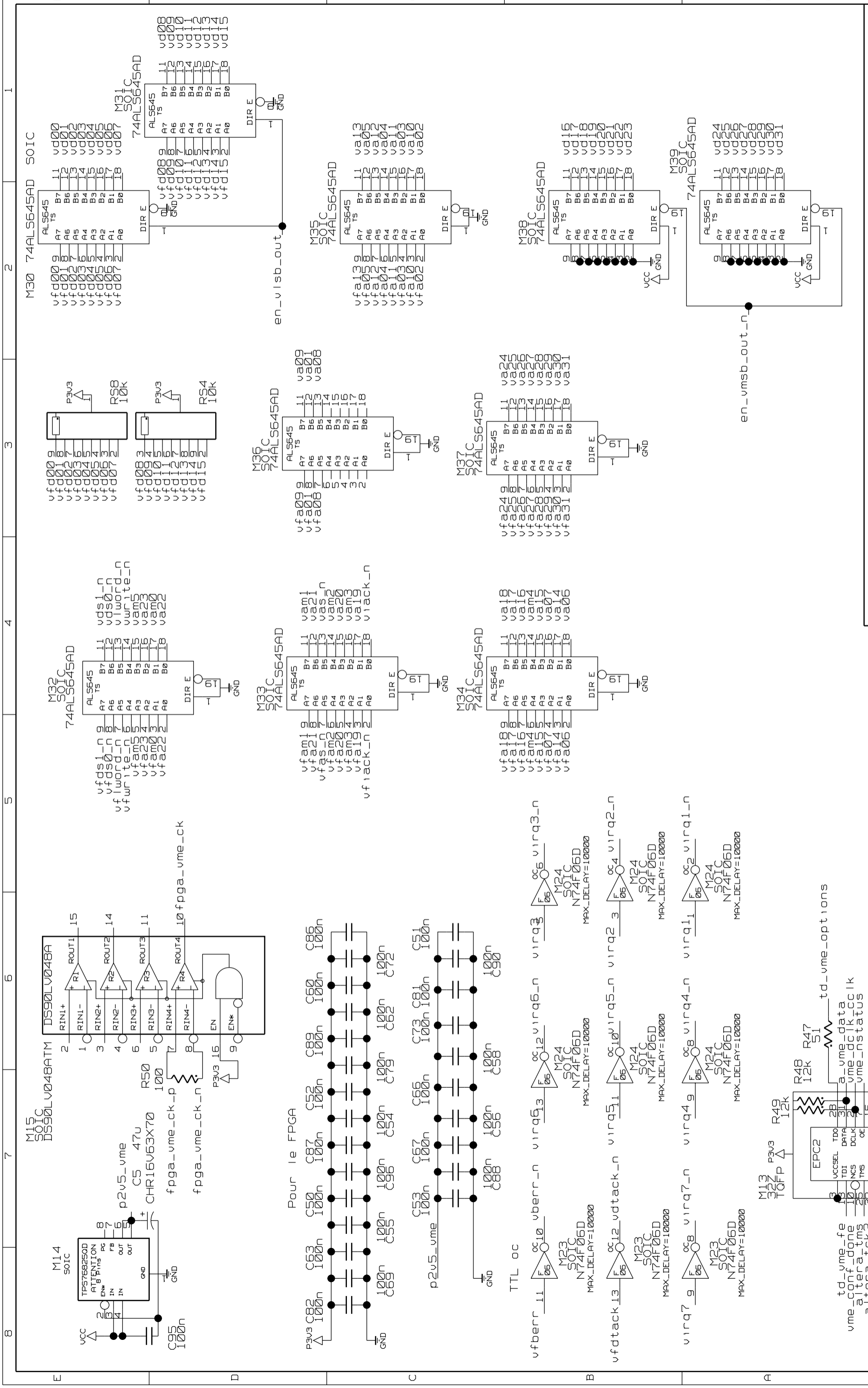


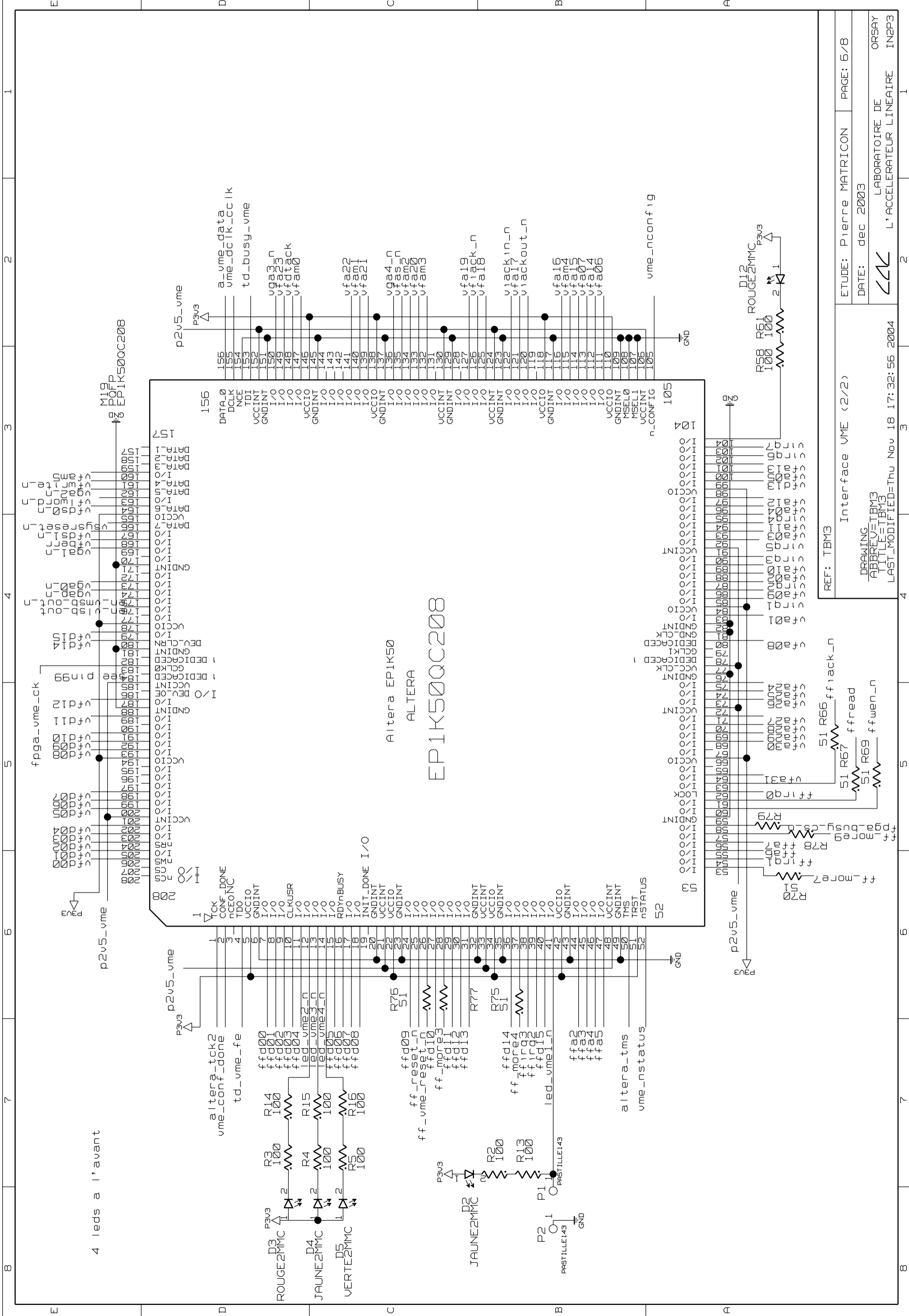
Pistes differentielles adaptees de meme longueur

Pour controle sur mezzanine
4 trous fixation
Connecteur male
Pin 1 en bas a gauche
7 signaux dans FPGA



REF: TBM3	Distribution et controle TTC	ETUDE: Pierre MATRICON	PAGE: 4/8
	DRAWING: ABBREVE TBM3	DATE: dec 2003	
	TITLE: TBM3		
	LAST_MODIFIED=Thu Nov 18 17:32:53 2004		
		LABORATOIRE DE L'ACCELERATEUR LINEAIRE	ORSAY IN2P3



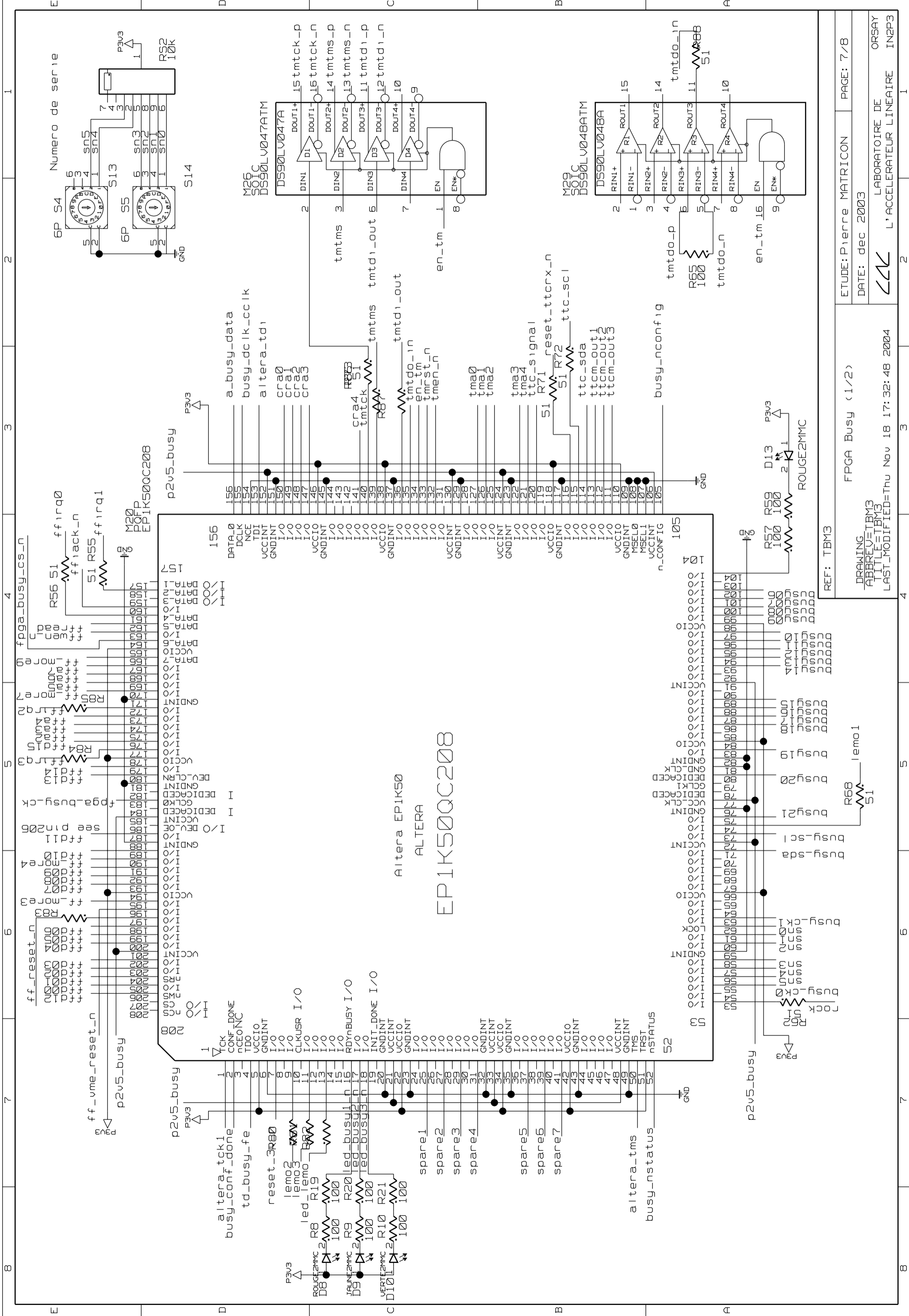


4 leds a l'avant

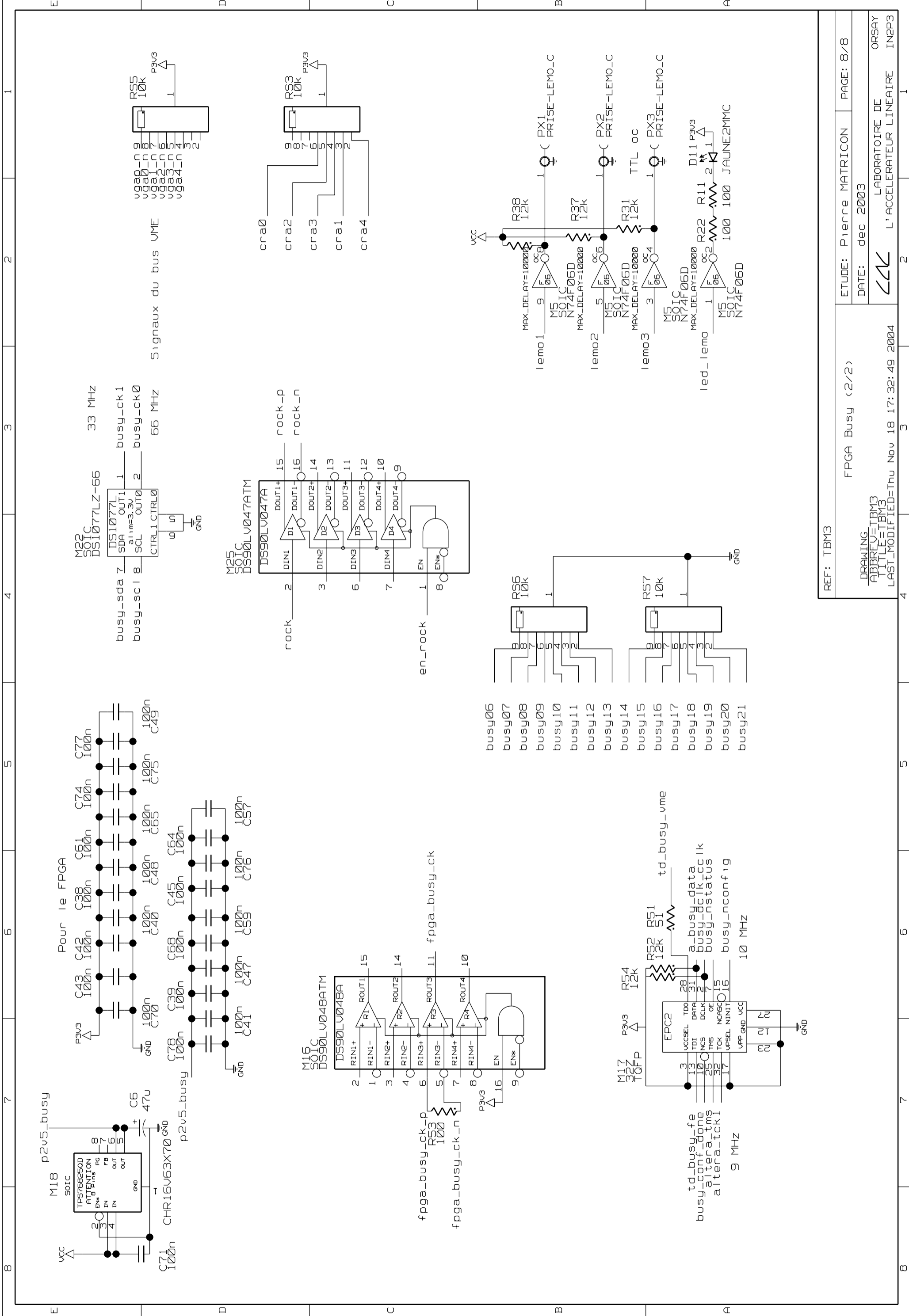
M19
POFIP
EPIK50QC208

ALTERA
EP1K50QC208

REF: TBM3	Interface VME (2/2)	ETUDE: Pierre MATRICON	PAGE: 6/8
DRAWING		DATE: dec 2003	
ABBREV=IBM3		LABORATOIRE DE	
TITLE=IBM3		L'ACCELERATEUR LINEAIRE	
LAST_MODIFIED=Thu Nov 18 17:32:56 2004		ORSAY	
		IN2P3	



REF: TBM3	FPGA Busy (1/2)	ETUDE: Pierre MATRICON	PAGE: 7/8
DRAWING: ABBREV=TBM3		DATE: dec 2003	
TITLE=TBM3			
LAST_MODIFIED=Thu Nov 18 17:32:48 2004			
		LABORATOIRE DE L'ACCELERATEUR LINEAIRE	ORSAY
			IN2P3



REF: TBM3	FPGA Busy (2/2)	ETUDE: Pierre MATRICON	PAGE: 8/8
DRAWING: ABBREU=TBM3		DATE: dec 2003	
TITLE=TBM3			
LAST_MODIFIED=Thu Nov 18 17:32:49 2004			
		LABORATOIRE DE	ORSAY
		L'ACCELERATEUR LINEAIRE	IN2P3