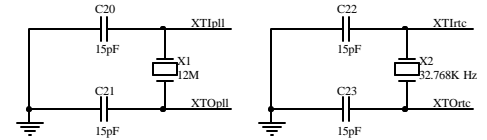
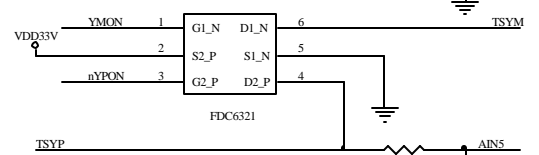
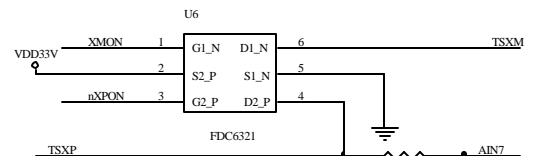
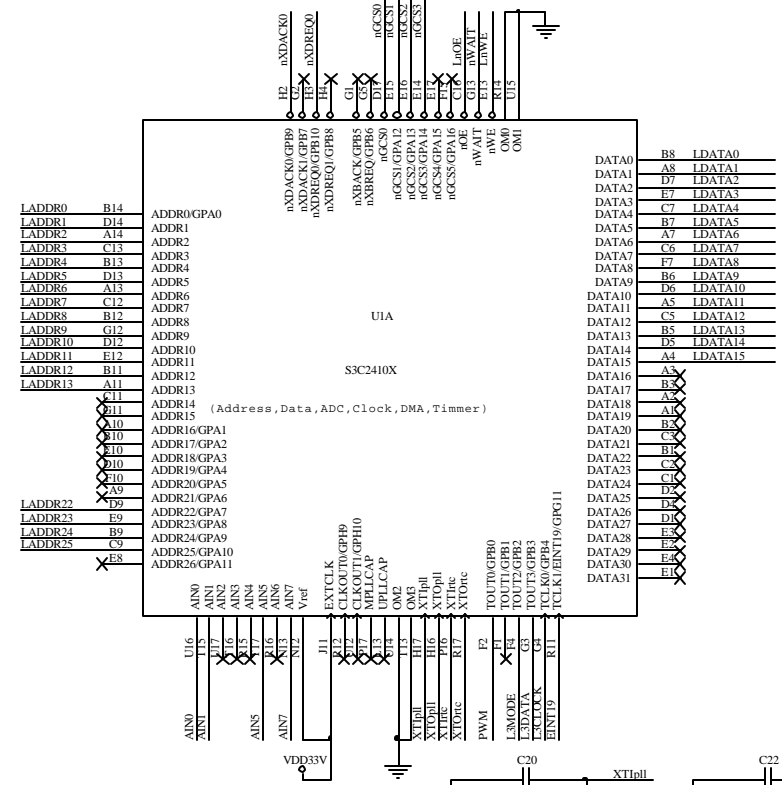
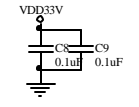
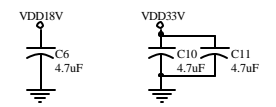
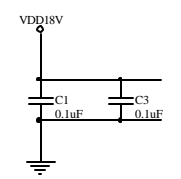


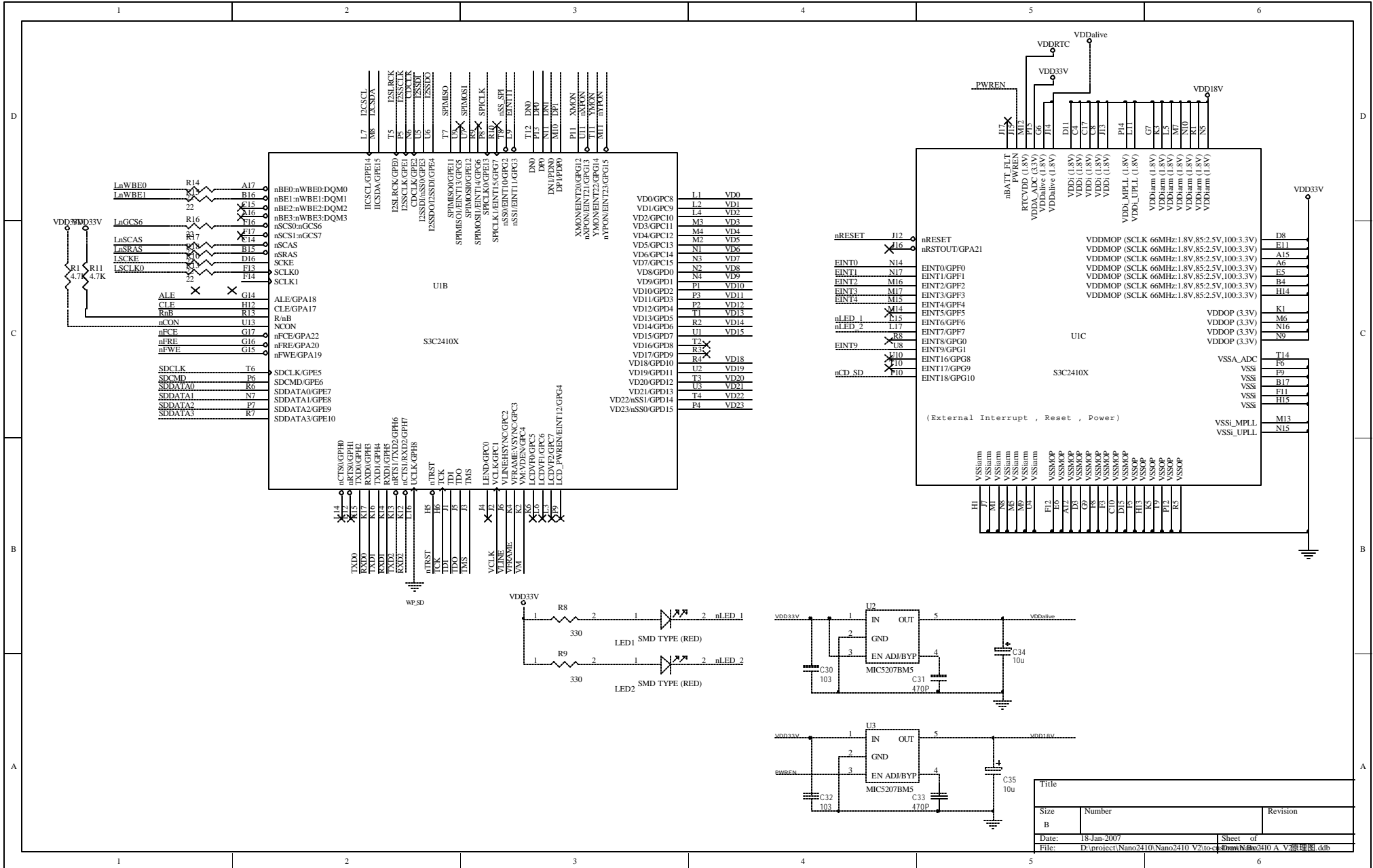
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Date: 18-Jan-2007	Sheet of	
File: D:\project\Nano2410\Nano2410 V2\toch\0mm\8.48x2410 A V2原理圖.ddb		

00(1-0) : Bus Size  
 00 : Word mode  
 01 : Halfword (16-bit)  
 10 : Word (32-bit)  
 11 : Byte mode

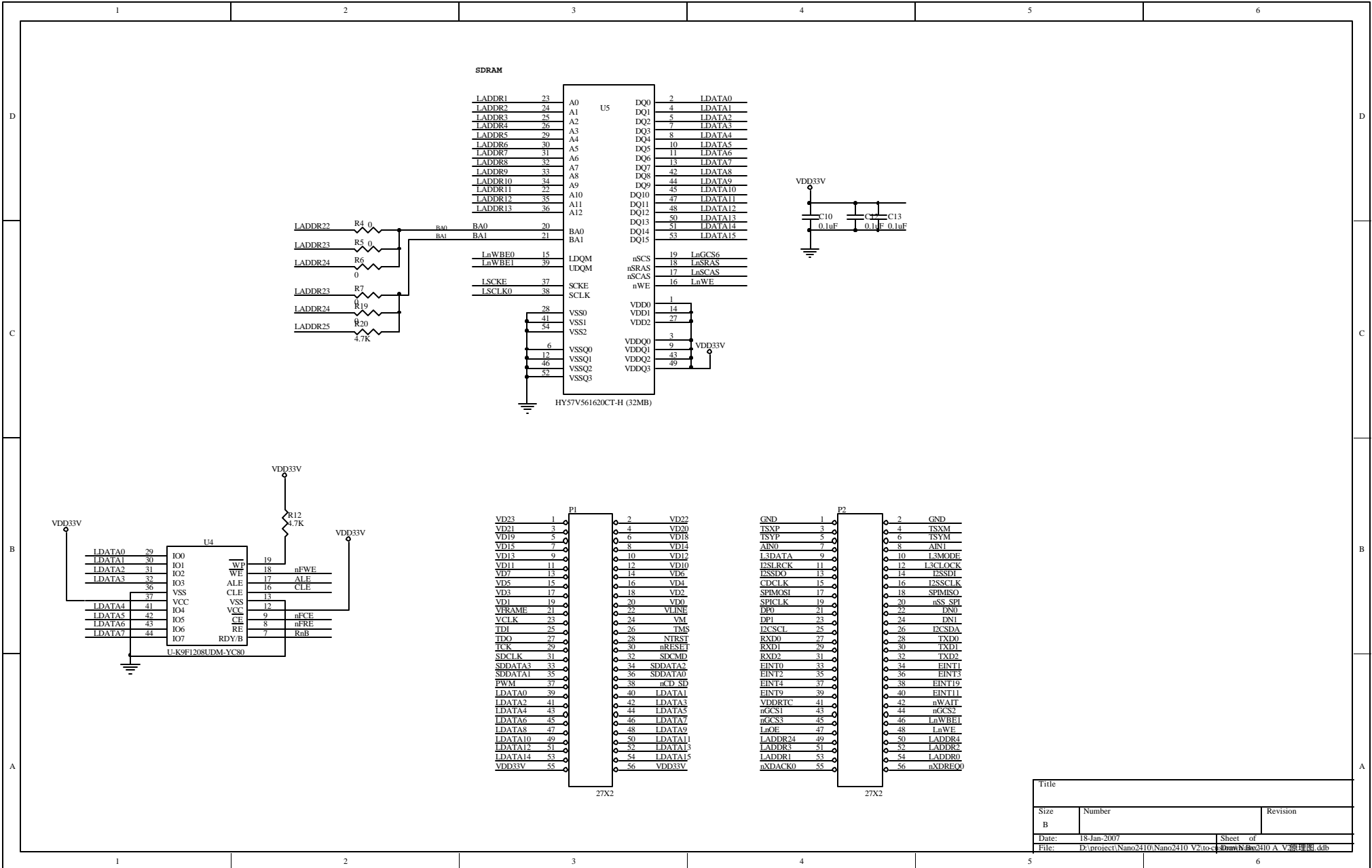
NOTE!!!  
 Lxxxx signals should be routed as short as possible.



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