Errata

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HP References in this Manual

This manual may contain references to HP or Hewlett-Packard. Please note that Hewlett-Packard's former test and measurement, semiconductor products and chemical analysis businesses are now part of Agilent Technologies. We have made no changes to this manual copy. The HP XXXX referred to in this document is now the Agilent XXXX. For example, model number HP8648A is now model number Agilent 8648A.

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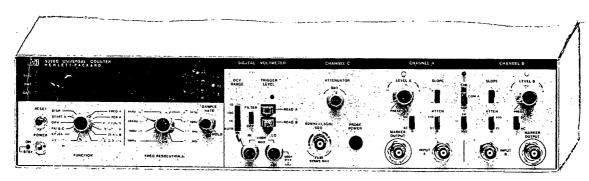
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5328B Universal Counter



OPTION 050 SHOWN



OPERATING AND SERVICE MANUAL

Model HP 5328B Universal Counter

SERIAL NUMBERS

This manual applies directly to Hewlett-Packard Model 5328B Universal Counters with serial numbers prefixed 2510A.

The manual for a Model 5328B with a serial number prefix higher than the one listed above will include a "Manual Changes" supplement which will describe what changes, if any, need to be made to the manual to make it match the instrument it accompanies.

OPTIONS

This manual provides operating and/or performance testing information for the Model 5328B Options listed below.

Option 010 - High Stability Oven Oscillator

Option 021 - Digital Voltmeter

Option 031 - Channel "C"

Option 050 - Combination of Options 021 and 031

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MANUAL PART NUMBER 05328-901 Microfiche Part Number 05328-90112

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SAFETY CONSIDERATIONS

GENERAL

This product and related documentation must be reviewed for familiarization with safety markings and instructions before operation.

This product is a Safety Class I instrument (provided with a protective earth terminal).

BEFORE APPLYING POWER

Verify that the product is set to match the available line voltage and the correct fuse is installed. Refer to Section II, Installation.

SAFETY EARTH GROUND

An uninterruptible safety earth ground must be provided from the mains power source to the product input wiring terminals or supplied power cable.

SAFETY SYMBOLS

 \triangle

Instruction manual symbol; the product will be marked with this symbol when it is necessary for the user to refer to the instruction manual.

4

Indicates hazardous voltages.



Indicates terminal is connected to chassis when such connection is not apparent.



Alternating current.

Direct current.

WARNING

The **WARNING** sign denotes a hazard. It calls attention to a procedure, practice, or the like.

which, if not correctly performed or adhered to, could result in personal injury. Do not proceed beyond a **WARNING** sign until the indicated conditions are fully understood and met.

CAUTION

The **CAUTION** sign denotes a hazard. It calls attention to an operating procedure, practice, or

the like, which, if not correctly performed or adhered to, could result in damage to or destruction of part or all of the product. Do not proceed beyond a **CAUTION** sign until the indicated conditions are fully understood and met.

SAFETY INFORMATION

WARNING

Any interruption of the protective grounding conductor (inside or outside the instrument) or disconnecting the protective earth terminal will cause a potential shock hazard that could result in personal injury. (Grounding one conductor of a two conductor outlet is not sufficient protection.)

Whenever it is likely that the protection has been impaired, the instrument must be made inoperative and be secured against any unintended operation.

If this instrument is to be energized via an autotransformer (for voltage reduction) make sure the common terminal is connected to the earthed pole terminal (neutral) of the power source.

Instructions for adjustments while covers are removed and for servicing are for use by service-trained personnel only. To avoid dangerous electric shock, do not perform such adjustments or servicing unless qualified to do so.

For continued protection against fire, replace the line fuse(s) only with 250V fuse(s) of the same current rating and type (for example, normal blow, time delay). Do not use repaired fuses or short circuited fuseholders.

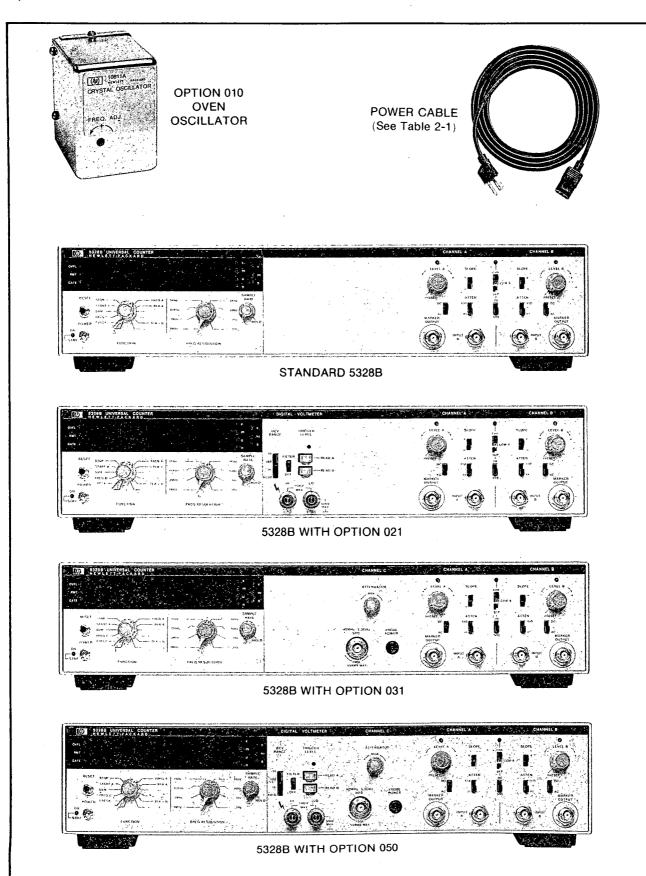


Figure 1-1. HP 5328B Universal Counter

SECTION I GENERAL INFORMATION

1-1. INTRODUCTION

This manual provides operating information for the standard Hewlett-Packard Model 5328B Universal Counter. It also provides operating instructions for Model 5328Bs having the following options —

- Option 010 High-stability Time Base Oscillator (Actually, no special instructions are required for this Option.)
- Option 021 Digital Voltmeter
- Option 031 Channel "C" (90-1300 MHz frequency range)
- Option 050 Combination of Option 021 and Option 031

1-2. DESCRIPTION

The Model 5328B in its standard configuration (i.e., without options), measures —

- frequency
- · period, period average
- · time interval, time interval average
- frequency ratio
- totalization

The Model 5328B's Specifications are given in Table 1-1.

The Model 5328B's capabilities can be expanded by adding optional circuits, as described below.

OPTION 010 - High-stability Time Base Oscillator.

OPTION 021 - Digital Voltmeter

OPTION 031 - Channel "C"

OPTION 050 - Provides OPTION 021 (Digital Voltmeter) and OPTION 031 (Channel "C")

The Model 5328B's measurement results are presented via a front-panel display that consists of nine seven-segment LED numeric positions,* plus additional indicators for measurement units and decimal point position. Additionally, an electrical representation of the output is available at the Model 5328B's rear-panel HP-IB connector.

The standard Model 5328B has two independently-controllable input channels ("A" and "B") that can be used for frequency or time-interval measurements. Each of these channels has its own attenuator, trigger slope selector, level control, ac/dc coupling selector, and oscilloscope marker output; however a front-panel CHK/COM A/SEP switch allows Channel "B" to be controlled by Channel "A"'s controls, which makes set-ups easier for some applications.

^{*}NOTE: Depending on the measurement being made, and the setting of the Model 5328B's controls, the numeric portion of the display may consist of fewer than nine digits.

The Model 5328B's rear-panel connectors include a gate output, a time base output, and an input for an external frequency standard.

A rear-panel ARM switch allows the Model 5328B to be "armed" by the signal being measured (ARM switch OFF) or by another input signal (ARM switch ON).

The Model 5328B's rear panel includes some additional holes, normally filled by plug buttons, to allow for easy installation (at the factory) of such options as rear-panel input, etc.

NOTE

The Model 5328B is not set up for field-installation (i.e., addition) of the Options described in this manual.

1-3. INSTRUMENT IDENTIFICATION

Each HP 5328B is identified by a Serial Number as described in paragraph 1-4 below. The contents of this manual apply to a Model 5328B whose Serial Number Prefix is listed under "SERIAL NUMBERS" on the title page.

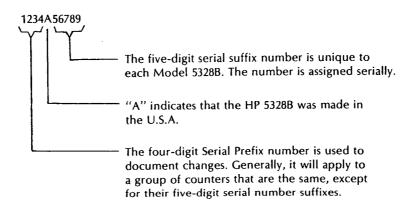
A HP 5328B manufactured after this manual was printed may have a Serial Number Prefix that is not listed on the title page. This unlisted Serial Number Prefix indicates that the Model 5328B in question may be different from those described in this manual. The manual for this new Model 5328B is accompanied by a "Manual Changes" supplement containing "change" information that explains how to adapt the manual to the newer Model 5328B.

In addition to "change" information, the supplement may contain information for correcting errors in the manual. To keep this manual as current and accurate as possible, Hewlett-Packard recommends that you periodically request the latest "Manual Changes" supplement. The supplement for this manual is identified with the manual's Part Number and Print Date, both of which appear on the manual's title page. Complimentary copies of the supplement are available from Hewlett-Packard.

For information concerning a Serial Number Prefix that is not listed on the title page or in the "Manual Changes" supplement, contact your nearest Hewlett-Packard office.

1-4. Serial Number

Each Model 5328B is identified by a unique serial number having the format -



The HP 5328B's Serial Number is printed on a label attached to its rear panel.

1-5. Series Number

Some parts in the HP 5328B may have a "SERIES" number on them.

The SERIES number is similar in function to the prefix portion of the Model 5328B's Serial Number. Generally, all parts having the same HP Part Number are the same; parts having the same Part Number, but different SERIES numbers are similar, but not identical.

1-6. APPLICATIONS

Specific applications information for the Model 5328B is provided in Section 3 of this manual. The general applications of the Model 5328B are described below. The standard Model 5328B can directly count (or measure the period of) signals in the range from dc to 100 MHz. Its high sensitivity, and signal conditioning circuits (which include attenuators, and trigger selection controls) allow measurements to be made on almost any signal in its frequency range. The Model 5328B's "arming" input for a measurement can be selected by a rear-panel switch; this selectable "arming" capability is useful in applications such as measurements of burst frequency or pulse amplitude.

The standard Model 5328B's single-shot resolution of 10 nS allows the counter to be used in such applications as —

- timing mechanical and/or electromechanical devices (such as relays)
- time-of-flight (ballistics) measurements
- · sonar ranging
- radio ranging
- navigation

The Model 5328B's standard "A"-and-"B"-Channel Input Module provides resolution to 10 nS and HP-IB (Hewlett-Packard Interface Bus) programming capability; this is useful in such measurement applications as —

- computer/peripheral timing
- logic timing
- radar ranging
- · optical ranging.

Using time interval averaging, the Model 5328B can measure time intervals as short 100 pS with a resolution to 10 pS. Uses for this capability include measurements of —

- cable length
- phase
- logic timing
- integrated circuit propagation delay

The optional integrating Digital Voltmeter (Option 021 or part of Option 050) measures trigger levels (this is critical in most time-interval measurements) and external voltages.

Model 5328B General Information

The optional 90-1300 MHz "C"-Channel (Option 031 or part of Option 050) allows a higher range of frequencies to be counted by the Model 5328B. Signals applied to the "C"-Channel input are pre-scaled (i.e. divided) by "4".

Full bandwidth, sensitivity, and signal conditioning of the "A" and "B" and (optional) "C" inputs are provided for ratio, totalizing, or scaling measurements.

The Model 5328B's HP-IB interface (standard) can output measurement data and can be controlled (i.e, be fully programmed) via the Hewlett-Packard Interface Bus (HP-IB). The Model 5328B is interfaced to HP-IB-compatible instruments, calculators, or computers simply by connecting it with an HP-IB cable. The HP-IB capability allows complete "hands-off" operation for the most-involved systems applications.

1-7. SPECIFICATIONS

Specifications for the Model 5328B, and the Options listed below, are listed in Table 1-1.

Option 010 Option 021 Option 031 Option 050

INPUT CHARACTERISTICS

(Channel A and B)

Range:

DC Coupled: 0 to 100 MHz AC Coupled: 20 Hz to 100 MHz AC Coupled (50Ω): 200 kHz to 100 MHz

Common A: 0 to 10 MHz

Sensitivity (x 1):

0 to 40 MHz Sine wave: 25 mV rms 40 MHz to 100 MHz Sine wave: 50 mV rms 5 ns Pulse: 140 mV p-p

Dynamic Range (x 1):

0 to 40 MHz: 71 mV to 3 V p-p

40 MHz to 100 MHz: 141 mV to 1.5 V p-p

Signal Operating Range (x 1, DC): ± 2.5 V DC

Trigger Level Range (x 1):
Preset: 0V NOMINAL
Adjustable: ± 2.5 V

Useable Range: 20% to 80% of Signal Amplitude

Coupling: AC or DC, Switchable

Impedance:

Separate: 1 M Ω NOMINAL shunted with <100 pF, or 50 Ω

NOMINAL, switchable

Common: 500 k Ω NOMINAL, shunted with <150 pF, or 50 Ω

NOMINAL, switchable

Attenuators (NOMINAL): × 1, × 10, Switchable Slope: Independent + or – Slope, Switchable Channel Input: Separate, Common A or Check

Damage Level:

1 M Ω , x 1, DC Coupled:

DC to 50 kHz250 V rms

50 kHz to 2.5 MHz1.25 × 107 V rms/Freq

2.5 MHz to 100 MHz5 V rms

1 M Ω , x 10, DC Coupled:

DC to 500 kHz250 V rms

0.5 to 25 MHz1.25 × 108 V rms/Freq

25 to 100 MHz 5 V rms

AC Coupled:

DC to 20 Hz200 V (Peak AC + DC)

20 Hz to 100 MHzSame as DC coupled.

50 Ω **Position**:

DC to 100 MHz 5 V rms

CROSS TALK

Measurements made above 20 MHz with input signals applied to both channels may be affected by cross talk. During dual channel measurements, input signals with similar amplitudes greater than 75mV (nominal) are recommended for both channels.

FREQUENCY A MEASUREMENT

Range: 0 to 100 MHz, Direct Count

LSD** Displayed: 1 MHz to 0.1 Hz in decade steps

Resolution: Same as LSD**

Accuracy: ± Resolution, ± Time Base Error × Freq

PERIOD A MEASUREMENT

Range: 100 ns to 1 × 107 s

LSD** Displayed: 10 ns to 0.1 s in decade steps

X Resolution: ± LSD** ± Trigger Error**

Accuracy: ± Resolution, ± Time Base Error × Period

PERIOD AVERAGE A MEASUREMENT

Range: 1100 ns to 1 × 107 s

LSD** Displayed:

10 ns

Number of Periods Averaged

Resolution:

Trigger Error**

 \pm LSD \pm Number of Periods Averaged

Accuracy: ± Resolution, ± Time Base Error × Period

TIME INTERVAL A'TO B MEASUREMENTS

Range: 10 ns to 1 × 107 s

Minimum Pulse Width: 10 ns

LSD** Displayed: 10 ns to 0.1 s in decade steps

Resolution: ± LSD** ± Trigger Error** ± Trigger Level Timing

Error**

Accuracy: \pm Resolution, \pm Time Base Error \times T.I.

TIME INTERVAL AVERAGE A TO B MEASUREMENTS

No more than one STOP pulse allowed for each START pulse, otherwise measurement scaling will occur.

Range: 0.1 ns to 1 s LSD** Displayed:

10 ns

√ Number of Intervals Averaged

Resolution:

Trigger Error**

± LSD** ±√ Number of Intervals Averaged ±Trigger Level Error**

±10 ps

Accuracy: \pm Resolution, \pm 2 ns \pm Time Base Error \times T.I.

Minimum Pulse Width: 10 ns

Minimum Dead Time: 40 ns (10 MHz maximum repetition rate)

RATIO MEASUREMENTS

Note — The ratio of the frequency at B (or C for C/A Function when Option 031 is installed) to the frequency at A is measured for N counts of A, where N is selected by the resolution switch (N = 1 to 1×10^7 in decade steps).

Range: A: 0 to 10 MHz, B: 0 to 100 MHz

C: 90 MHz to 1300 MHz (Option 031)

LSD** Displayed:

1 part in
$$\frac{B}{A} \times N$$
 (or $\frac{C}{A} \times N$)

Resolution: ± LSD

Accuracy: ± Resolution, ± Trigger Error** of A × Frequency of

B (or C) (N > 1)

(For N = 1, add \pm 12 ns \times Frequency of B (or C)

TOTALIZING AND SCALING MEASUREMENTS

Start A: Totalizes A/N, where A is the number of events on input A and N is the scaling factor as selected on the resolution switch $(N = 1 \text{ to } 1 \times 10^7 \text{ in decade steps})$. The scaled output (A/N) is available at the rear panel time base output. For N > 1.

Range:

N = 1

0 to 100 MHz 0 to 10 MHz

- * Specifications describe the instrument's warranted performance. Supplemental characteristics are intended to provide information useful in applying the instrument by giving TYPICAL or NOMINAL but nonwarranted performance para-
- ** See notes/definitions section for further information.

GENERAL

meters.

Display: 8 digit LED display (Standard) 9 digit LED display (Option 031)

Blanking: Suppresses leading zeros.

Storage: Holds reading between measurements but can be

overriden by rear panel switch.

Sample Rate: Variable from less than 2 ms between measurements to infinity (hold).

Gate Time: For frequency measurements, gate time is the reciprocal of the selected resolution (for Option 031, frequency C, the gate time is four times the reciprocal of resolution).

Gate Time Range: 1 μ s to 10 s (4 μ s to 40 s for Option 031, frequency C). Minimum signal burst measureable is 8 μ s.

Gate Output: Rear Panel output, TTL levels. High when counter gate is open.

Check Signal: Place function switch in Freq. A and input section in check (CHK) — counter displays 100 MHz ± 1 count. Time Base Output: Rear panel output, TTL levels.

Time Base (Standard):

Frequency: 10 MHz

Aging Rate: <3 × 10-7/month

Temperature: $<2.5 \times 10^{-6}$, 0 to 50°C, ref. to 25°C.

Line Voltage: $<1 \times 10^{-7}$ for 10% change

Ext Freq Std Input: Input: 30 kHz to 10 MHz signal of amplitude >1.0 V rms into 1 k Ω . Maximum Input: 5 V p-p. Input other than 10 MHz will give scaled readings. External frequency standard must be 10 MHz for Period Average, Time Interval Average, Period (N = 1) and Time Interval (N = 1).

Trigger Lights: Light is on when input is above trigger level; off when input is below trigger level; blinking when channel is triggering. Operates over frequencies from 0 to 100 MHz.

Marker Outputs: Channel A and B Schmitt trigger outputs available on front panel. NOMINALLY 0 mV to ± 300 mV levels into ± 50 v DC.

Arm: Rear Panel Switch turns arming ON or OFF. With arming ON, the measurement is armed by an input other than the input involved in the measurement. The following are armed by an event at B: Freq A, Period A, Period Avg A, Freq C, DVM and Ratio C/A. The following are armed by an event at $C:T.I.A \rightarrow B$; $T.I.Avg A \rightarrow B$; and Ratio B/A.

HP-IB INTERFACE

HP-IB interface provides both digital output of measurement data ("talker"), as well as remote control inputs ("listener"), on the HP-IB.

Programmable Functions: Function, resolution, sample rate (maximum or manual control), arming, display modes, measurement modes, output mode, and reset commands. Trigger level, trigger slope, input impedance, coupling, separate/common/check, invert A and B. Trigger level is programmable in 10 mV steps in × 1; 100 mV in × 10. Trigger level accuracy under remote control in × 1: ± 35 mV.

HP-IB Commands: Unlisten, untalk, local lockout, device clear, serial poll enable, serial poll disable, go to local, selected device clear, and group execute trigger.

Service Request (SRQ): If enabled, indicates end of measurement. Maximum Data Output Rate: 500 readings per second.

Operating Temperature: 0° to 50°C.

Power Requirements: 100/120/220/240 V rms, +5%, -10% (Switch

selectable); 48-66 Hz; 150 VA Max. Accessories Furnished: Power cord Weight: Net 8.5 kg (18 lb., 12 oz.) Shipping 11.2 kg (24 lb., 12 oz.)

TIME BASE OPTION

OPTION 010 — High Stability Time Base (Oven)

Frequency: 10 MHz

Aging Rate: $<5 \times 10^{-10}$ /day after 24 hour warm up **Short Term:** $<1 \times 10^{-10}$ rms for 1 s averaging time

Temperature: <7 × 10-9 0° to 50°C Line Voltage: <5 × 10-9 for 10% variation Warm Up: <5 × 10-9 in 20 minutes

DIGITAL VOLTMETER OPTION

Trigger levels of input channels A and B and external voltages may be measured with DVM Option 021.

OPTION 021 — High Performance Digital Voltmeter***
Ranges: ±10, ±100, ±1000 V DC and autoranging
Full Range Display Resolution (1 sec. Meas. Time):

± 1000.00 V DC

± 125.000 V DC

± 12.5000 V DC

Sensitivity:

10 μ V for measurement time of 10 s (N = 107) 100 μ V for measurement time of 1 s (N = 106)

1 mV for measurement time of 100 ms (N = 105)

10 mV for measurement time of 10 ms (N = 104)

100mV for measurement time of 1 ms (N = 103)

Accuracy (20 Min. Warm Up): $\pm 0.03\%$ of reading, $\pm 0.004\%$ of range. For 1000 V range: $\pm 0.087\%$ of reading, $\pm 0.004\%$ of range.

Temperature Coefficient: $\pm 0.002\%$ of reading/°C, $\pm 0.001\%$ of range/°C.

Response Time (Filter Off, Step Input): 10 ms

Input: 10 MΩ NOMINAL, floating pair

Maximum Input:

High to Low: \pm 1100 V all ranges Low to Chassis Ground: \pm 500 V

Normal Mode Rejection Ratio: >80 dB at 50 Hz or greater, with filter on.

Effective Common Mode Rejection Ration (1k Ω Unbalance): DC: >120 dB

AC: >120 dB for multiples of 50/60 Hz with filter on Trigger Level Display (x 1):

Resolution: 1 mV

Accuracy: 4% of reading ± 25 mV (NOMINAL)

Note — Readings are automatically multiplied by attenuator setting.

*** Performance: 60 days at 23°C ± 5°C and R.H. <80%.

C CHANNEL OPTION

OPTION 031 - 1300 MHz C Channel

INPUT CHARACTERISTICS

Sensitivity: 20 mV rms

Coupling: AC

Trigger Level: 0 V NOMINAL, Fixed Impedance: 50Ω NOMINAL Maximum Input: 5 V rms, ± 5 V DC

Input Protection: Fused input Attenuator: AGC and variable attenuator for optimum noise

suppression on signals to 5 V rms.

FREQUENCY C MEASUREMENT

Range: 90 to 1300 MHz, prescaled by 4

LSD** Displayed: 1 MHz to 0.1 Hz in decade steps Resolution: Same as LSD**

Accuracy: ± Resolution ± Time Base Error × Frequency

Probe Power: Power to operate 10855A or HP active probe is

available on the front panel.

NOTES/DEFINITIONS

Trigger Error:

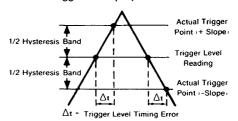
 $\sqrt{e_i^2 + e_n^2}$ Trigger Error = 1.4

Input Voltage Slew Rate at Trigger Point Where ei = Effective rms noise of counter's input channel (300 µV TYPICAL)

> en = rms noise voltage of input signal for a 100 MHz bandwidth

Note — The above assumes that both start and stop signal slew rates are the same.

Trigger Level and Trigger Point (x 1):



Trigger Level Timing Error (x 1): Trigger Level

Timing Error as it applies to Time Interval measurements:

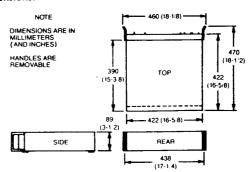
1/2 Hysterisis Band [Input slew rate at START trigger point]

1/2 Hysterisis Band Input slew rate at STOP trigger point

Time Interval Averaging:

Time Interval Averaging is a statistical process. Resolution is calculated for a 2 Sigma (σ) confidence level.

Dimensions:



1-8. OPTIONS

The options available for the Model 5328B, are listed in Table 1-2, below.

If the Option is ordered at the time the Model 5328B is ordered, it will be installed at the factory, and the instrument will be ready for operation when received.

To order any of the options listed in *Table 1-2* for field installation in a Model 5328B that has already been shipped, order by the HP Part Number (given in the table's right-hand column).

Table 1-2. Options

Opt. No.	Description	HP Part Number
010	High-stability Time Base Aging rate: 5×10^{-10} day	See Note at bottom of table
021	High-performance Digital Voltmeter Floating high-accuracy Digital Voltmeter for trigger level and external dc voltage measurements.	See Note at bottom of table
031	Channel C Frequency measurements to 1300 MHz; 20 mV sensitivity (prescaled by 4)	See Note at bottom of table
050	Combination of Option 021 (High-performance Digital Voltmeter) and Option 031 (Channel "C"), which are described above, and elsewhere throughout this manual	See Note at bottom of table
907	Front Handle Kit	5061-0088
908	Rack Flange Kit (for instruments without handles)	5061-9674
909	Rack and Handle Kit	5061-0075

NOTE

To order parts for field installation of options, refer to the following tables:

Option 010	Table 1-2a
Option 021	Table 1-2b
Option 031	Table 1-2c
Option 050	Table 1-2d

The following is a list of procedures for field installation of each option and where the procedures are in the manual:

Option 010	Page 8-20
Option 021	Page 8-110
Option 031	Page 8-124
Option 050	Combine Option 021 and 031 proceedures.

Table 1-2a. Option 010 Field Installation Parts

Reference Designator	HP Part Number	Description	Qty.
A3	05328-60018	Bd Ay-Oven Osc. Option 010	1
A3A1	10811-60111	Quartz Oscillator 10 MHz	1
į	0380-0640	Standoff-Threaded Nylon	1
H34	0570-0111	Screw, 6-32 Nylon Mach	1
H40	2360-0113	Screw, 6-32 SS Mach	3
H49	3050-0003	Washer, #6 Flat Fiber	2

Table 1-2b. Option 021 Field Installation Parts

Reference Designator	HP Part Number	Description	Qty.
A5	05328-60012	Bd Ay-PS Autorange	1
A6	05328-60013	Bd Ay-Buffer Amplifier	1 - 1
A7	05328-60014	Bd Ay-Voltage/Frequency Converter	1
A17	05328-60015	Bd Ay-DVM Front Panel Switch	1
J210/J211	1510-0091	Binding Post/Banana Plug Connector	2
J210A/J211A	2950-0144	Nut-Hex 3/8-32	2
J210B/J211B	2420-0001	Nut-Hex 6-32 W/Lockwasher	2
A6MP3/A6MP4	0370-2486	Pushbutton-Jade Gray	2
MP18	05328-20227	Front Panel-DVM	1
H59	2360-0476	Screw, Mach 6-32 .188 In Lg (to secure front panel)	8

Table 1-2c. Option 031 Field Installation Parts

Reference Designator	HP Part Number	Description	Qty.
A8	05328-60025	Bd Ay-1.3 GHz	1
J310	05305-60205	Connector Assembly-BNC	1
J310F1	2110-0301	Fuse, .125 Amp	1
J311	053 28-60013	Cable Ay-Probe Power 05328 - 601/3	1 1
J311A	2950-0001	Nut, Hex 3/8-32	1
H23	0370-1005	Knob-Base-PTR 3/8 JGK	1
MP19	05328-20228	Front Panel, C-Channel	1
H59	2360-0476	Screw-Mach 6-32, .188 In Lg	8
		(to secure Front Panel)	
A1U37	1818-2274	ROM, C-Channel Option	1 1
		Replaces Standard A1U37 ROM	

Table 1-2d. Option 050 Field Installation Parts

Reference Designator	HP Part Number	Description	Qty.	
MP20	05328-20229	Front Panel, Option 050	1	
Option 021	All Parts listed in Table 1-2b, EXCEPT: MP18, Option 021 Front Panel.			
Option 031	EXCEPT: MP1 H59,	All Parts listed in Table 1-2c, EXCEPT: MP19, Option 031 Front Panel. H59, 6-32 Machine Screws for securing the Front Panel. (A set of 8 are already included in Option 021 parts.)		

1-9. ACCESSORIES AVAILABLE

Table 1-3 lists accessories available.

- 1. The test leads listed in Table 1-3 are used with the (optional) Digital Voltmeter.
- 2. The coaxial cable listed is used with input "A", "B", or (optional) "C".
- 3. The Rack Flange Kit is used to install the Model 5328B in a rack mount cabinet.

Table 1-3. Accessories Available

Description	HP Part Number
HP Interface Bus Interconnect Cable	
Length: 1 m (3.3 feet)	10833A
2 m (6.6 feet)	10833B
4 m (13.2 feet)	10833C
0.5 m (1.6 feet)	10833D
Front Handle Kit*	5061-0088
Rack Flange Kit*	5061-0074
(for instrument without handles)	
Rack and Handle Kit*	5061-0075
Test Leads	11002A
Dual banana plug to alligator clips	
Length: 150 cm (60 inches)	
Test Leads	11003A
Dual banana plug to pencil probe and alligator clips	
Length: 150 cm (60 inches)	
Cable, coaxial, 50-ohms	10503A
Terminated at each end with UG-88C/U	
BNC Male	
Length: 120 cm (48 inches)	
*Installation instructions included with kit	

1-10. RECOMMENDED TEST EQUIPMENT

Equipment recommended for testing and/or servicing the Model 5328B is listed in *Table 1-4*. Unless otherwise indicated, all equipment listed is made Hewlett-Packard. If the recommended equipment is not available, other equipment may be used, provided it meets the required characteristics.

Table 1-4. Recommended Test Equipment

Instrument Type	Required Characteristic	Recommended Instrument
VHF Signal Generator	Freq Range: 500 kHz to 512 MHz	HP 8640A/B
Synthesized Signal Generator	Freq Range: 1MHz to 1300 MHz	HP 8550A/C with 44 Option 100
Sweep Oscillator	Freq Range: 0.01 GHz to 1.4 GHz	HP 8620C with # HP 86222A

Table 1-4. Recommended Test Equipment (continued)

Instrument Type	Required Characteristic	Recommended Instrument		
RF Plug-in		HP 86602B		
Power/Meter/Sensor	Range: -20 dBm to + 20 dBm	HP 436A and HP 8481A		
Test Oscillator	Freq Range: 10 Hz to 10 MHz Amplitude: 5V p-p	HP 654A 🔏		
Termination, Feed-through (Two required)	50 ohms ±1%	HP 10100C		
Function Generator	Freq Range: 0.01 Hz to 13 MHz	HP 3312A		
Adapter, BNC	"T" (m) (f) (f)	1250-0781		
Adapter, Type "N" (Two required)	N (m) to BNC (f)	1250-0780		
Adapter, Type "N"	N(m) to BNC(m)	1250-0082		
Power Splitter	Freq Range: dc to 18 GHz	HP 11667A		
Attenuator	10 dB	HP 8491A (Option 010)		
DC Standard	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	Fluke 343A		
Calculator	HP-IB Compatible	HP 9825A* //4		
HP-IB Calculator Interface	Connects HP 9825A to HP-IB	HP 98034A*		
Oscilloscope	Bandwidth: 50 MHz Sensitivity: 50 mV/major div	HP 1725A		
Digital Voltmeter	10V Range 0.01% Accuracy, plus ohmmeter range to 1 Mohm	HP 3468B		
Logic Clip	\geq 0.4 × Supply voltage = Logic High	HP 548A		
DC Power Supply	0-10V Stable to \pm 1 mV	HP 6214B		
RMS Voltmeter	RMS AC Voltage 0-10V Range	HP 3400A		
Logic Probe	TTL Compatible	HP 10525T		
Logic Pulser	TTL Compatible	HP 10526T		
Pulse Generator	0.5 Hz to 25 MHz at 1V	HP 8008A		
	TTL Compatible	HP 5005B		

SECTION II INSTALLATION

2-1. INTRODUCTION

This section provides installation instructions for the HP Model 5328B Universal Counter. It also includes information about initial inspection and damage claims, preparation for use, packaging, storage, and shipment.

2-2. SAFETY CONSIDERATIONS

The warnings and cautions below must be followed for your protection and to avoid damage to equipment.

WARNING

BE SURE ALL ELECTRICALLY OPERATED EQUIPMENT YOU USE IS PROPERLY GROUNDED.

CAUTION

Be sure to turn electrical power off whenever you are connecting or disconnecting test equipment, installing or removing parts, etc.

2-3. INITIAL INSPECTION

Inspect the shipping container for damage. If the shipping container or cushioning material is damaged, it should be kept until the contents of the shipment have been checked for completeness and the counter has been checked mechanically and electrically.

The contents of the shipment should be as shown in Figure 1-1. Procedures for checking electrical performance are given in Section 4 of this manual.

If the contents are incomplete, if there is mechanical damage or defect, or if the Model 5328B fails its performance test (see Section 4 of this manual), notify the nearest Hewlett-Packard office.

If the shipping container is damaged, or the cushioning material shows signs of stress, notify the carrier as well as the Hewlett-Packard office. Keep the shipping materials for the carrier's inspection. The HP office will arrange for repair or replacement at HP's option without waiting for a claim settlement.

2-4. PREPARATION FOR USE

CAUTION

- To prevent damage to the HP 5328B, make the line voltage selection before connecting it to line power.
- Also, be sure the line power cord is connected to a line power socket that is provided with a protective ground contact.
- Be sure the correct-value line fuse is installed for the line voltage to be used.

2-5. Power Requirements

The Model 5328B can be operated on single-phase 100V, 120V, 220V, or 240V ac power; these are "nominal" voltages, actual line voltages may vary from -10% to +5% from these values. A chart indicating these voltages, the voltage limits, and the appropriate fuse to use is presented in *Table 2-1*.

The Model 5328B requires 150VA (maximum).

The power line frequency must be in the range from 48 to 66 Hz.

Line Voltage Line Fuse (Nominal and Range) **HP Part Number** Rating 100V 2110-0002 2.0A 90V to 105V 250V 120V Fast-108V to 126V blow 220V 1.0A 2110-0001 250V 198V to 231V 240V Fast-216V to 252V blow

Table 2-1. Line Fuses



2-6. Line Voltage and Fuse Selection

To avoid damage to the Model 5328B,

- 1. Its rear-panel line voltage selector switches must be set to the positions corresponding to the nominal line voltage being used (i.e., "100V", "120V", "220V", or "240V"). The switch settings are illustrated near the switches at the left-hand side of the rear panel.
- 2. A line fuse of the correct rating must be installed. The line fuse values for the Model 5328B's operating voltage ranges are listed near the line fuse holder on the Model 5328B's rear panel, and in *Table 2-1* in this manual section.

To configure the Model 5328B for operation from a specific nominal line voltage

- a. Disconnect the ac power cable.
- b. Set the rear-panel line selector switches as required.
- c. Install the appropriate (1.0-amp or 2.0-amp) fuse.
- d. Reconnect the ac power cable.

2-7. Power Cable

WARNING

TO PROTECT OPERATING AND SERVICING PERSONNEL, THE MODEL 5328B IS EQUIPPED WITH A THREE-PIN POWER RECEPTACLE. THE CENTER PIN OF THE RECEPTACLE CONNECTS THE INSTRUMENT'S CHASSIS AND PANELS TO EARTH GROUND WHEN USED WITH A PROPERLY WIRED THREE-CONDUCTOR OUTLET AND POWER CABLE. IMPROPERLY GROUNDED EQUIPMENT CAN RESULT IN HAZARDOUS POTENTIALS BETWEEN UNITS.

To accommodate to the different power receptacles used throughout the world, the Model 5328B is supplied with a power cord appropriate for the destination country. The power cords available are shown in *Figure 2-1*.

Δ	`\
1)

PLUG TYPE	CABLE HP PART NO.	+C D	PLUG DESCRIPTION	CABLE LENGTH (INCHES)	CABLE COLOR	FOR USE IN COUNTRY
250V	8120-1351 8120-1703	0 6	Straight **BS1363A 90°	90 90	Mint Gray Mint Gray	United Kingdom, Cyprus, Nigeria Rhodesia, Singapore
250V	8120-1369 8120-0696	0 4	Straight **NZSS198/ASC112 90°	79 87	Gray Gray	Austrailia, New Zealand
250V	8120-1689 8120-1692	7 2	Straight **CEE7-Y11 90°	79 79	Mint Gray Mint Gray	East and West Europe, Saudi Arabia, Egypt, So Africa, India (Unpolarized in many nations)
125V	8120-1348 8120-1398 8120-1754 8120-1378 8120-1521 8120-1676	5 7 1 6 2	Straight **NEMA5-15P 90° Straight **NEMA5-15P Straight **NEMA5-15P 90° Straight **NEMA5-15P	80 80 36 80 80 30	Black Black Black Jade Gray Jade Gray Jade Gray	United States, Canada, Japan (100V or 200V), Mexico, Philippines, Taiwan
250V	8120-2104	3	Straight **SEV1011 1959-24507 Type 12	79	Gray	Switzerland
250V	8120-0698	6	Straight **NEMA6-15P			United States, Canada
220V (C)	8120-2956 8120-2957	2 3	Straight **DHCK 107 90°	79 79	Gray Gray	Denmark

^{*}CD = Check Digit (refer to Section VI).

N = Neutral



^{**}Part number shown for plug is industry identifier for plug only. Number shown for cable is HP Part Number for complete cable including plug.

E = Earth Ground

L = Line

2-8. Interconnections

The Model 5328B's power cable requirements are described above.

2-9. BNC CONNECTORS

Except for its rear-panel HP-IB connection (use of which is not required for basic operations of the counter) all signal connections to the Model 5328B are made via standard BNC connectors. Hewlett-Packard offers a variety of cables and adapters that can be used with the Model 5328B's BNC connectors; refer to a current HP catalog or to your local Hewlett-Packard Sales and Service Office for additional information. A listing of the Model 5328B's BNC connectors is provided in Table 2-2.

Table 2-2. Model 5328B Mating Connectors

Connector	Industry Identification	HP Part Number	Alternate Source	
Channel "A" INPUT (A12J2)	BNC, male	1250-0118	Bendix 30384-1	
Channel "A" MARKER OUTPUT (A19J2)	BNC, female	1250-1163	none	
Channel "B" INPUT (A12J3)	BNC, male	1250-0118	Bendix 30384-1	
Channel MARKER OUTPUT (A19J4)	BNC, female	1250-1163	none	
Channel "C" INPUT (J311)	BNC, female	1250-0186	Specialty Connector Co 28JR129-1	
Digital Voltmeter "High" INPUT (J210)	Banana, female	1510-0091	none	
Digital Voltmeter "Low" INPUT (J211)	Banana, female	1510-0091	none	
Inhibit Open Collector (rear) J1	BNC, female	1250-0083	none	
Gate Out (rear) (J1)	BNC, female	1250-0083	none	
TB Out (rear) (J3)	BNC, female	1250-0083	none	
OSC EXT (rear) (J4)	BNC, female	1250-0083	none	
HP-IB Connector (rear) (A15J6)	Microribbon, female	1251-3283	none	

2-10. HP-IB (HEWLETT-PACKARD INTERFACE BUS)

A standard feature of the Model 5328B is its capability to be connected to other units via the Hewlett-Packard Interface Bus (HP-IB).*

Via the HP-IB the Model 5328B can "TALK ONLY", sending data to some other device (such as a printer); or it can be "ADDRESSABLE", receiving commands from a device connected to it as a "controller". (A typical "controller" is an HP Calculator, such as the Model 9826.)

The HP-IB cable connectors include locking screws to lock them together. When you have connected each cable to its mating connector, beginning with the connector on the Model 5328B, tighten the locking screws; this will keep the cables from accidentally coming loose.

NOTE

On HP-IB cables — black screws have metric threads, silver screws have English threads. The connection at the Model 5328B requires metric threads. Do not try to mate black and silver locking screws.

CAUTION

To prevent possible damage to the Model 5328B, be sure you turn the line power off before connecting or disconnecting any cable (especially the HP-IB cable).

CAUTION

Be sure to unlock cable connectors before you try to separate a cable from its mating connector. Failure to do so can cause you to strain the cable, damaging it internally; it can also cause you to damage the connectors.

HP-IB cables are available as listed in *Table 2-3* below. For more information about HP-IB, refer to the current Hewlett-Packard catalog or contact your local HP Sales and Service Office.

Table 2-3. HP-IB Cables Available

Cable Length	th Hewlett-Packard Product Number	
1 m (3.3 ft)	HP10833A	
2m (6.6 ft)	HP10833B	
4 m (13.2 ft)	HP10833C	
0.5 m (1.6 ft)	HP10833D	

^{*}HP-IB is Hewlett-Packard's implementation of IEEE standard 488-1978 and IEC 625-1.





2-11. Operating Environment

While it is operating, the Model 5328B's environmental conditions should be limited as follows —

Temperature 0°C to 50°C, (32 F to 122 F)

The maximum and minimum temperatures are also listed as part of the Model 5328B's Specifications in Table 1-1. If these limits are exceeded at the installation site, auxiliary cooling or heating should be used to keep the environment within allowable limits. A one-inch space should be kept clear above the Model 5328B in order to allow for the circulation of cooling air. Also, you should be sure to keep clear the fan intake at the rear of the instrument.

2-12. INSTALLATION INSTRUCTIONS

The Model 5328B can be installed for use in any of several ways —

- It can be operated on a bench, individually, or as part of a "system" of instruments that are connected and operated via HP-IB. Additional information is provided below.
- It can be installed and operated in a rack, individually or as part of a "system". Additional information is provided below.
- It can be installed in a Transit Case for additional protection during transit. Additional information is provided below.

2-13. Bench Operation

The Model 5328B's cabinet has plastic feet and fold-away tilt stands for convenience in bench operation. The tilt stands allow the instrument to be tilted for easier use of its front-panel controls and indicators.

2-14. Rack Mounting

The standard Model 5328B is ready for bench operation as shipped from the factory. To mount the Model 5328B in a rack, it is necessary to order and install the rack flange kit listed in Table 1-2.

CAUTION

Ambient temperature in the rack during operation of the Model 5328B should not exceed 50°C (122 F). Be sure that the Model 5328B's position in the rack allows sufficient air circulation and that nearby equipment does not discharge hot air directly onto the instrument.

2-15. Transit Cases

Hewlett-Packard offers Transit Cases that can be used to protect an individual instrument that is often transported. Some Transit Case versions are large enough that they allow groups of instruments (i.e., "systems") to be protected as they are transported, without having to disconnect the system connections during transit. For additional information about HP's transit cases and their possible use with the Model 5328B, contact your local Hewlett-Packard Sales and Service Office.

2-16. MODIFICATIONS

Field-installation of any of the Model 5328B Options listed below is possible. If you want to have any one or more of the Options listed below added to a Model 5328B that was originally ordered (and shipped) without it, contact your local Hewlett-Packard Sales and Service Office.

- Option 010 High-stability Oscillator
- Option 021 Digital Voltmeter
- Option 031 90 MHz to 1300 MHz "C" Channel
- Option 050 combination of Option 021 and Option 031, described above.

2-17. STORAGE AND SHIPMENT

2-18. Environment

The HP 5328B Universal Counter can be stored or shipped in environments within the following limits —

Maximum Temperature	+75°C (167°F)
Minimum Temperature	40°C (-40°F)
Altitude (unpressurized)	

The Model 5328B should also be protected from temperature extremes which could cause condensation inside it.

2-19. Packaging

Tagging for Service. If the Model 5328B is being returned to Hewlett-Packard for service, please complete one of the blue repair tags located at the end of this section and attach it to the unit.

Original Packaging. Containers and materials identical to those used in factory packaging are available through Hewlett-Packard offices. If the Model 5328B is being returned to HP for servicing, attach a tag indicating the type of service required, return address, model number, and full serial number. Also mark the container "FRAGILE" to ensure careful handling. In any correspondence, refer to the unit by Model Number and full Serial Number.

Other Packaging. The following general instructions should be used for re-packing with commercially available materials.

- a. Wrap the Model 5328B in heavy paper or plastic. (If shipping to a Hewlett-Packard office or service center, attach a tag indicating the type of service required, return address, model number, and full serial number.)
- b. Use a strong shipping container. A double-wall carton made of 350-pound test material is adequate.
- c. Use a layer of shock-absorbing material 75 to 100 mm (3 to 4 inches) thick around all sides of the Model 5328B to provide firm cushioning and prevent movement inside the container. Protect the front panel with cardboard.
- d. Seal the shipping container securely.
- e. Mark the shipping container "FRAGILE" to ensure careful handling.
- f. In any correspondence, refer to the unit by Model Number and full Serial Number.

2-20. Storage

If the Model 5328B is to be stored for an extended period of time, it should be enclosed in a clean, dry, sealed container. See specifications in Section 1 for storage environment limitations.

PACKARD

COMPANY

Should one of your HP instruments need repair, the HP service organization is ready to serve you. However, you can help us serve you more effectively. When sending an instrument to HP for repair, please fill out this card and attach it to the product. Increased repair efficiency and reduced turn-around time should result.

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over

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OTHER	OTHER,	OTHER
Observed symptoms/problems	Observed symptoms/problems	Observed symptoms/problems
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□constant □INTERMITTENT	□ CONSTANT □ INTERMITTENT	CONSTANT DINTERMITTENT
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SECTION III OPERATION (APPLICATIONS AND PROGRAMMING)

3-1. BASIC COUNTER OPERATION

The HP Model 5328B Universal Counter can make the following measurements —

- Frequency
- Period
- Frequency Ratio
- Time Interval
- Totalization or Scaling

Procedures for making each of these measurements are provided later in this manual section.

The Model 5328B includes provision for remote control via the Hewlett-Packard Interface Bus (HP-IB). Instructions for this are also provided later in this manual section.

The Model 5328B's front-panel controls, connectors and indicators, including those for the optional Digital Voltmeter and the optional C Channel, are illustrated in *Figure 3-1*. Descriptions of the controls are necessarily shortened in this figure; more-detailed descriptions of these controls are given following the figure. Similarly, the Model 5328B's rear-panel controls, connectors, and indicators are described in *Figure 3-2*, with more-complete descriptions following the figure.

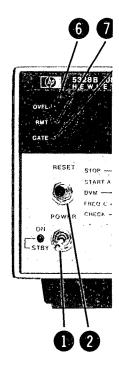
WARNING

BEFORE THE INSTRUMENT IS SWITCHED ON, ALL PROTECTIVE EARTH TERMINALS, EXTENSION CORDS, AUTOTRANSFORMERS AND DEVICES CONNECTED TO IT SHOULD BE CONNECTED TO A PROTECTIVE EARTH GROUNDED SOCKET. ANY INTERRUPTION OF THE PROTECTIVE EARTH GROUND WILL CAUSE A POTENTIAL SHOCK HAZARD THAT COULD RESULT IN PERSONAL INJURY.

CAUTION

Before energizing the counter, the instrument must be set to the voltage of the power source or damage to the instrument may result. See Section II of this manual.

- POWER switch. In STBY position, supplies power to oven of the Option 010 Highstability Time Base (if installed), to maintain a constant temperature for the crystal. In the ON position, supplies normal operating power to the Model 5328B.
- RESET button. Resets display and internal count to zero. When continuously
 depressed, lights all segments of the LED display and all annunciator LED's for LED test.
 Returns the Model 5328B to LOCAL CONTROL when HP-IB Interface is in use.
- 3. FUNCTION selector. Selects mode of operation.
 - a. RATIO C/A. Sets counter to measure the ratio of the frequency at (optional) Channel C to the frequency at Channel A.
 - CHECK. Applies 10 MHz to decade counting assemblies. Verifies operation of SAMPLE RATE control, RESOLUTION switch, and RESET.
 - FREQ C. Sets counter to measure the frequency at the (optional) Channel C input.
 - d. DVM. Sets counter to measure the voltage applied to the terminals of the (optional) Digital Voltmeter.
 - e. START A,STOP. Sets the counter to totalize the number of events at the "A" input until STOP is selected, for N=1 on the RESOLUTION switch. For N=1, the number of counts divided by N is totalized. The scaled output (i.e., frequency of A/N) is available at the rear-panel TB OUT (Time Base Output) connector.
 - f. START CLOCK. A "phantom" function, located one switch position clockwise from STOP. Totalizes the internal 10 MHz clock divided by N, where "N" is selected by the RESOLUTION switch (N≤10). The scaled output (i.e., 10 MHz/N) is available at the rear-panel TB OUT (Time Base Output) connector.
 - g. DVM/A. A "phantom" function, located two switch positions clockwise from STOP. Used for scaling DVM measurements. Counter displays DVM/freq A, where freq A is \leq 10 MHz.
 - h. DVM, A → B. A "phantom" function, located three switch positions to the right of STOP. The voltage at the DVM terminals is integrated over the synchronized time interval defined by events at Channels A and B. Units of the displayed reading are volt•seconds.
 - i. FREQ A. Sets counter to measure frequency at Channel A.
 - j. PER A. Sets the counter to measure period at Channel A.
 - k. PER AVG A. Sets counter to make a period average measurement of the signal at Channel A. The number of periods over which the average measurement is made is determined by "N", selected by the RESOLUTION switch.
 - I. RATIO B/A. Sets counter to measure the ratio of the frequency at Channel B to the frequency at Channel A.
 - m. T.I. $A \rightarrow B$. Sets counter to make a time interval measurement. Start signal is applied to Channel A and the stop signal is applied to Channel B.



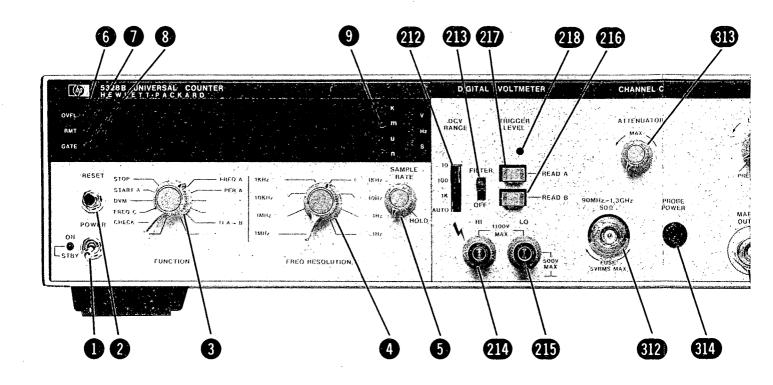
 T.I. AVG A→B. Set time interval from measurement is r switch.

NOTE: "Phantom not labelled on th

FREQ RESOLUTION, N
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106

107



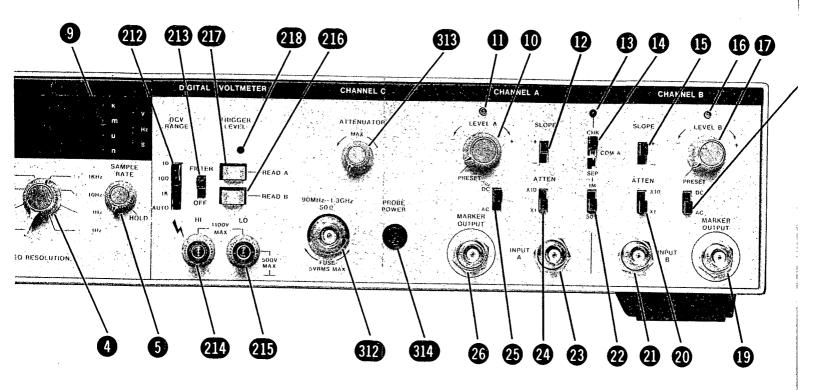
n. T.I. AVG A→B. Sets counter to make a time interval average measurement of the time interval from A TO B. The number of time intervals over which the average measurement is made is determined by "N", selected by the RESOLUTION switch.

NOTE: "Phantom" functions are those which, due to relatively limited use, are not labelled on the Model 5328B's front panel.

4. FREQ RESOLUTION, N selection. Selects resolution in frequency measurements and "N" for totalizing and averaging measurements. Determines how long the main gate is open for frequency measurements.

<u>N</u>	Gate Time	Resolution
1	1 μS	1 MHz
10	10 μS	100 kHz
100	100 μS	10 kHz
103	1 mS	1 kHz
104	0.01 S	100 Hz
105	0.1 S	10 Hz
106	1.0 S	1 Hz
107	10 S	0.1 Hz

- 5. SAMPLE RATE control. Varies tl from 2 mS to HOLD, which he
- 6. OVFL (OVerFLow) annunciato digits (digits left-most from the
- 7. RMT (ReMoTe) annunciator. I
- 8. GATE annunciator. Indicates wl is in progress.
- 9. "V", "K", "S", "m", "u", "n", the measurement.
- 10, 17. LEVEL control. Used in conjutriggering occurs. With X1 att volts.
- 11, 16. Trigger lights. Light blinks whe is below the trigger level. Ligh
- 12, 15. SLOPE switch. Selects triggerir



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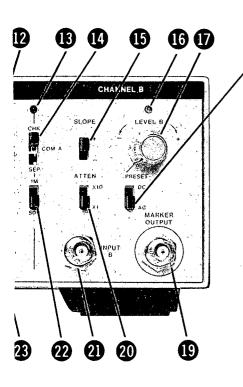
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- 5. SAMPLE RATE control. Varies the time between measurements, in a continuous range from 2 mS to HOLD, which holds the display indefinitely.
- 6. OVFL (OVerFLow) annunciator. Indicates that one or more of the most-significant digits (digits left-most from the decimal) are not displayed.
- 7. RMT (ReMoTe) annunciator. Lights when Model 5328B is in remote operation.
- 8. GATE annunciator. Indicates when the counter's main gate is open and a measurement is in progress.
- 9. "V", "K", "S", "m", "u", "n", and "Hz" annunciators. Indicate the units multiplier of the measurement.
- 10, 17. LEVEL control. Used in conjunction with ATTEN switch to select voltage at which triggering occurs. With X1 attenuator, level is variable over ± 2.5 volts; in X10, ± 25 volts.
- 11, 16. Trigger lights. Light blinks when its channel is triggering. Light is OFF when input signal is below the trigger level. Light is ON when input signal is above trigger level.
- 12, 15. SLOPE switch. Selects triggering on either positive or negative slope of input signal.



13. CHK indicator. Lights when instrument is in CHK mode.

14. Input amplifier control switch.

- a. CHK. With FUNCTION switch in FREQ A, counter should display 100 MHz ±1 count. Checks operation of phase-locked 100 MHz oscillator, SAMPLE RATE, RESOLUTION, N, and RESET.
- b. COM A. The signal at "A" is also applied to Channel B for single-source time interval measurement. The "B" input and attenuator is disconnected from the input circuitry. Channel B coupling and attenuation are determined by the Channel A settings.
- c. SEP. Allows independent operation of Channels A and B.
- 15. See "12,15", above.
- 16. See "11,16", above.
- 17. See "10,17", above.
- 18, 25. Coupling switch. Selects ac or dc coupling for input signal. When input amplifier control switch ("14") is in COM A, Channel B coupling is determined by the setting of the Channel A coupling switch.
 - 19. MARKER OUTPUT B. Inverted Channel B Schmitt trigger output indicates when Channel B has triggered; 0 to +300 mV level into 50 ohms with less than 20 mS delay.
- 20, 24. ATTEN switch. Selects attenuation of input signal. Signal amplitude is reduced by 10 in X10. When input amplifier control switch ("14") is in COMA, Channel B attenuation is determined by Channel A attenuation switch.
- 21, 23. Channel Inputs. Input Channels B and A, respectively.
 - 22. Input Impedance switch. Selects input impedance of 50 ohms or 1 Megohm for both channels A and B. For input amplifier control switch ("14") in COM A, impedance remains the same.
 - 23. See "21,23", above.
 - 24. See "20,24", above.
 - 24. See "18,25", above.
 - 26. MARKER OUTPUT A. Channel A Schmitt trigger output indicates when Channel A has triggered; 0 to +300 mV level into 50 ohms with less than 20 nS delay.

neasurements, in a continuous range definitely.

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1 5328B is in remote operation.

nain gate is open and a measurement

ators. Indicate the units multiplier of

N switch to select voltage at which rariable over ± 2.5 volts; in X10, ± 25

gering. Light is OFF when input signal out signal is above trigger level.

ve or negative slope of input signal.

NOTES

- should display 100 MHz ±1 z oscillator, SAMPLE RATE,
- iel B for single-source time or is disconnected from the on are determined by the

and B.

- gnal. When input amplifier letermined by the setting of
- ger output indicates when s with less than 20 mS delay.
- nplitude is reduced by 10 in IA, Channel B attenuation is
- hms or 1 Megohm for both 4") in COM A, impedance

dicates when Channel A has n 20 nS delay.

- 1. Items "212 through 218" below are part of the Digital Voltmeter, which is not a standard part of the Model 5328B. The Digital Voltmeter is available when the Model 5328B is ordered with "Option 021" or "Option 050".
- 2. Operation of the Digital Voltmeter (if installed) is described in Paragraph 3-25.
- 212. DCV RANGE switch. Allows selection of $\pm 10V$, $\pm 100V$, $\pm 1000V$, or AUTORANGE.
- 213. FILTER switch. When "on", allows small signals to be measured in the presence of high common-mode noise. Response time when "on" is one second, when "off" 10 mS.
- 214. HI Input Terminal. Input terminal for "high" potential of external voltage. Must be less than 1100V above LO input terminal. (Types of test leads available are listed in Table 1-2.)
- 215. LO Input Terminal. Input terminal for "low" potential of external voltage. Must be less than 500 volts above chassis ground.
- 216. READ B. When depressed, measures Channel B trigger level, regardless of setting of FUNCTION switch.
- 217. READ A. When depressed, measures Channel A trigger level, regardless of setting of FUNCTION switch.

NOTE

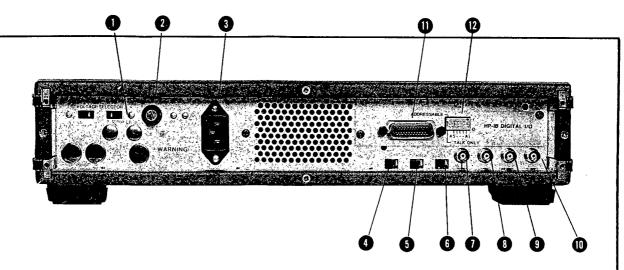
The Model 5328B's "V" annunciator lights if: 1) the FUNCTION switch is set to DVM, 2) the DVM's READ B or READ A button is pressed.

218. Access hole. Allows access to the DVM's "zero" adjustment. (The label "TRIGGER LEVEL" pertains to the READ A and READ B switches.)

NOTE

Items "312" through "314" below are part of the C Channel hardware, which is not a standard part of the Model 5328B. The C Channel is available when the Model 5328B is ordered with "Option 031" or "Option 050".

- 312. Input for Channel C. Frequency range allowed is 90 MHz to 1300 MHz (1.3 GHz).
- 313. ATTENUATOR. Attenuates Channel C input signal from 20 mV (maximum sensitivity, control fully clockwise) to 5 Vrms (minimum sensitivity, control fully counterclockwise).
- 314. PROBE POWER. Provides power for 1300 MHz preamplifier or active probe.



- 1. VOLTAGE SELECTOR switches. Set these switches as indicated in the chart at the lower left-hand side of the Model 5328B's rear panel to correspond to the nominal operating line voltage.
- 2. LINE FUSE. A listing of the fuse required for each nominal line voltage is provided to the right of the voltage selector setting chart at the bottom of the Model 5328B's rear panel. Instructions for fuse installation are provided in Section 2 of this manual.
- 3. AC LINE connector. Connection point for the Model 5328B's ac line cord. A listing of the line cords available is provided in Section 2 of this manual.
- 4. ARM switch. This switch selects the signal that is used to "arm" the Model 5328B for measurements. A listing of the effects of the setting of this switch, depending on the measurement FUNCTION selected, is provided in *Table 3-1*, below.
- 5. STORAGE switch. Controls length of time reading is displayed.

In "ON", display is held during the time a measurement is being made (i.e., it changes only at the end of a measurement).

In "OFF", the display changes as the count changes as the measurement is being made.

6. OSC switch. Selects source of time base to be used during a measurement.

In "INT", the Model 5328B's internal oscillator is selected as the time base for measurement.

In "EXT", a signal source (connected to the External Oscillator Input/Internal Oscillator Output connector (item #7, below) is used as the time base for measurements.

7. External Oscillator Input/Internal Oscillator Output connector. Use depends on setting of OSC switch (item #6, above).

When OSC switch is set to "INT", a buffered TTL signal derived from the internal time base is available at this connector.

When the OSC switch is set to "EXT", the external signal source for time base signals is connected to this connector.

8. TB OUT Connector. Output signal here depends on setting of the Model 5328B's front-panel FUNCTION switch.

When the FUNCTION switch is set to "START CLOCK", the signal here is a TTL level at the frequency of the internal oscillator (10 MHz), divided by "N" (which is determined by the setting of the front-panel RESOLUTION switch).

When the FUNCTION switch is set to "START A", and the RESOLUTION switch is set to an "N" value of "10" or more, the signal here is a TTL level at the frequency of the INPUT "A" signal, divided by "N".

- 9. GATE OUT Connector. Signal here indicates status of the Model 5328B's main gate "high" = "gate is open", "low" = "gate is closed".
- 10. INHIBIT Connector.

A TTL "low" (or "ground") signal applied here (standard TTL load impedance) prevents the Model 5328B from making a measurement.

- 11. HP-IB Interface Connector. Input/output connector for HP-IB data and/or control signals. For a description of HP-IB, see paragraph 3-35.
- 12. HP-IB Interface Address Switch. Determines the Model 5328B's HP-IB address when used with an HP-IB "controller". For a description of HP-IB, see paragraph 3-35.

NOTE

Additional holes, not specifically identified by number in this illustration, are provided for easy installation (at the factory, typically) of such options as rear-panel connectors that duplicate existing front-panel connectors, or connectors that provide capabilities not part of the standard Model 5328B or its standard set of Options. Any of these holes that is not used for installation of a connector will be blocked by use of a plug-button.



3-2. CONTROLS, CONNECTORS, INDICATORS — DETAILED DESCRIPTION

3-3. Front-panel

3-4. DISPLAY

The Model 5328B's display consists of a nine-digit numeric display, plus annunciators for indicating units of measurement.

The numeric indicators are of the seven-segment LED type.

Measurement units indicated by the annunciators are — "Hz", "S", "V", and the multipliers "M" (for 106 or 10-3, as appropriate for the units being used), "k" (for 103), " μ " (for 10-6), and "n" (for 109 or 10-9, as appropriate).

The display units and multipliers are automatically displayed, along with the correct decimal point location.

The "OVFL" indicator lights to indicate that the left-most significant digit(s) have overflowed the display.

The "RMT" indicator lights to indicate that the Model 5328B is operating under remote control via the HP-IB.

The "GATE" indicator indicates that the counter has been "armed" and that a measurement is in process.

3-5. POWER

The POWER switch has two positions — "ON" and "STBY".

NOTE

The Model 5328B has no "on/off" switch controlling its operating power. Whenever its line cord is plugged into an operating ac line, the input to the main power transformer, plus the unregulated dc power to the (optional) high-stability oscillator oven is "on".

In its STBY position, some (but not all) of the Model 5328B's power supplies are turned off.

In its ON position, all of the Model 5328B's power supplies are turned on.

3-6. RESET

The RESET pushbutton resets the display and internal count to zero, and also initiates single measurements when the SAMPLE RATE control is in the HOLD mode. Pushing the RESET button also restores the counter to local control (when not remotely locked out by the HP-IB Local Lockout universal command).

3-7. SAMPLE RATE CONTROL

The SAMPLE RATE control sets the minimum time between samples. This time is continuously variable from less than two milliseconds between measurements to "HOLD", which holds the display indefinitely. The counter will internally (self) arm (via the SAMPLE RATE control) only when ARMING is OFF and the FUNCTION selected is other than FREQ A, FREQ C, and RATIO C/A.

3-8. ARMING

The counter may be armed internally (i.e., made ready to start a measurement) by the SAMPLE RATE control, or externally by the input signal itself, (arming off) or by a signal not directly involved in the measurement (arming on). Table 3-1 is an arming status table. A rear-panel switch turns ARMING either ON or OFF. The counter is armed within one microsecond after the event at the "B" arming input, and is armed within ten microseconds after the event of the "C" arming input.

Table 3-1. Arming Status

FUNCTION	ARMING OFF Armed by	ARMING ON Armed by			
FREQ A	"A" input	"B" input			
PERIOD A	SAMPLE RATE	"B" input			
PERIOD AVG A	SAMPLE RATE	"B" input			
T.I. A TO B	SAMPLE RATE	"C" input			
T.I. AVG A TO B	SAMPLE RATE	"C" input			
FREQ C	"C" input	"B" input			
DVM	SAMPLE RATE	"B" input			
RATIO B/A	SAMPLE RATE	"C" input			
RATIO C/A	"C" input	"B" input			
DVM/A	SAMPLE RATE	"B" input			
DVM A TO B	SAMPLE RATE	"C" input			
CHECK	Internal	"B" input			
START	Measurement contro	lled by FUNCTION switch			
START CLOCK	Measurement controlled by FUNCTION switch				

3-9. RESOLUTION, N SWITCH

The RESOLUTION, N switch determines the amount of time that the counter's main gate is open for a particular measurement when the Main Gate Flip Flop determines the gate time. Depending on the measurement, this time results in a certain measurement resolution (e.g., frequency measurements), a number of intervals averages (e.g., period measurements), or an integration time (e.g., DVM measurements).

Table 3-2 shows the setting of the RESOLUTION switch and the corresponding time the main gate is open.

Table 3-3 summarizes the FUNCTIONS and the corresponding interpretation of the RESOLUTION N switch setting.

Table 3-2. RESOLUTION Switch Settings and Gate Times

RESOLUTION	N	GATE TIME
0.1 Hz	107	10 s
1 Hz	106	1 s
10 Hz	105	0.1 s
100 Hz	104	10 ms
1 kHz	103	1 ms
10 kHz	102	100 μs
100 kHz	10	10 μs
1 MHz	1	1 μs

Table 3-3. FUNCTIONS and RESOLUTION Switch Settings

FUNCTION	RESOLUTION, N Switch
FREQ A, FREQ C	Indicates frequency resolution in Hz.
PERIOD A, T.I. A to B	Indicates the factor (N) by which time base is scaled. Maximum resolution occurs with N=1.
PERIOD AVG A, T.I. AVG A to B	Indicates number of time intervals or periods over which the average measurement is made.
DVM	Indicates the integration time (per Table 3-2) for the measurement.
RATIO B/A, RATIO C/A, DVM/A†	Indicates the number of counts at the A input over which the ratio measurement is made. Resolution improves with increasing N.
START, START CLOCK	Indicates scale factor. In START, the frequency at A is scaled by N. In START CLOCK, internal oscillator is scaled by N.
DVM A to B†	Independent of RESOLUTION setting.
DVM/A	Indicates the number of counts at the A input over which the ratio measurement is made. (The DVM circuits are of the V to F type with the factor 10 kHz/volt.) Resolution improves with increased N. Annunciator is not displayed in units (V) but prefix (m, μ , n, etc.) is displayed.

t"Phantom" functions described in Figure 3-8

3-10. INPUT CHANNEL SELECTION

Two separate inputs ("A" and "B") are provided on the Model 5328B's universal module. These two inputs have identical specifications and controls.

The CHK/COM A/SEP switch, located between the two inputs, controls selection of input. When this switch is in its SEP position, the "A" and "B" inputs and controls operate independently of each other for applications in time interval, ratio, or other operations where the signals applied to the "A" and "B" inputs are from different sources. When the CHK/COM A/SEP switch is in its COM A position, the input circuits are connected as described below.

In the COM A position, the output of the Channel B attenuator is disconnected. The output of the Channel A attenuator is routed to the "A" and "B" input amplifiers as shown in Figure 3-3. In COM A, the Channel B AC/DC, 50-ohm/1 Megohm, and X1/X10 ATTENuator relays are disabled. The Channel A AC/DC, 50-ohm/1 Megohm, and "X1/X10" Attenuator settings determine the coupling for the Channel B amplifier (as well as for the Channel A amplifier). The impedance remains one Megohm when in COM A and in the high-impedance position (input capacitance remains <100pF). In the 50-ohm position, the impedance remains 50 ohms for COM A. The sensitivity, dynamic range, and damage levels remain unchanged.

The "A" and "B" input amplifiers have independent LEVEL and SLOPE controls, regardless of the mode of operation (SEP or COM A).

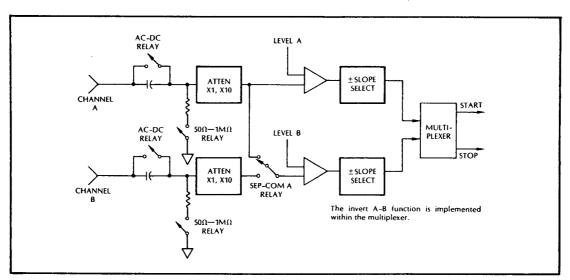


Figure 3-3. Input Switch Configuration for COM A

3-11. SIGNAL CONDITIONING

AC/DC SWITCH. The AC/DC switch controls the coupling of the external signal to the attenuator-amplifier by switching a capacitor in series in the AC position or by direct coupling in the DC position. The obvious advantage of AC coupling is to provide a DC block for signals containing a DC component. AC has the disadvantage of being unable to pass low-frequency (below 20 Hz) signals. A distinct advantage of having DC coupling cover the full bandwidth (DC-100 MHz) is that extremely accurate time interval or pulse measurements can be achieved even though pulse widths or repetition rates vary since the trigger point is independent of the duty cycle of the input signal.

ATTENUATOR. The attenuator (ATTEN) connects the input signal directly to the amplifier (in X1) or through a 10-to-1 attenuator (in X10) this allows the voltage range to be increased by ten times, allowing measurements of high-level signals that would otherwise be impossible without external attenuation.

SLOPE SWITCH. The \pm SLOPE switch (provided for each channel) determines which slope of the input signal will trigger the counter. As a simple example (see *Figure 3-4*), if the pulse width of a positive pulse is to be measured the Channel "A" SLOPE switch would be set to "+" and the Channel "B" SLOPE switch would be set to "-". (For time interval measurements, Channel "A" always begins the measurement and Channel "B" ends the measurement.)

NOTE

A simple pulse width measurement can be made by setting Channel "A"'s SLOPE to "+" and Channel "B"'s SLOPE to "-".

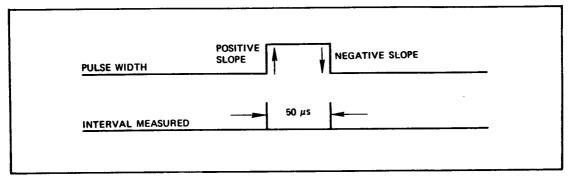


Figure 3-4. SLOPE Switch Settings

LEVEL. Each channel's LEVEL control can be adjusted over a range of ± 2.5 V dc (with the attenuator for that channel in its "X1" position). A typical use of the LEVEL controls is shown in *Figure 3-5*. You can read the triggering level if your Model 5328B includes a Digital Voltmeter module. Trigger level measurements are discussed under DVM measurements in paragraph 3-25.

NOTE

For a simple time interval measurement, the Channel "A" and Channel "B" "+LEVEL" settings are used to set the triggering levels.

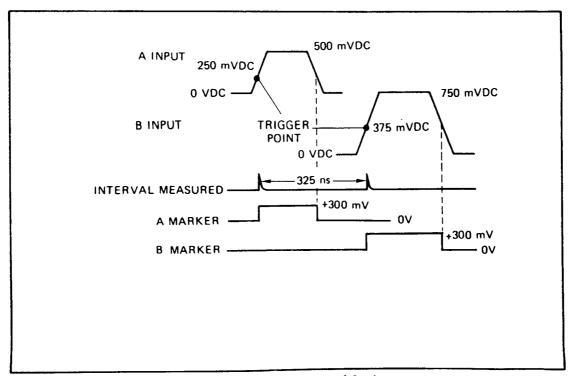


Figure 3-5. LEVEL Control Settings

3-12. HYSTERESIS

The width of the hysteresis band, shown in Figure 3-6, is determined by the sensitivity of the Model 5328B. For frequencies below 40 MHz, it is typically less than 25 mV peak-to-peak. At frequencies from 40 MHz to 100 MHz, it is typically less than 70 mV peak-to-peak. The signal must pass through the entire hysteresis band before a trigger pulse is generated. If the SLOPE switch is set to "+", the trigger pulse occurs at the top of the hysteresis band. If the SLOPE switch is set to "-", the trigger pulse occurs at the bottom of the hysteresis band.

Since trigger level measurements made by the (optional) Digital Voltmeter indicate the center of the hysteresis band (except when hysteresis compensation is in effect — see the "filter switch" paragraph under the "DIGITAL VOLTMETER MEASUREMENTS" heading (paragraph "3-25")), a better value for the actual trigger level may be obtained by subtracting one-half the hysteresis band ("-" slope) or adding one-half the hysteresis band ("+" slope). A typical value for the width of the hysteresis band is 50 mV peak-to-peak.

The value to use for the hysteresis band depends on the frequency; or, for pulses, it depends on the rise time.

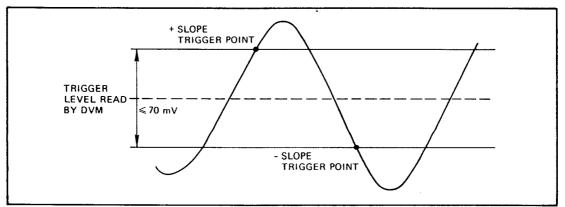


Figure 3-6. Hysteresis Band

3-13. MARKER OUTPUTS

The Model 5328B's front-panel MARKER OUTPUTs provide non-inverted square-wave versions of the input signal for their related channels. The output levels are — "low" = 0V, "high" = +500 mV. These outputs are useful for oscilloscope monitoring.

Time interval measurement setups can be simplified if you use an oscilloscope that allows display of the signal of interest and its associated marker output at the same time.

You can make a frequency measurement on a noisy signal with more confidence, since the markers can indicate the presence of noise triggering.

These outputs are protected from inadvertently applied voltages to $\pm 5V$ dc.

3-14. TRIGGER LIGHTS

One trigger light each is provided for Channel "A" and Channel "B", to let you know if the channel is triggering. This information is also useful, since you can determine how the trigger LEVEL control must be adjusted in order to cause triggering.

For either channel, the TRIGGER light lights when the input signal is above the triggering level; it is off when the input signal is below the triggering level. When the channel is actually triggering, the TRIGGER light for that channel blinks.

The trigger lights are operative over the full frequency range of dc to 100 MHz.

The trigger lights can be used with a 10:1 oscilloscope probe to provide a logic-probe-type function. By adjusting the trigger level to one-tenth (since a 10:1 divider probe is being used) of the threshold voltage for the logic family under investigation (e.g., 0.14V for TTL), the light indicates the logic state of circuit points which are contacted with the probe. When the trigger level light is lighted, the circuit node is a "high" (i.e., above the threshold voltage). If the trigger level light is off, the node is a logic "low". If the light blinks, then pulses (up to 100 MHz rep rate) are present at the node. The trigger lights can also indicate the polarity of low-rep-rate pulses, down to 5 nS pulse width; positive pulses cause the light to blink on while negative pulses cause the light to blink off.

3-15. Rear Panel

3-16. EXTERNAL FREQUENCY STANDARD INPUT

The rear-panel external frequency standard input ("OSC INT EXT") is useful for locking the counter to a high-stability external frequency standard. This external standard must be in the range of 30 kHz to 10 MHz, with an amplitude greater than 1V rms into 1k-ohm (maximum of 5V peak-to-peak). If the frequency is not 10 MHz, the counter operates with the frequency measurements multiplied by the factor "10 MHz/Fext", and time measurements multiplied by the factor "Fext/10 MHz". For example, in a frequency measurement, a 1 MHz external standard would give a 100-second measurement time for the RESOLUTION switch in the "0.1 Hz" position, and would display a 100 kHz input as "1000 kHz", while, in a period measurement (with RESOLUTION set for maximum) a 100 kHz input (10 μ S period) would be displayed as 1 μ S. The measurement time, however, would be unaffected.

For the Model 5328B, the external frequency must be 10 MHz for PERIOD AVG, T.1. AVG A \rightarrow B, PERIOD (N=1), AND T.1. A \rightarrow B (N=1). Otherwise, phase lock will not be achieved, and the time base signal will bear no relationship to the frequency of the external frequency standard.

3-17. GATE OUT

This rear-panel output supplies a TTL-level which is "high" when the counter's main gate is open and "low" when it is closed. Monitoring the GATE OUT with an oscilloscope can provide this information for applications where the markers do not give the desired information.

3-18. TIME BASE OUT

The rear-panel TIME BASE OUT connector supplies a TTL level output when the front-panel FUNCTION switch is set to "START CLOCK" or "START A".

In "START CLOCK", the internal oscillator's 10 MHz is divided by "N", where "N" is determined by the setting of the RESOLUTION switch.

In "START A" the Channel "A" input signal, divided by "N" (where "N" is greater than "1"), is available at the TIME BASE OUT connector.

3-19. EXTERNAL FREQUENCY STANDARD

When the OSC INT EXT switch is in the INT position, the 10 MHz oscillator output is available at the rear-panel INT/EXT connector, at TTL levels. When terminated into 50 ohms, the OSC INT EXT output is a square wave of approximately one volt amplitude.

3-20. APPLICATIONS

This part of this manual section contains information you can use to get the most effective performance from your Model 5328B (and its options). Specific details and examples are provided for making measurements of —

- frequency
- period
- time interval and time interval average
- totalizing
- ratio

Use of the (optional) Digital Voltmeter in making measurements is also included, in addition to use of the external frequency standard input.

Programming information about use of the HP-IB interface is provided beginning at paragraph 3-27.

3-21. Frequency Measurements

To make a frequency measurement on a CW (continuous wave) signal below 100 MHz -

- 1. Set the FUNCTION switch to FREQ A.
- 2. Select the appropriate input signal conditioning (see paragraph 3-11).
- 3. Apply the signal to the Model 5328B's Channel "A" input.

The RESOLUTION switch determines the resolution of the measurement. Since the Model 5328B is a conventional counter, 1 Hz resolution is obtained in one second of measurement time (e.g., 0.1 Hz in ten seconds, etc.). The 0.1 Hz best-case frequency resolution limits the low-frequency measurement accuracy. In practice, low frequencies are measured by making a period or period average measurement and inverting the result to obtain frequency.

To make a frequency measurement on a CW signal in the range of 90 MHz to 1300 MHz (this requires use of the optional "C" Channel) —

- 1. Set the FUNCTION switch to "FREQ C"
- 2. Apply the signal the the Channel C input.

Be sure the signal amplitude is not greater than 5V rms.

The trigger level for the C Channel is fixed at 0V dc. If a pulse waveform is being measured, it must cross through 0V dc by at least 25 mV. Pulse widths down to 1 nS can be counted.

Signals at the Model 5328B's Channel "A" and Channel "B" inputs are counted by the "direct count" method. This method allows greater resolution per second of measurement time than "prescaling" techniques, and is important in making frequency measurements on pulse bursts since the allowable measurement time is fixed (it must be less than the width of the burst).

Signals at the Model 5328B's Channel "C" input are prescaled by a factor of "4".

When the Model 5328B's "FREQ A" or "FREQ C" FUNCTION is chosen, and the rear-panel ARM switch is set to OFF, a measurement cycle is initiated (i.e., the counter is armed) by the the first trigger level crossing the "A" or "C" input. This means that pulsed signals are measured as easily as continuous wave signals if the measurement time (determined by the RESOLUTION switch) is less than the width of the pulse.

With the ARM switch ON, FREQ A and FREQ C are armed by a triggering event at the "B" input. This mode is useful whenever you want to have real time control over when a measurement is to begin. Useful applications include measuring frequency variations along a frequency burst and linearity testing of sweep generators. (Figure 3-7 illustrates the setup for measuring the linearity of a sweep generator.) The Channel "B" Trigger level is adjusted to trigger (and thereby arm) the counter at various points along the sweep out waveform. By plotting the "B" trigger levels (using the Model 5328B's optional Digital Voltmeter) and the corresponding frequency measurements made at those levels, the linearity of the generator can be determined.

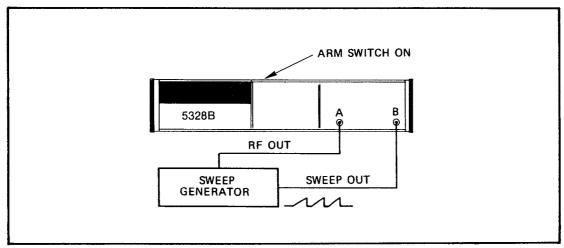


Figure 3-7. Measuring Sweep Generator Linearity

3-22. Period Measurements

The PERIOD and PERIOD AVG functions allow single-period measurements or multiple-period averages to be made on signals at the Channel "A" input, for frequencies up to 10 MHz. These modes are useful for making low-frequency masurements where maximum resolution is desired.

To make a PERIOD or PERIOD AVG measurement —

- 1. Set the FUNCTION switch to the desired function.
- 2. Select the appropriate input signal conditioning (see paragraph 3-11).
- 3. Apply the signal to the Channel "A" input.

For single-period measurements, the RESOLUTION switch scales the time base frequency which determines the resolution of the measurement. For optimum resolution, select "N=1". Other "N" values may be desirable to prevent display overflow or to get rid of unstable digits. For PERIOD AVG measurements, the RESOLUTION switch selects the number of periods over which the period average measurement is to be made (the time base is 10 MHz for this case). The PERIOD AVG mode gives increased resolution and accuracy. Trigger error is decreased by "N" and the resolution is increased by "N" (resolution = "100 nS/N" or "10 nS/N"). The measurement time is equal to the period-times-"N".

In PERIOD and PERIOD AVG, with the rear-panel ARM switch OFF, the measurement cycle is initiated by the SAMPLE RATE control and the input signal. With the ARM switch ON, PERIOD and PERIOD AVG are armed by a trigger event at the Model 5328B's Channel "B" input. To measure the frequency of a tone burst signal, use arming and the PERIOD AVG (for increased resolution over a low-frequency measurement) as shown in *Figure 3-8*. Select "N" equal to or less than the number of periods in the tone burst and adjust Channel "B"'s trigger LEVEL to trigger on the first cycle of the input signal.

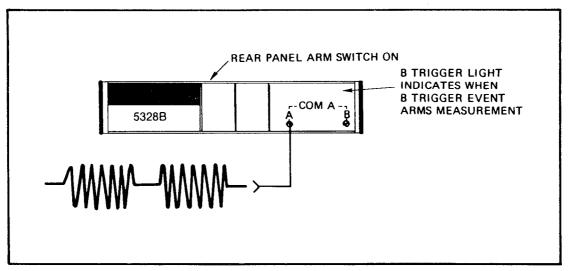


Figure 3-8. Tone Burst Measurement

3-23. Time Interval Measurements

One of two time-interval functions can be selected — time interval or time interval average. These functions measure the time interval between a START signal at the Channel "A" input and a STOP signal at the Channel "B" input. If both the START and STOP signals are to be derived from the same signal, place the CHK/COM A/SEP switch in the COM A position. Separate slope and level controls for each channel allow variable triggering on either positive- or negative-going slope.

In single-shot time-interval measurements, Channel "A" opens the main gate and Channel "B" closes it. While the main gate is open, 100 MHz is divided by the setting of the RESOLUTION switch and totalized by the counter. For optimum resolution, select "N=1". Other "N" values can be chosen to prevent display overflow (e.g., long time intervals) or to get rid of unstable digits. In time interval average measurements, the main gate is open for the number of time intervals selected by the RESOLUTION switch. The Model 5328B's 100 MHz clock is totalized only during the individual time intervals. The resolution of the measurement is improved by the square-root of "N".

In order to allow the synchronizers time to reset during time interval averaging there must be at least 40 nS of dead time (corresponding to a repetition rate of 25 MHz or less), and the additional constraint that the event and the current time intervals start event as shown in *Figure 3-9*.

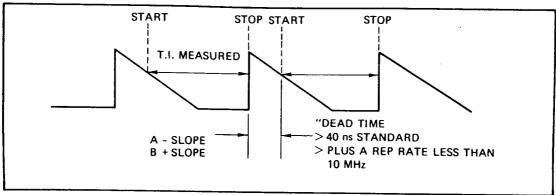


Figure 3-9. Time Interval Measurements — Dead Time

During a time interval average, there must be only one STOP pulse for each START pulse. Any extra STOP pulse which occurs before the next START pulse is accumulated and gives an erroneous reading. For example, the case illustrated in *Figure 3-10* would result in a reading equal to one-half the desired time interval.

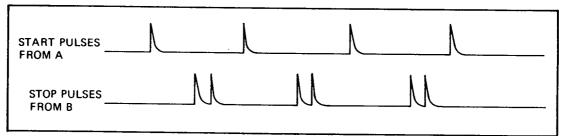


Figure 3-10. Time Interval Measurements — Multiple STOP Pulses

To set up a time interval measurement, the marker outputs can be monitored on an oscilloscope (see Figure 3-11) to indicate where the channels are triggering with relation to the time interval of interest. Voltage levels sent to the (optional) Digital Voltmeter indicate trigger levels for ease in measurement set-up.

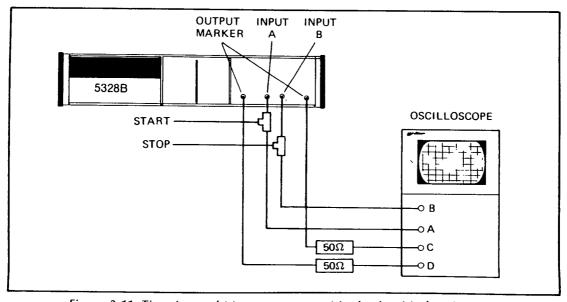


Figure 3-11. Time Interval Measurements — Monitoring Marker Outputs

In T.I. $A \rightarrow B$ and T.I. AVG $A \rightarrow B$, with the rear-panel ARM switch "OFF", the counter is armed by the run down of the SAMPLE RATE control.

With the rear-panel ARM switch "ON", T.I. $A \rightarrow B$ and T.I. AVG $A \rightarrow B$ are armed by an event at the (optional) Channel "C" input.

For T.I. AVG A B, only one armed signal is required per average measurement (i.e., the counter doesn't need to be armed before each individual time interval in the time interval measurement.)

3-24. Totalizing Measurements

The only totalizing function available is START A. In this FUNCTION, the number of counts at the Channel "A" input are totalized if "N=1". If "N" is greater than "1", "A/N" is totalized, and the scaled output is available at the rear-panel TIME BASE OUT connector.

3-25. Digital Voltmeter Measurements

NOTE

The Digital Voltmeter is not a standard part of the Model 5328B; it is available as Option 021 or a part of Option 050.

Three modes of Digital Voltmeter (DVM) measurements are available — "DVM", "READ A" or "READ B". When a mode is selected (by pressing the appropriate pushbutton on the DVM module's front panel), the Digital Voltmeter measures the trigger level of the Channel "A" or Channel "B" input, regardless of the 5328B's FUNCTION switch setting. The "V" annunciator lights to indicate that a voltage measurement is being made. To return control to the FUNCTION selector, the trigger level pushbutton must be released (by pressing it). When the FUNCTION selector is in its "DVM" position, an external voltage applied to the Digital Voltmeter's input can be measured in a 10V, 100V, or 1000V range, or AUTO-ranging can be selected.

A filter switch allows a filter to be switched into the input path to allow the measurement of very small dc levels in the presence of high common-mode or normal-mode noise.

The trigger level voltage read by the Digital Voltmeter when "READ A" or "READ B" is pressed is a direct indication of the voltage of the center of the hysteresis band for the indicated input channel; the setting of that channel's input attenuator is automatically taken into consideration.

The Digital Voltmeter has three ranges —

 $\pm 10V$ ($\pm 12.5V$ overrrange)

 $\pm 100V$ ($\pm 125V$ overrange)

±1000V dc.

Allowable measurement times and corresponding sensitivity are shown in the chart below —

Maximum Sensitivity	Resolution N	Measurement Time
100 mV	1 kHz, 10 ³	1 mS
10 mV	100 Hz, 104	10 mS
1 mV	10 Hz, 105	100 mS
100 uV	1 Hz, 106	1 \$
10 uV	0.1 Hz, 10 ⁷	10 S



The Digital Voltmeter has a "floating" input; this allows differential voltage measurements to be made. Internal isolators and a floating power supply allow the Digital Voltmeter's output to be sent via the HP-IB. Maximum allowable voltage between the DVM's "high" and "low" inputs is 1100V dc (the "high" input must be more positive than the "low"; maximum allowable voltage between the DVM's "low" input and chassis ground is ±500V dc.

With the rear-panel ARM switch "ON", a DVM measurement is begun by an event at the Channel "B"input. This ability to "command" a DVM mesurement can be particularly useful when measuring switching dc levels, such as those from dc supplies or digital-to-analog converters operating in computer-operated test systems. By adjusting Channel "B" to trigger on the transition from one voltage to the next, the DVM can be made to make a measurement only during the time that the voltage level is stable.

Figure 3-12 shows an example of an "armed" DVM measurement. To make such a measurement -

- 1. Select a measurement time that is less than the width of the step to be measured.
- 2. Adjust the Channel "B" trigger LEVEL to trigger on the leading edge of the step.

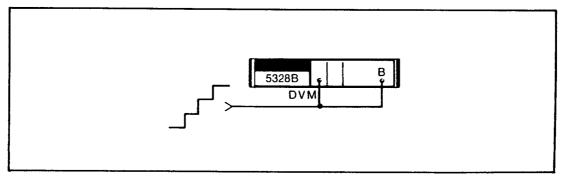


Figure 3-12. Digital Voltmeter Measurement — Armed

3-26. Ratio Measurements

The Model 5328B offers full bandwidth, sensitivity, and signal conditioning of the Channel "A", "B" and "C" input amplifiers.

The standard Model 5328B offers one ratio function ("B/A"); a Model 5328B with the "C" Channel option also offers a "C/A" ratio function.

The ratio of the frequency at the Channel "B" (or, optionally, the Channel "C") input to the frequency at the Channel "A" input is measured for "N" counts of the Channel "A" input, where "N" is selected by the RESOLUTION switch. The resolution of the measurement improves as "N" gets larger, and is given by "1 part in $B/A \times N$ " (or "C/A $\times N$ "). Since the range of "A" is 0 to 10 MHz while "B" is 0 to 100 MHz, the lower frequency is normally applied to the "A" input, although there is no restriction that this be the case (i.e., ratios less than "1" can be measured). If "B/A" is greater than "1", the measurement resolution is better than switching the inputs for a ratio less than "1", provided the value of "N" remains the same.

3-27. PROGRAMMING (USING HP-IB)

The Model 5328B is fully compatible with the Hewlett-Packard Interface Bus (HP-IB). The counter can respond to remote control instructions and can output measurement results via the HP-IB. Thus, the Model 5328B gives all the benefits of standardized systems operation with the HP-IB. At the simplest level, the Model 5328B can output data to other devices such as the HP 5150A

Thermal Printer or the HP 59303A Digital-to-Analog Converter. In more-sophisticated systems, the calculator or other system controller can remotely program the Model 5328B, trigger measurements, and read the results. Of course, a calculator or computer adds other benefits to an HP-IB-based measurement system. The calculator can manipulate data to compute the mean and standard deviation, check for linearity, compare results to limits, or perform many other functions. The Model 5328B has fully-programmable input signal conditioning control and extended measurement capability.

The paragraphs below describe how to use the HP Calculators to program the Model 5328B. Before starting to operate a system, you should familiarize yourself with the selected calculator, the capabilities of the HP-IB, and the manual operation and capabilities of the Model 5328B counter. The following HP manuals provide very useful background information —

HP 9825A Operating and Programing Manual HP 9825A General and Extended I/O Manuals HP-IB Programming Hints for Selected Instruments

Each of these manuals is included with the appropriate hardware. The HP 9825A manuals are included with the I/O cards that interface the calculators to the HP-IB.

3-28. Setting Address Switches

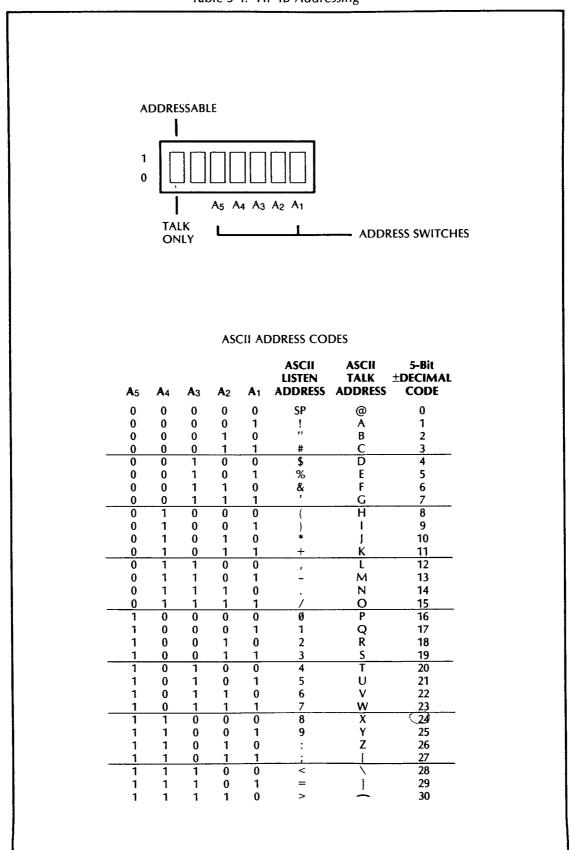
To use the Model 5328B in an HP-IB-based system, the first step is to set the rear-panel address switches shown in *Table 3-4*. The left-most switch sets the counter to "ADDRESSABLE" or "TALK ONLY" mode. "ADDRESSABLE" mode is used whenever a calculator or other controller is used within the system. "TALK ONLY" mode is used when the counter will be controlled manually, but will output its results to another device (such as a printer or digital-to-analog converter) on the bus.

The five right-hand switches, "A5" through "A1", set the "TALK" and "LISTEN" addresses of the Model 5328B when it is used in the "ADDRESSABLE" mode. *Table 3-4* shows the possible address settings and the corresponding ASCII codes for "TALK" and "LISTEN" addresses.

The examples listed in this section assume an address setting of "11001" (decimal "25").

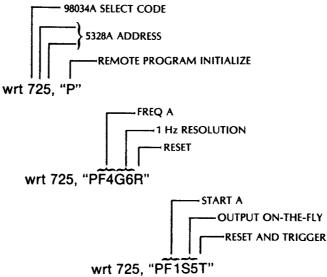
NOTES

- 1. The address setting "11111" (decimal "31") should not be used, since it is reserved for the "UNTALK" or "UNLISTEN" commands.
- 2. The setting "10101" (decimal "21") should not be used, since it is reserved for the HP 98034A Interface Card.

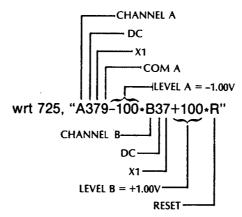


3-29. PROGRAMMING THE MODEL 5328B

The HP 9825A Calculator uses the "cmd" and "wrt" statements to program the Model 5328B. The calculator addresses itself to "TALK" and the counter to "LISTEN", then sends a series of program codes. The codes to control the operation of the Model 5328B are listed in the program code set (see *Table 3-5*). Suppose you have set the counter to address "25" (decimal); typical statements to program the counter are —



The Model 5328B also allows program control of the Channel "A" and "B" input signal conditioning controls. The program codes are from the 19 groups of the Program Code Set (see *Table 3-5*). No more than one code from a group should be used in a program code string; use the codes in the same order in which the groups are listed.



The code "P" gives a single way to initialize the state of the counter program. "P" substitutes for -

"FØGØSØ246AØ2468<+ØØØ*BØ2468+ØØØ*"

The Program Code Set (see Table 3-5) shows these states in **bold** face. When other codes are included in a program code string, either "R" or "T" should be included at the end of the string. Both of these codes update the counter's program storage cells. The "T" also initiates a measurement; "R" does not. In a string of codes, the prefix letter of a two-character code need not be repeated. The prefix letter can be used just once at the beginning of a sequence of codes –

"S1S3S4S6"→"S1346"
"A1A7A9A123*"→"A179+123"



Codes shown in **bold** face are start-up conditions. These conditions are set by the code "P", Remote Program Initialize, or by the bus commands Device Clear or Selected Device Clear.

- 1. Initialization
 - Remote Program Initialize
- Function

FØ	Stop	F8	T.I. A→B
F1	Start A	F9	Ratio B/A
†F2	Start Clock	F:	T.I. Avg. A-E
tF3	DVM/A	F<	Check
F4	Freq. A	F=	Ratio C/A
†F5	DVM/T.I. A→B	F>	Freg. C
F6	Period A	F?	DVM
F7	Per. Avg. A		

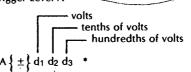
3. Time Base

Code	Freq Res	Multiplier	Time Res (Std)	Time Res (Opt. 040 and 041)
Gø	1 MHz	1	100 ns	10 ns
G1	100 kHz	10 ,	1 μs	100 ns
G2	10 kHz	102	10 μs	1 μs
G3	1 kHz	103	100 μs	10 μs
G4	100 Hz	104	1 ms	100 μs
G5	10 Hz	105	10 ms	1 ms
G6	1 Hź	106	100 ms	10 ms
G7	0.1 Hz	107	1 s	100 ms

- Single-Multiple Measurement
 - Single measurement. Hold sample rate.
 - Trigger required.
 - Multiple measurement. Not Hold. No trigger **S1** required.
- Measurement Cycle
 - Hold measurement until address to output. SRQ at end of measurement.
 - 53 Output only if a device is ready to accept data when 5328A) is ready to output.
- 6. Output Mode
- Output at end of measurement most universal.
 - · S5 Output during open gate - usable only in START/STOP mode or if the decade reset is disabled.
- Sample Rate
 - Maximum
 - Manual control (from front panel)
- **Arming**
 - Off
 - On
- **Display Storage**
 - On (normal) Off
- 10. Decade Reset

 - Normal
 - Disabled (for cumulative measurements)
- 11. Display Blanking
 - Normal Display
 - Q Blank display (digits and decimal point)

- 12. Channel A Signal Conditioning
 - Impedance
 - ΑØ 1 Megohm
 - Α1 50 Ohms
 - Coupling
 - A2
 - **A3** DC
 - Slope
 - A4 +slope **A5** -slope
 - Attenuator
 - **A6** X10 ·
- 13. Separate/Common
 - **A8** Separate
 - A9 Common A
- 14. Check
 - Normal Operation
 - A?/-Check, Measures internal clock
- 15. Trigger Level A



Permissible trigger level range: -2.50V to +2.50V.

The program sequence to set trigger level starts with the channel designation letter followed by a "+" or "-" sign. Next, three digits set the voltage level. An "*" terminates the sequence. The same sequence must be used even to set Ø volts:†

> Examples: "A+000*" Ø volts "A-123*" -1.23 volts

- 16. Channel B Signal Conditioning
 - a. Impedance
 - 1 Megohm BØ
 - **B1** 50 ohms
 - Coupling
 - **B2** AC
 - DC -B3

 - Slope
 - +slope
 - **B5** -slope
 - Attenuator
 - **B**6 X10
 - ·B7 X1
- 17. Trigger Level B
 - $B \left\{ \pm \right\} d_1 d_2 d_3$
 - See Group 15, Trigger Leval A, for details
- 18. Channel Invert
 - RR Normal
 - **B9** Invert A and B inputs
- 19. Reset; Trigger
 - (Also see Bus Command GET)
 - Reset, no trigger
 - Reset and trigger

†Functions not labeled on instrument front panel.

Trigger levels set to 0 volts when counter is initialized with code "P" or bus commands DCL and SDC.

3-30. Triggering A Measurement

The calculator must trigger each measurement when the Model 5328B is programmed for single-measurement mode, "S0". Two trigger measurements are available; the program code "T" offers the simplest way to trigger a measurement. However, the bus command "Selective Device Trigger" ("SDT") may also be used. The counter responds more quickly to SDT; the "Group Execute Trigger" ("GET") can trigger the counter simultaneously with other devices on the bus.

Using the program code "T": wrt 725, "T" (9825A)

Using the Selective Device Trigger: trg 725

Using Group Execute Trigger: trg 7

If the Model 5328B is programmed for multiple measurements, "S1", it will start each measurement without a trigger command (see Figure 3-13). When the Model 5328B is also programmed to wait to output, "S2", it will start a new measurement as soon as the calculator reads the previous one. In the continuous cycle mode, "S3", the Model 5328B does not wait to output, but starts a new measurement. This mode is useful in allowing you to visually monitor a series of readings.

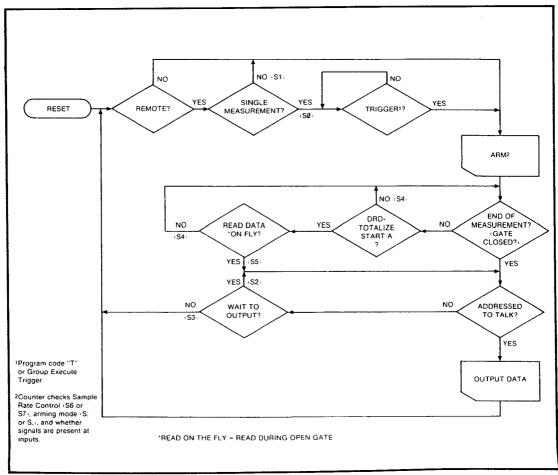


Figure 3-13. Measurement and Output Flow Chart

3-31. Reading A Measurement

To read a measurement from the Model 5328B to a calculator, the Model 5328B must first be addressed to "TALK" and the calculator to "LISTEN". The examples below indicate how a calculator may read a measurement from the counter.

0: wrt 725, "PF4G5R" 1: wrt 725, "T" 2: red 725,A 3: dsp A 4: end *25861

Note that the data output string from the Model 5328B may include a leading "O" when a counter measurement overflows. The Model 9825A example above ignores all leading non-numeric characters, and won't give any indication of an overflow. The program below stores the measurement into a string variable. The string variable loads all characters (alpha and numeric), thus overflow "O" will be displayed. Line "4" takes the value of "A" and uses it for comparison in line "5".

0: dim A [17]
1: wrt 725, "PF4G5R"
2: wrt 725, "%"
3: red 725, A\$
4: val (A\$)→A
5: dsp A\$, A
6: end
*14192

In these examples, string variable "A\$" contains all output characters, while simple variable "A" contains only the numeric data. If the display has not overflowed, a space ("SP") will replace the "O".

3-32. PROGRAMMING EXAMPLE

The program below illustrates how the HP 9825A calculator can control the Model 5328B counter. The program causes the Model 5328B to make a series of frequency measurements. The Model 5328B reads the measurements into memory and the results are printed.

Program	Explanation
1: rem 7	Dimension ten storage locations for "A" Set HP-IB to enable remote state Program counter to frequency measurement, 1 Hz resolution
5: red 725,A[I]	
8: end *11062	

3-33. MEASUREMENT OUTPUT FORMAT

The Model 5328B sends the following character string to output a measurement

Position	1	2	3 through 12	13	14	15	16	17
Character	(0)	\ +\	9 digits and decimal point .	F	(+)	d	CR	I F
Character	SP)	_	decimal point .	L	(-)	u	Cit	

"O" in the first position indicates measurement overflow. Leading "O"'s in positions "3" to "12" are output as "SP" (space) if they occur to the left of the decimal point, except for the "O" next to the decimal point ("0.x"). The decimal point may appear at any position from "4" to "12". The output string is **always** 17 characters long. Typical character output strings are —

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
SP	+	5	Ø	3		2	1	7	6	9	8 -	E	+	6	CR	LF ~
SP	+	SP	SP	SP	5	4	3	2	1	Ø		£	-	3	CR	LF
Ø	+	Ø	5	3	1		8	5	4	2	Ø	Ε	+	6	CR	LF

The Model 5328B inserts a "0" in position "12" of the output string for all measurements that don't use the ninth digit of the display. This extra "0" fills the output string to the standard 17 characters.

3-34. BUS COMMANDS

The Model 5328B obeys the following bus commands (ASCII codes shown in parentheses and in *Table 3-6*).

a. Unaddress Commands

UNL UNListen (ASCII question mark — "?")
Clears the counter from acting as a listener.

UNT UNTALK (ASCII underscore — "_ "

Clears the counter from acting as a talker. The same result can be achieved by addressing any other device on the bus to talk.

b. Universal Commands

LLO Local LockOut (ASCII "DC1")

Disables all programmable front-panel control including RESET. Go To Local (GTL) must be programmed to return to manual control.

DCL Device CLear (ASCII "DC4")

Resets the programmed state of the counter to the codes shown in **bold** face in the program code set. Has the same effect as the program code "P".

SPE Serial Poll Enable (ASCII "CAN")

Sets the counter to the Serial Poll mode. When addressed to talk during the serial poll mode, the Model 5328B produces a status byte to indicate its condition. If the Model 5328B has completed a measurement, and is requesting service, the status byte contains a "1" in bit "7" (decimal value "64"). If the Model 5328B has not requested service, the status byte will be "0" in all bits. When addressed to "talk" in the Serial Poll Mode, the Model 5328B will immediately stop requesting service.

SPD Serial Poll Disable (ASCII "EM")

Terminates the Serial poll mode. The Model 5328B can resume its normal data output mode.

c. Addressed Commands

SullP

GTL Go To Local (ASCII "SOH")

Returns the Model 5328B to local (manual) control from remote (HP-IB) control.

SDC Selected Device Clear (ASCII "EOT")

Responds as with "Device Clear" or program code "P".

GÉT Group Execute Trigger (ASCII "BS")

Starts a measurement. This command provides the quickest method to start a measurement cycle.

Table 3-6. American Standard Code for Information Interchange (ASCII)

BITS		TS		BITS		BITS		b ₇	000	⁰ ₀ ₁	010	011	¹ 00	¹ ₀ ₁	¹ ₁ ₀	1 1
b ₄	b ₃	b ₂	b ₁	COLUMN ♦	0	1	2	3	4	5	6	7				
0	0	0	0	0	NUL	DLE	SP (blank)	0	@	Р	`	р				
0	0	0	1	1	SOH	DC1	!	1	Α	Q	a	q				
0	0	1	0	2	STX	DC2	,,	2	В	R	b	r				
0	0	1	1	3	ETX	DC3	#	3	С	S	С	s				
0	1	0	0	4	EOT	DC4	\$	4	D	Т	đ	t				
0	1	0	1	5	ENQ	NAK	%	5	E	U	e	u				
0	1	1	0	6	ACK	SYN	&	6	F	V	f	v				
0	1	1	1	7	BEL	ЕТВ	,	7	G	w	g	w				
1	0	0	0	8	BS	CAN	(8	н	Х	h	×				
1	0	0	1	9	нт	EM)	9	1	Y	i	У				
1	0	1	0	10	LF	SUB	*	;	J	Z	j	z				
1	0	1	1	11	VT	ESC	+	;	К	[k	{				
1	1	0	0	12	FF	FS	,	<	L	\	1	:				
1	1	0	1	13	CR	GS	_	=	М]	m	}				
1	1	1	0	14	so	RS		>	N	1	n	_~				
1	1	1	1	15	SI	US	/	?	0		0	DE				
				•	UNIVE ADDRI COMM	ESS ANDS	1	UNLISTE COMMAN	ID TALK ADDR	UNTALE COMMA ESSES						

3-35. HP INTERFACE BUS DESCRIPTION

The Hewlett-Packard Interface Bus (HP-IB) transfers data and commands between the components of an instrumentation system on 16 signal lines. The interface functions for each system component are performed within the component so only passive cabling is needed to connect the system. The cables connect all instruments, controllers, and other components of the system in parallel to the signal lines.

Eight of the lines ("DIO1" through "DIO8") are reserved for the transfer of data and other messages in a byte-serial, bit-parallel manner. Data and message transfer is asynchronous, coordinated by the three handshake lines ("DAV", "NRFD", and "NDAC"). The other five lines are for control of bus activity.

Devices connected to the bus may be "talkers", "listeners", or "controllers". The "controller" dictates the role of each of the other devices by setting the "ATN" (attention) line "low" and sending talk or listen addresses on the data lines (DIO1 through DIO8). Addresses are set into each device at the time of system configuration either by switches built into the device or by jumpers on a printed circuit board. While the ATN line is "low", all devices must listen to the data lines. When the ATN line is "high", only devices that have been addressed will actively send or receive data. All others ignore the data lines.

Several listeners can be active simultaneously, but only "talker" can be active at a time. Whenever a talk address is put on the data lines (while ATN is "low"), all other talkers will be automatically unaddressed.

Information is transmitted on the data lines under sequential control of the three handshake lines. No step in the sequence can be initiated until the previous step is completed. Information transfer can proceed as fast as devices can respond, but no faster than allowed by the slowest device presently addressed as active. This permits several devices to receive the same message byte concurrently.

The ATN line is one of the five control lines. When ATN is "low", addresses and universal commands are sent on only seven of the data lines using the ASCII (American Standard Code for Information Interchange) code. When ATN is "high", any code of eight or fewer bits understood by both talker and listener(s) may be used.

The other control lines are "IFC", "REN", "SRQ", "EOI". "IFC" (interface clear) places the interface system in a known quiescent state. "REN" (Remote ENable) is used with other coded messages to select either local or remote control of each device.

Any active device can set the SRQ (Service ReQuest) line "low". This indicates to the controller that some device on the bus (say the Model 5328B) had just completed some operation (such as making a measurement) and wants attention (such as use of the bus to send its output to a printer).

"EOI" (End Or Identify) is used by a device to indicate the end of a multiple-byte transfer sequence. When a controller sets both the ATN and EOI lines low, each device capable of a parallel poll indicates its current status on the DIO line assigned to it.

For a more-detailed description of bus operation, refer to the manual entitled "Condensed Description of the Hewlett-Packard Interface Bus", HP Part No. 59401-90030.

3-36. HP-IB INTERFACE OPERATION

The HP 5328B's HP-IB interface is used to remotely program the Model 5328B and deliver the measurement results to the bus. Thus, the HP-IB interface operates as both a "listener" and a "talker". As a "listener", the Model 5328B's HP-IB interface can be used to program most of the counter's mainframe controls and all of its programmable functions. The HP-IB board contains storage circuits to control the mainframe remotely, and is set up to program the storage circuits in any programmable module.

As a "talker", the Model 5328B's HP-IB interface can output measurement data in scientific (i.e., number-plus-exponent) format, with a mantissa of nine digits (whose leading zeroes are output as spaces) and a one-digit exponent. Overflow and sign information are also contained, along with a carriage return-line feed ("CR LF") termination to make it compatible with the standard HP-IB serial data format.

In addition to being a talker and listener, the Model 5328B's HP-IB interface follows a set of HP-IB commands. This includes complete service request capability. The ASCII codes used for addressing and for data are shown in *Table 3-6*. Address switch information is shown in *Table 3-4*. The program code set is shown in *Table 3-5*.

SECTION IV PERFORMANCE TESTS

4-1. INTRODUCTION

This manual section contains two groups of tests you can use to check for proper operation of a Model 5328B Universal Counter.

All tests in either group can be performed without having to remove any cover from the Model 5328B.

The first test group is the "OPERATIONAL VERIFICATION". This test group, which begins at paragraph 4-3, can be used to give a high degree of confidence that the Model 5328B is operating properly, without performing the more-complete "PERFORMANCE TEST" tests. The "OPERATIONAL VERIFICATION" tests should be useful for incoming Quality Assurance, routine maintenance, and after instrument repair.

The second test group is the "PERFORMANCE TESTS". This test group, which begins at paragraph 4-11, actually tests the performance of the Model 5328B against its specifications (given in *Table 1-1* of this manual). The tests in this group are (in general) much more demanding and time-consuming than those in the "OPERATIONAL VERIFICATION" group.

Following each test group is a form on which you can record the results of your tests of the Model 5328B.

4-2. EQUIPMENT REQUIRED

The equipment required for the performance tests is listed in the "Recommended Test Equipment" table in Section 1 of this manual. Any equipment that satisfies the critical specifications given in the table may be substituted for the recommended model(s).





OPERATIONAL VERFICATION

4-3. SELF CHECK

a. Set the Model 5328B's controls as follows -

FUNCTION	CHECK
RESOLUTION	10 kHz,10 ²
SAMPLE RATE	Mid-range

- b. Verify that the Model 5328B displays 10.00 ± 0.01 MHz.
- c. Record the results of your test on the test record card provided.
- d. Set the Model 5328B's controls as follows —

FUNCTION	FREG	QΑ
CHK/COM A/SEP		CHK

- e. Verify that the Model 5328B's "CHECK" indicator is lighted, and that its numeric display is 100.00 ± 0.01 MHz.
- f. Record the results of your test on the operational verification record provided.

OPERATIONAL VERIFICATION (Continued)

4-4. 0-100 MHz SENSITIVITY — CHANNEL "A"

a. Set up equipment for this test as shown in Figure 4-1.

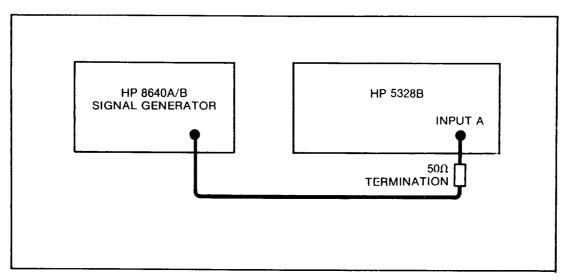


Figure 4-1. Channel "A" Test Setup

b. Set the Model 5328B's controls as follows —

FUNCTION FREQ A
RESOLUTION 10 kHz,102
CHANNEL "A" LEVEL PRESET
ATTEN A X1
CHK/COM A/SEP SEP
COUPLING A AC
INPUT IMPEDANCE 1 MEGOHM

- c. Set the Signal Generator for an output of 40 MHz at 25 mV rms.
- d. Verify that the Model 5328B displays approximately 40.00 MHz, and that its Channel "A" trigger indicator is blinking.
- e. Increase the Signal Generator's output frequency and signal level to 100 MHz at 50 mV rms.
- f. Verify that the Model 5328B displays approximately 100.00 MHz, and that its Channel "A" trigger indicator is blinking.
- g. Record the results of your tests on the operational verification record card provided.
- h. Set the Model 5328B's Channel "A" input impedance to 50 ohms and remove the 50-ohm termination at its input.
- i. Repeat step "c" through "g" above.



OPERATIONAL VERIFICATION (Continued)

4-5. 0-100 MHz SENSITIVITY — CHANNEL "B"

a. Set up equipment for this test as shown in Figure 4-2.

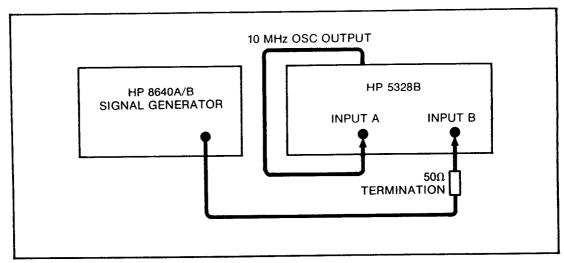


Figure 4-2. Channel "B" Test Setup

b. Set the Model 5328B's controls as follows —

FUNCTION RATIO B/A
RESOLUTION 10 kHz,102
CHANNEL "A" LEVEL PRESET
CHANNEL "B" LEVEL PRESET
ATTEN (both channels) X1
CHK/COM A/SEP SEP
COUPLING (both channels) AC
INPUT IMPEDANCE (both channels) 1 MEGOHM

- c. Set the Signal Generator for an output of 40 MHz at 25 mV rms.
- d. Verify that the Model 5328B displays approximately "4.00", and that its Channel "A" and Channel "B" trigger indicators are blinking.
- e. Increase the Signal Generator's output frequency and signal level to 100 MHz at 50 mV rms.
- f. Verify that the Model 5328B displays approximately "10.00", and that its Channel "A" and Channel "B" trigger indicators are blinking.
- g. Record the results of your test on the operational verification record card provided.

OPERATIONAL VERIFICATION (Continued)

4-6. PERIOD MEASUREMENTS

a. Set up equipment for this test as shown in Figure 4-3.

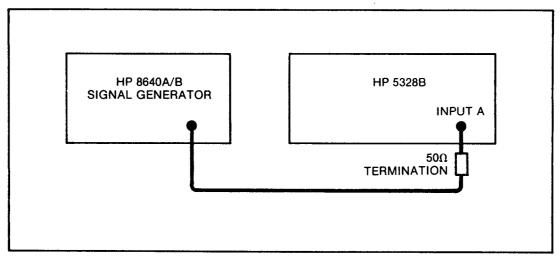


Figure 4-3. Period — Test Setup

b. Set the Model 5328B's controls as follows -

FUNCTION PE	RA
RESOLUTION 1 MF	tz,1
LEVEL A PRE	SET
COUPLING A	AC
ATTEN A	X1
INPUT IMPEDANCE 1 MEGO	НМ
CHK/COM A/SEP	

- c. Set the Signal Generator for an output of 10 MHz at 50 mV rms.
- d. The Model 5328B should display approximately 0.10 μ S (the period of a 10 MHz signal).
- e. Record the results of your test on the operational verification record card provided.

4-7. TIME INTERVAL MEASUREMENTS

- a. Set up the equipment for this test as shown in Figure 4-3.
- b. Set the Signal Generator for an output of 5 MHz at 50 mV rms.
- c. Set the Model 5328B's controls as follows -

FUNCTION	T.I. A→B
RESOLUTION	1 MHz, 1
LEVEL (both channels)	
COUPLING (both channels)	AC
ATTEN (both channels)	X1
INPUT IMPEDANCE (both channels)	1 MEGOHM
CHK/COM A/SEP	COM A
SLOPE (Channel "A")	"+"
SLOPE (Channel "B")	"-"

- d. The Model 5328B should display approximately 0.10 μ S.
- e. Set the Model 5328B to T.I. AVG $A \rightarrow B$.
- f. The Model 5328B should continue to display approximately 0.10 μ S.
- g. Record the results of your test on the operational verification record card provided.

4-8. TOTALIZING MEASUREMENTS

a. Set up the equipment for this test as shown in Figure 4-4.

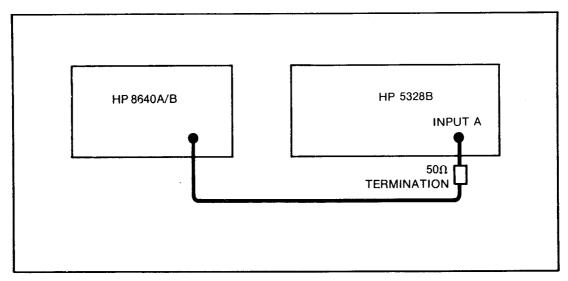


Figure 4-4. Totalizing Test (START/STOP) — Test Setup

b. Set the Model 5328B's controls as follows -

FUNCTION FREQ A
RESOLUTION 0.1 kHz,104
LEVEL A PRESET
ATTEN A X1
COUPLING A AC
CHK/COM A/SEP SEP
INPUT IMPEDANCE
SAMPLE RATE Mid-range

- c. Set the Signal Generator for an output of 10 MHz at 50 mV rms.
- d. The Model 5328B should display approximately 10000.0 kHz, and its Channel "A" trigger indicator should be blinking.
- e. Set the Model 5328B's FUNCTION switch to START A.
- f. Allow the Model 5328B to totalize for 30 seconds, then set its FUNCTION switch to STOP.
- g. The Model 5328B should display approximately 300.00 m.
- h. Record the results of your test on the operational verification record card provided.



4-9. DIGITAL VOLTMETER (OPTION 021 OR OPTION 050)

a. Set the Model 5328B's controls as follows -

FUNCTION	DVM
RESOLUTION	1 Hz, 10 ⁶
LEVEL (both channels)	PRESET
ATTEN (both channels)	X1
RANGE (Digital Voltmeter)	AUTO
FILTER (Digital Voltmeter)	OFF
READ A and READ B (Digital Voltmeter)	out

- b. Short (i.e., connect together) the Digital Voltmeter's input terminals.
- c. Verify that the Model 5328B displays $0.0000V \pm 0.4 \text{ mV}$.
- d. Record the results of your test on the operational verification record card provided.
- e. Disconnect the short at the Digital Voltmeter's input.
- f. Place the Digital Voltmeter in its READ A mode.
- g. Vary the Model 5328B's Channel "A" LEVEL control from fully counter-clockwise to fully clockwise. The Model 5328B's display should change from approximately -3.000V to +3.000V as you do this.
- h. Record the results of your test on the operational verification record card provided.
- i. Make sure the Digital Voltmeter's READ A and READ B buttons are both out.
- j. Short (i.e., connect together) the Digital Voltmeter's input terminals.
- k. Observe the Model 5328B's display as the Digital Voltmeter's RANGE switch is set as indicated in the chart below.

Digital Voltmeter Range	Model 5328B Display	
AUTO	0.0000V ±0.4 mV	
1K	$0.00V \pm 40 \text{ mV}$	
100	$0.000V \pm 4 \text{ mV}$	
10	$0.0000V \pm 0.4 \text{ mV}$	

I. Record the results of your test on the operational verification record card provided.



4-10. CHANNEL C (OPTION 031 OR OPTION 050)

a. Set up equipment for this test as shown in Figure 4-5.

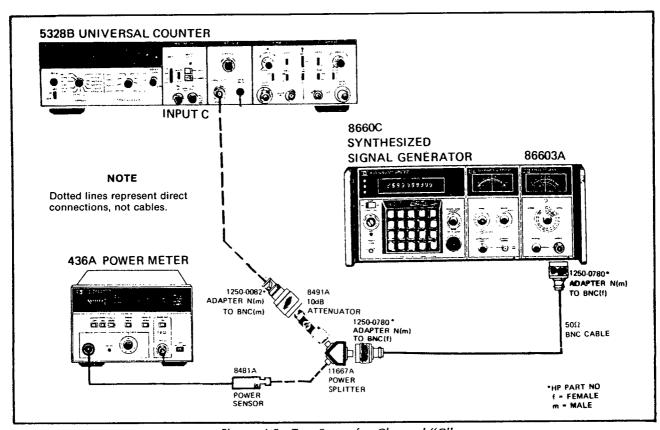


Figure 4-5. Test Setup for Channel "C"

b. Set the Model 5328B's controls as follows -

FUNCTION FREQ	Į C
RESOLUTION 10 kHz,	102
SAMPLE RATE Mid-ran	ige
CHANNEL "C" Attenuator fully clockw	ise

- c. Set the Signal Source to provide -11 dBm at 90 MHz, as measured on a HP 436A Power Meter.
- d. Verify that the Model 5328B counts at 90 MHz.
- e. Set the Signal Source to 300 MHz, 600 MHz, 900 MHz, and 1300 MHz (at -11 dBm at each frequency), and verify that the Model 5328B counts at each frequency.
- f. Record the results of your test on the operational verification record card provided.
- g. RATIO C/A
 - 1) Set up equipment for this test as shown in Figure 4-6.



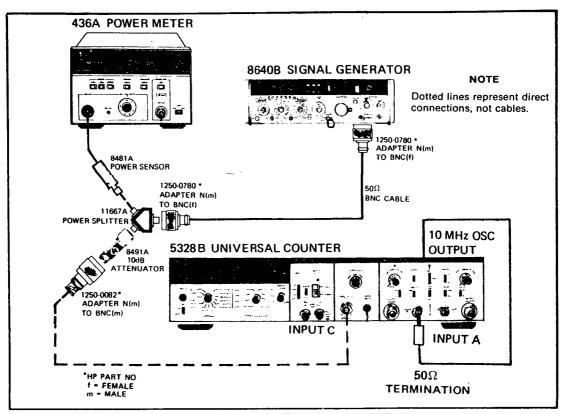


Figure 4-6. Channel "C" RATIO TEST SETUP

2) Set the Model 5328B's controls as follows —

FUNCTION RATIO C/A
RESOLUTION 10 kHz, 102
LEVEL (Channel "A") PRESET
COUPLING (Channel "A") AC
ATTEN (Channel "A") X1
ATTEN (Channel "C") fully clockwise
CHK/COM A/SEP SEP
INPUT IMPEDANCE

- 3) Set the Signal Source such that the Power Meter reads -3 dBm at 100 MHz.
- 4) The Model 5328B's Channel "A" trigger indicator should be blinking and its display should read approximately 10.00. Note, you may have to adjust the SAMPLE RATE setting slightly in order to see the indicator flash.
- 5) Record the results of your tests on the operational verification record card provided.



OPERATIONAL VERIFICATION RECORD

Hewlett-	Packard Model 5328B Universal Counter		
Serial N	umber		
Tests Pe	rformed by		
Date			
Test Ref	Descripiton	Res Pass	ults Fail
4-3	SELF CHECK		
	Check 10.00 ±0.01 MHz		
	Check 100.00 ± 0.01 MHz		
4-4	0-100 MHz INPUT SENSITIVITY – CHANNEL "A"		
	1 Megohm: 40 MHz		
	100 MHz		
	50 ohms: 40 MHz		
	100 MHz		
4-5	0-100 MHz INPUT SENSITIVITY – CHANNEL "B"		
	1 Megohm: 40 MHz		
	100 MHz		
4-6	PERIOD MEASUREMENTS		
	PER A 0.10 μS		
4-7	TIME INTERVAL MEASUREMENTS		
	T.I. A → B 0.10 μS		

T.I. AVG A \rightarrow B - 0.10 μ S

Test		Results	
Ref	Description	Pass	Fail
4-8	TOTALIZING MEASUREMENTS		
	START A/STOP		
4-9	DIGITAL VOLTMETER (OPTION 021 OR OPTION 050)		
	Short Connector = $0.0000 \pm 0.4 \text{ mV}$		
	Read A = $(\approx -3.000V \text{ to } \approx +3.000V)$		
	$AUTO = 0.0000V \pm 0.4 \text{ mV}$		
	$1K = 0.00V \pm 40 \text{ mV}$		
	$100 = 0.000V \pm 4 \text{ mV}$		
	$10 = 0.0000V \pm 0.4 \text{ mV}$. 30.0170	
4-10	CHANNEL C (OPTION 031 OR OPTION 050)		
	90 MHz		
	300 MHz		
	600 MHz		
	900 MHz		
	1300 MHz		
	RATIO C/A		







PERFORMANCE TEST

4-11. SELF CHECK

a. Set the Model 5328B's controls as follows -

FUNCTION	CHECK
SAMPLE RATE cou	inter-clockwise
OSC	INT
ARM ON/OFF (rear-panel)	OFF
STORAGE ON/OFF (rear-panel)	ON

b. Vary the Model 5328B's RESOLUTION switch from 1 MHz ("1") to 0.1 Hz (107) while observing the display.

Resolution	Display (NOTE 1)	
1 MHz (1)	10. MHz	
0.1 MHz (10)	10.0 MHz	
10 kHz (102)	10.00 MHz	
1 kHz (103)	10.000 MHz	
0.1 kHz (104)	10.0000 MHz	
10 Hz (10 ⁵)	10.00000 MHz	
1 Hz (106)	10.000000 MHz	
.1 Hz (10 ⁷)	OVFL 0.0000000 MHz (NOTE 2)	

NOTES:

- 1. ±1 count
- 2. After ten seconds
- c. Push and hold the Model 5328B's RESET switch.

The Model 5328B's display should show "8.8.8.8.8.8.8.8.", with all annunciators except OVFL, RMT, and GATE lighted.

- d. Set the Model 5328B's RESOLUTION control to 1 MHz (1).
- e. SAMPLE RATE and GATE light check.

Observe the flashing of the Model 5328B's GATE light as you rotate its SAMPLE RATE control between its fully counter-clockwise and fully clockwise positions.

When the SAMPLE RATE control is in its fully counter-clockwise position, gating will be so fast that the GATE indicator will appear to be lighted continuously. The gating frequency will decrease as the SAMPLE RATE control is turned clockwise. When the control is in its fully clockwise position, no gating will occur (i.e., the counter will be in "HOLD").

- f. FUNCTION control.
 - 1) Set the Model 5328B's RESOLUTION switch to 1 Hz (106).
 - 2) Observe the Model 5328B's display as you set its FUNCTION switch to the positions listed in the chart below.



	Display		
Function	With No Options Installed	With Indicated Option Installed (See Note)	
CHECK	10.000000 MHz ±1 count		
FREQ C	0.00 kHz	OPT. 031, 0.000 kHz	
DVM	0.0000V	OPT. 021, 0.0000V ±0.4 mV (See Note 2)	
START A	0. M GATE light ON		
STOP	0. M GATE light OFF		
FREQ A	0.000 kHz		
PER A	0.00 s		
PER AVG A	0.00000 ns		
RATIO B/A	0.000000		
T.I. A→B	0.00 s		
T.I. AVG A→B	0.00000 ns		
RATIO C/A	0.00000 OPT. 031, 0.0000000		

NOTES:

- 1. "OPT "021" or "OPT 031" also refer to "OPT 050", which is the combination of these two options.
- 2. When checking the DVM function in a HP 5328A in which the digital voltmeter has been installed, connect (short) the DVM's input terminals together and set the DVM's range switch to "AUTO".
 - 3) Set the Model 5328B's controls as follows —

CHK/COM A/SEP	 	 CHK
FUNCTION		
RESOLUTION	 	 10 kHz, (10 ²)

The Model 5328B's display should show "100.00 MHz" ± 0.01 MHz.

4) Record the results of your test on the performance test record provided.





4-12. FREQUENCY RESPONSE AND SENSITIVITY — CHANNEL A

Specifications:

25 mV rms 0-40 MHz (dc coupled) 25 mV rms 20 Hz - 40 MHz (ac coupled)

50 mV rms 40-100 MHz

Description: A signal generator with calibrated output is set to the specified Model 5328B signal sensitivity level and varied over the specified frequency range. The Model 5328B must display the correct frequency.

- a. 10 Hz to 10 MHz
 - 1) Set up equipment for this test as shown in Figure 4-7.

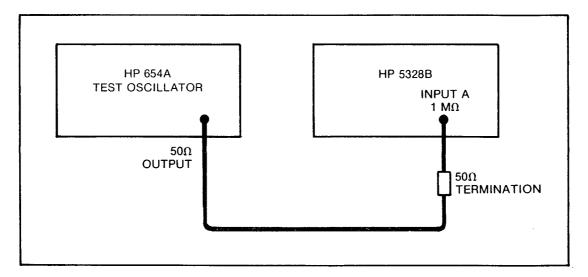


Figure 4-7. Channel A Frequency Response and Sensitivity Test Setup, 10 Hz to 10 MHz

- 2) DC-coupled 10 Hz to 10 MHz
 - a) Set the Model 5328B's controls as follows —

FUNCTION FREQ A
RESOLUTION 1 Hz
SAMPLE RATE fully counter-clockwise
LEVEL A PRESET
COUPLING DC
ATTEN X1
CHK/COM A/SEP SEP
ARM (rear-panel) OFF

PERFORMANCE TEST (Continued)		
b)	Set the Test Oscillator's controls as follows —	
	Output level 25mV rms	
C)	Vary the Test Oscillator's frequency from 10 Hz to 10 MHz.	

NOTE

Adjust the Model 5328B's LEVEL A control as necessary to achieve a stable numeric display.

d) Record the results of this test on the performance test record at the end of this table.

Observe that the Model 5328B displays the correct frequency for all oscillator

3) AC-coupled 20 Hz to 10 MHz —

settings.

a) Set the Model 5328B's controls as follows —

FUNCTION FREQ A
RESOLUTION 1 Hz
SAMPLE RATE fully counter-clockwise
LEVEL A PRESET
COUPLING AC
ATTEN X1
CHK/COM A/SEP SEP
ARM (rear-panel) OFF

b) Set the Test Oscillator's controls as follows —

Output level 25mV rms

c) Vary the Test Oscillator's frequency from 20 Hz to 10 MHz.

Observe that the Model 5328B displays the correct frequency for all oscillator settings.

NOTE

Adjust the Model 5328B's LEVEL A control as necessary to achieve a stable numeric display.

- d) Record the results of this test on the performance test record at the end of this table.
- b. 10 MHz to 100 MHz
 - 1) Set up equipment for this test as shown in Figure 4-8.





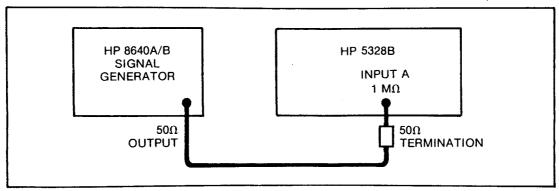


Figure 4-8. Channel A Frequency Response and Sensitivity Test Setup, 10 MHz to 100 MHz

- 2) DC-coupled 10 MHz to 100 MHz
 - a) Set the Model 5328B's controls as follows —

FUNCTION FREQ A
RESOLUTION 1 Hz
SAMPLE RATE fully counter-clockwise
LEVEL A PRESET
COUPLING DC
ATTEN X1
CHK/COM A/SEP SEP
ARM (rear-panel) OFF
(

b) Set the Test Oscillator's controls as follows —

Output level 25mV rms

c) Vary the Test Oscillator's frequency from 10 MHz to 40 MHz.

Observe that the Model 5328B displays the correct frequency for all oscillator settings.

NOTE

Adjust the Model 5328B's LEVEL A control as necessary to achieve a stable numeric display.

- d) Record the results of this test on the performance test record at the end of this table.
- e) Set the Test Oscillator's level control to 50 mV.
- f) Vary the Test Oscillator's frequency from 40 MHz to 100 MHz.

Observe that the Model 5328B displays the correct frequency for all oscillator settings.

NOTES

- 1. Adjust the Model 5328B's LEVEL A control as necessary to achieve a stable numeric display.
- 2. The display will overflow if the signal being measured is 100 MHz or more.



- g) Record the results of this test on the performance test record at the end of this table.
- 3) AC-coupled 10 MHz to 100 MHz
 - a) Set the Model 5328B's controls as follows —

FUNCTION FREQ A
RESOLUTION 1 Hz
SAMPLE RATE fully counter-clockwise
LEVEL A PRESET
COUPLING AC
ATTEN X1
CHK/COM A/SEP SEP
ARM (rear-panel) OFF

b) Set the Test Oscillator's controls as follows —

Output level 25mV rms

c) Vary the Test Oscillator's frequency from 10 MHz to 40 MHz.

Observe that the Model 5328B displays the correct frequency for all oscillator settings.

NOTE

Adjust the Model 5328B's LEVEL A control as necessary to achieve a stable numeric display.

- d) Record the results of this test on the performance test record at the end of this table.
- e) Set the Test Oscillator's level control to 50 mV.
- f) Vary the Test Oscillator's frequency from 40 MHz to 100 MHz.

Observe that the Model 5328B displays the correct frequency for all oscillator settings.

NOTES

- 1. Adjust the Model 5328B's LEVEL A control as necessary to achieve a stable numeric display.
- 2. The display will overflow if the signal being measured is 100 MHz or more.
- Record the result of this test on the performance test record at the end of this table.



4-13. FREQUENCY RESPONSE AND SENSITIVITY — CHANNEL B

Specifications:

25 mV rms 0-40 MHz (dc coupled) 25 mV rms 20 Hz - 40 MHz (ac coupled) 50 mV rms 40-100 MHz

Description: The Model 5328B is set to RATIO B/A. The Model 5328B must display the indicated ratio value throughout the frequency range.

- a. 10 Hz to 10 MHz
 - 1) Set up equipment for this test as shown in Figure 4-9.

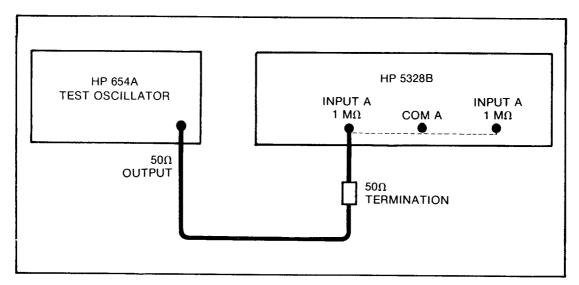


Figure 4-9. Channel B Frequency Response and Sensitivity Test Setup, 10 Hz to 10 MHz

- 2) DC-coupled 10 Hz to 10 MHz
 - a) Set the Model 5328B's controls as follows -

FUNCTION RATIO B/A
RESOLUTION 0.1 MHz,10
SAMPLE RATE Mid-range
LEVEL A PRESET
LEVEL B PRESET
Input Impedance 1 Megohm
COUPLING (both channels) DC
ATTEN (both channels)
CHK/COM A/SEP COM A
ARM (rear-panel) OFF

NOTE

In the steps below, you may have to change the SAMPLE RATE setting slightly in order to see the GATE indicator flash.

b) Set the Test Oscillator's controls as follows -

Output level 25mV rms

c) Vary the Test Oscillator's frequency from 10 Hz to 10 MHz.

Observe that the Model 5328B displays "1.0 \pm 0.1", and that its GATE light is flashing, at all Test Oscillator frequency settings.

NOTE

Adjust the Model 5328B's LEVEL A and LEVEL B controls as necessary to achieve a stable numeric display.

- d) Record the results of this test on the performance test record at the end of this table.
- 3) AC-coupled 20 Hz to 10 MHz
 - a) Set the Model 5328B's controls as follows —

FUNCTION RATIO B/A
RESOLUTION 0.1 MHz,10
SAMPLE RATE Mid-range
LEVEL A PRESET
LEVEL B PRESET
Input Impedance 1 Megohm
COUPLING (both channels)
ATTEN (both channels)
CHK/COM A/SEP COM A ARM (rear-panel) OF
ARM (rear-panel)



c) Vary the Test Oscillator's frequency from 20 Hz to 10 MHz.

Observe that the Model 5328B displays "1", and that its GATE light is flashing at all Test Oscillator frequency settings.

NOTE

Adjust the Model 5328B's LEVEL A control as necessary to achieve a stable numeric display. Adjust SAMPLE RATE slightly, if necessary to see GATE light flashing.

d) Record the results of this test on the performance test record at the end of this table.

b. 10 MHz to 100 MHz

1) Set up equipment for this test as shown in Figure 4-10.

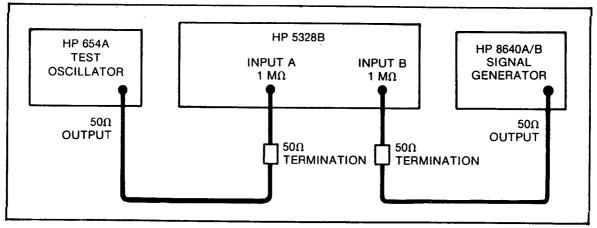


Figure 4-10. Channel B Frequency Response and Sensitivity Test Setup, 10 MHz to 100 MHz

- 2) DC-coupled 10 MHz to 100 MHz
 - a) Set the Model 5328B's controls as follows —

FUNCTION RATIO B/A
RESOLUTION 1 Hz
SAMPLE RATE fully counter-clockwise
LEVEL (both channels) PRESET
COUPLING (both channels) DC
ATTEN (both channels)
CHK/COM A/SEP SEP
ARM (rear-panel) OFF

NOTE

In the steps below, you may have to change the SAMPLE RATE setting slightly in order to see the GATE indicator flash.

,		
	Output Frequency	1 MHz
		25mV rms

c) Set the Signal Generator's controls as follows -

b) Set the Test Oscillator's controls as follows -

Output Frequency	1	10 MHz
Output Level	25 ı	mV rms

Observe that the Model 5328B displays approximately 10.000 000 \pm 1, with the GATE light flashing.

- d) Mark the results of your test on the performance test card provided.
- e) Set the Signal Generator's controls as follows —

Output Frequency		40 N	ЛHz
Output Level	25	mV	rms

Observe that the Model 5328B displays approximately 40.000 000 \pm 1, with the GATE light flashing.

- f) Mark the results of your test on the performance test card provided.
- g) Set the Signal Generator's controls as follows —

Output Frequency	 70 MHz
Output Level	 50 mV rms

Observe that the Model 5328B displays approximately 70.000 000 \pm 1, with the GATE light flashing.

- h) Mark the results of your test on the performance test card provided.
- i) Set the Signal Generator's controls as follows —

Output Frequency	 100 MHz
Output Level	 50 mV rms

Observe that the Model 5328B displays approximately 99.999 000 or OVFL 00.000 000 \pm 1, with the GATE light flashing.

NOTE

The display will overflow if the signal being measured is 100 MHz or more.

j) Mark the results of your test on the performance test card provided.







3)

AC-c	coupled 10 MHz to 100 MHz —
a)	Set the Model 5328B's controls as follows —
	FUNCTION
	NOTE
	In the steps below, you may have to change the SAMPLE RATE setting slightly in order to see the GATE indicator flash.
b)	Set the Test Oscillator's controls as follows —
	Output Frequency
c)	Set the Signal Generator's controls as follows —
	Output Frequency
	Observe that the Model 5328B displays approximately 10.000 000 $\pm 1\text{,}$ with the GATE light flashing.
d)	Mark the results of your test on the performance test card provided.
e)	Set the Signal Generator's controls as follows —
	Output Frequency 40 MHz Output Level 25 mV rms
	Observe that the Model 5328B displays approximately 40.000 000 \pm 1, with the GATE light flashing.
f)	Mark the results of your test on the performance test card provided.

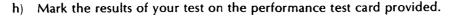
g) Set the Signal Generator's controls as follows —

GATE light flashing.

 Output Frequency
 70 MHz

 Output Level
 50 mV rms

Observe that the Model 5328B displays approximately 70.000 000 \pm 1, with the



i) Set the Signal Generator's controls as follows —

Observe that the Model 5328B displays approximately 99.999 000 or OVFL 00.000 000 \pm 1, with the GATE light flashing.

NOTE

The display will overflow if the signal being measured is 100 MHz or more.

j) Mark the results of your test on the performance test card provided.

4-14. PERIOD AND PERIOD AVERAGE TESTS

Specifications:

PER A — the Model 5328B will measure periods of signals up to 10 MHz, with resolutions from 10 nS to 0.1 S in decade steps.

PER AVG A — the Model 5328B will measure periods of signals up to 10 MHz, with resolutions from 10 nS to 0.001 pS in decade steps.

Description: A 10 MHz signal from a generator drives the Model 5328B's Channel "A" as shown in *Figure 4-11*.

a. Set up equpiment for this test as shown in Figure 4-11.

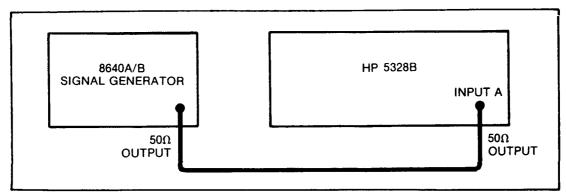


Figure 4-11. Period Test Setup — 10 MHz at 50 mV rms

- b. Set the Signal Generator for an output of 10 MHz at 50 mV rms.
- c. Set the Model 5328B's controls as follows —

FUNCTION PER A
RESOLUTION 1 MHz,1
LEVEL A PRESET
COUPLING (Channel A) AC
CHK/COM A/SEP SEP

Verify that the Model 5328B displays approximately 0.10 μ S $\pm 0.01 \mu$ S.

- d. Record your results on the performance test record card provided.
- e. Set the Model 5328B's controls as follows -

FUNCTION PER	
RESOLUTION 10 k	:Hz,10 ²
SAMPLE RATE Mid	l-range
LEVEL A	PRESET
COUPLING	AC
CHK/COM A/SEP	SEP

Verify that the Model 5328B displays approximately 100.0 nS ± 1 nS.

f. Record your results on the performance test record card provided.



4-15. TIME INTERVAL AND TIME INTERVAL AVERAGE

Specifications:

T.I. A -B — the Model 5328B measures time intervals (in the range 10 nS to 10⁷ nS) between a "START" signal at the Channel "A" input and a "STOP" signal at the Channel "B" input.

T.I. AVG A \rightarrow B — the Model 5328B measures time intervals (in the range 1 nS to 1 S) between a "START" signal at the Channel "A" input and a "STOP" signal at the Channel "B" input. The number of time intervals over which the time interval measurement is made can be selected by the Model 5328B's RESOLUTION switch.

Description: A common signal drives the Model 5328B's Channel "A" and Channel "B" inputs.

a. Set up equipment for this test as shown in Figure 4-12.

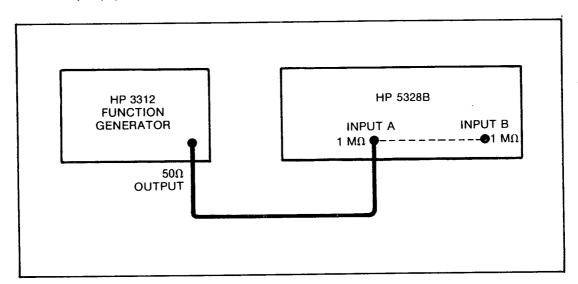


Figure 4-12. Time Interval Test Setup, 10 MHz at 50 mV rms

- b. Set the Function Generator for an output of 10 MHz at 50 mV rms.
- c. Set the Model 5328B's controls as follows -

FUNCTION	T.I. A → B
RESOLUTION	
LEVEL A	
LEVEL B	. PRESET
COUPLING (both channels)	, AC
ATTEN (both channels)	
CHK/COM A/SEP	COM A

- d. Set the Model 5328B's Channel "A" SLOPE to "+" and its Channel "B" SLOPE to "-".
- e. Verify that the Model 5328B's display reads approximately 0.05 μ S \pm 0.01 μ S.

- f. Record the results of your test on the test record card provided.
- g. Set the Function Generator for an output of approximately 9.5 MHz at 50 mV rms.
- h. Set the Model 5328B's FUNCTION switch to T.I. AVG A \rightarrow B, and its RESOLUTION switch to 10 kHz, 102.
- i. Verify that the Model 5328B displays approximately 52.6 nS, ± 2 nS.
- j. Record the results of your test on the test record card provided.
- k. Change the Model 5328B's Channel "A" SLOPE to "-" and its Channel "B's" SLOPE to "+".
- 1. Verify that the Model 5328B displays 52.6 nS ± 2 nS.
- m. Record the results of your test on the test record card provided.

4-16. RATIO B/A AND C/A

Specifications:

The ratio of the frequency at the Model 5328B's Channel "B" input to the frequency of the Channel "A" input signal is measured for "N" counts of the "A" signal, where "N" is selected by the Model 5328B's RESOLUTION switch, in the range of N=1 to N=107. If the optional Channel "C" is installed, the ratio of the frequency of its input signal to that of the Channel "A" input signal can be similarly measured.

Input Channel	Frequency Range	
Α	0-10 MHz	
В	0-100 MHz	
C	90-1300 MHz	

Description: Separate input signals are applied to Channel "A" and to Channel "B" (or Channel "C", if Option 031 or Option 050) is installed. Frequencies selected are the highest specified for each individual channel.

a. Set up equipment as shown in Figure 4-13.

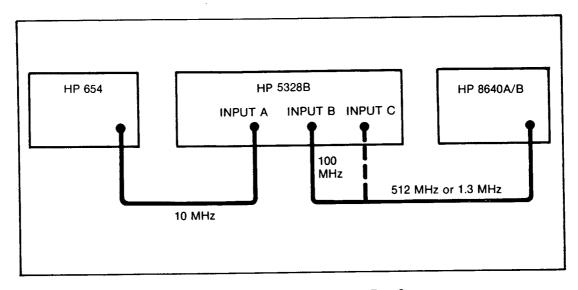


Figure 4-13. Ratio Measurement — Test Setup

b. RATIO B/A

- 1) Set the lower-frequency Test Oscillator to provide a 10 MHz signal at 50 mV.
- 2) Connect the lower-frequency Test Oscillator to the Model 5328B's Channel "A" input.
- 3) Set the higher-frequency oscillator to provide a 100 MHz signal at 100 mV.
- 4) Connect the higher-frequency oscillator to the Model 5328B's Channel "B" input.
- 5) Set the Model 5328B's controls as follows —

FUNCTION	RATIO B/A
RESOLUTION	. 1 kHz,103
LEVEL A	PRESET
LEVEL B	PRESET
COUPLING (both channels)	AC
ATTEN (both channels)	X1
CHK/COM A/SEP	SEP
INPUT IMPEDANCE	50 OHMS

- 6) Verify that the Model 5328B displays approximately 10.000 ± 1 .
- 7) Record the results of your test on the test record provided.

c. RATIO C/A

NOTE

This test can be performed only on a Model 5328B that includes the optional Channel "C" electronics.

- 1) Set the lower-frequency Test Oscillator to provide a 10 MHz signal at 50 mV.
- 2) Connect the lower-frequency Test Oscillator to the Model 5328B's Channel "A" input.
- 3) Set the higher-frequency oscillator to provide a 1300 MHz signal at 40 mV.
- 4) Connect the higher-frequency oscillator to the Model 5328B's Channel "C" input.
- 5) Set the Model 5328B's controls as follows —

JNCTION RATIO C/A	L
ESOLUTION 0.1 MHz, 10)
EVEL A PRESET	-
OUPLING A AC	-
TTEN A X	l
HK/COM A/SEP SEF	
TTEN C fully clockwise	•

- 6) Verify that the Model 5328B displays approximately 130.00 ± 1 .
- 7) Record the results of your test on the test record provided.



4-17. CHANNEL "C" MODULE (OPTION 031 OR OPTION 050); FREQUENCY RESPONSE AND SENSITIVITY

Specification: -21 dBm (20 mV rms) 90 MHz-1.3 GHz

Description: A Signal Generator is connected through a Power Splitter to the Model 5328B's Channel "C" input and a Power Meter. The Signal Generator is varied over the frequency range while the specified signal level is maintained. The counter must display the correct frequency.

a. Set up the equipment for this test as shown in Figure 4-14.

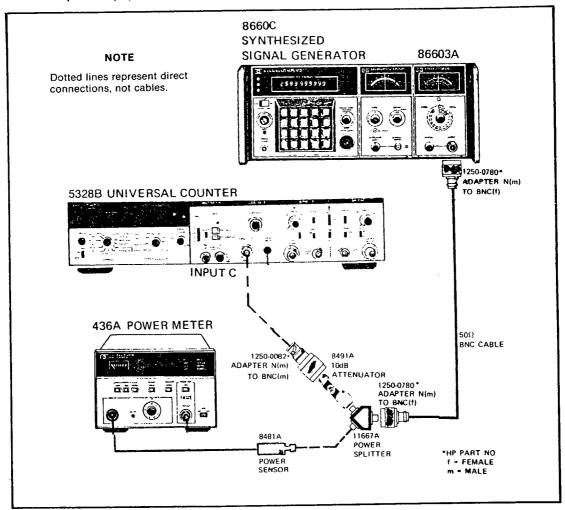


Figure 4-14. Channel "C" — Frequency Response and Sensitivity Test — Setup

b. Set the Model 5328B's controls as follows —

FUNCTION	FREQ C
RESOLUTION	1 kHz,10 ³
SAMPLE RATE	MID-RANGE
ATTEN (Channel "C")	fully clockwise



- c. Set the Signal Generator's output level such that the Power Meter reads "-11 dBm" at each of the following frequencies 90 MHz, 300 MHz, 600 MHz, 900 MHz, and 1300 MHz.
- d. Verify that the Model 5328B displays the proper frequency for each Signal Generator output frequency.
- e. Record the results of your test on the test record card provided.

4-18. TOTALIZE (START A/STOP)

Specification: The number of counts at the Model 5328B's Channel "A"input, divided by "N" (selected by the Model 5328B's RESOLUTION switch) is displayed. Scaled output is available at the rear-panel TB OUT (Time Base Out) connector.

a. Set up equipment for this test as shown in Figure 4-15.

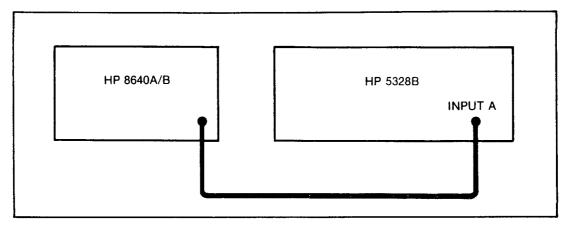


Figure 4-15. START/STOP — Test Setup

b. Set the Model 5328B's controls as follows —

FUNCTION FREQ A
RESOLUTION
LEVEL A PRESET
ATTEN A X1
COUPLING (Channel A)
CHK/COM A/SEP SEP
INPUT IMPEDANCE 50 ohms
SAMPLE RATE Mid-range

NOTE

In the steps below, you may have to change the SAMPLE RATE setting slightly in order to see the GATE indicator flash.

- c. Set the Signal Generator for an output of 10 MHz at 50 mV rms.
- d. The Model 5328B should display approximately 10000.0 kHz, and its Channel "A" Trigger indicator should be flashing.

- e. Set the Model 5328B's FUNCTION switch to "START A".
- f. Allow the Model 5328B to totalize for 30 seconds, then set the FUNCTION switch to "STOP". The Model 5328B should Display about "300.00 m". Record the result of your test on the performance test record provided.

4-19. DIGITAL VOLTMETER (OPTION 021 OR OPTION 050)

NOTE

This test can be performed only if your Model 5328B includes the optional Digital Voltmeter.

Specification: The Model 5328B will measure input voltages with a range of $\pm 10V$, $\pm 100V$, $\pm 1000V$, and AUTO RANGE.

Description: A precision power supply is connected to the Model 5328B's DVM input. Several voltages are selected, in order to cover the voltmeter's ranges.

a. Set up equipment for this test as shown in Figure 4-16.

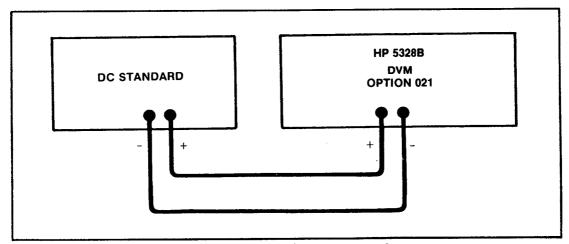


Figure 4-16. Digital Voltmeter — Test Setup

b. Note in the space provided here the time at which you turned on the Model 5328B.

Power turned on at (time)

- c. Allow 20-minute warm-up time before proceeding with the Digital Voltmeter test(s).
- d. Set the Model 5328B's controls as follows -

FUNCTION DVM
RESOLUTION 1 Hz, 106
SAMPLE RATE fully counter-clockwise
FILTER OFF
ATTEN ("A" and "B") X1
LEVEL ("A" and "B") counter-clockwise (not PRESET)
DVM RANGE AUTO
READ A and READ B out (i.e., not pressed)





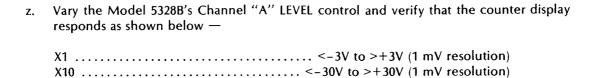


- e. Place a jumper across the Digital Voltmeter's input connectors.
- f. Verify that the Model 5328B displays 0.0000 ± 0.0004 volts.
- g. Record the results of your test on the test record card provided.
- h. Disconnect the jumper from the Digital Voltmeter's inputs.
- i. Set the DC Standard for +9.5000 volts output.
- j. Connect the DC Standard to the Digital Voltmeter.
- k. The Model 5328B's reading should be 9.5000 ± 0.0033 volts.
- 1. Record the results of your test on the test record card provided.
- m. Repeat steps "i" through "I" above for "-9.5000" volts.
- n. Set the DC Standard for +95.00 volts output.
- o. The Model 5328B's reading should be 95.000 ± 0.033 volts.
- p. Record the results of your test on the test record card provided.
- q. Repeat steps "m" through "p" above for "-95.000" volts.

CAUTION

Failure to perform steps "r" through "x" in the sequence in which they are presented may result in damage to the Model 5328B's Digital Voltmeter.

- r. Set the DC Standard for +1000.00 volts output.
- s. The Model 5328B's reading should be $\pm 1000.00 \pm 0.91$ volts.
- t. Set the DC Standard for zero volts (0V) output.
- u. Set the DC Standard for -1000.00 volts output.
- v. The Model 5328B's reading should be -1000.00 ± 0.91 volts.
- w. Record the results of your test on the test record card provided.
- x. Set the DC Standard for zero volts (0V) output.
- y. Press the "READ A" button on the Model 5328B's Digital Voltmeter.



- aa. Press the "READ A" button, to cause it to move to its "out" position.
- ab. Press the "READ B" button on the Model 5328B's Digital Voltmeter. (This will release the Digital Voltmenter's "READ A" button.)
- ac. Vary the Model 5328B's Channel "B" LEVEL control and verify that the counter display responds as shown in step "z" above.
- ad. Record the results of your test on the test record card provided.





4-20. HP-IB INTERFACE VERIFICATION PROGRAM

A program that can be used to check for proper operation of the Model 5328B's HP-IB interface is presented in *Table 4-1*.

The program is designed to verify that the Model 5328B's HP-IB interface is working correctly, and that its front-panel control settings — including those of the (optional) Digital Voltmeter and/or Channel "C" — can be remotely performed via the HP-IB.

To verify the Model 5328B's HP-IB operation —

a. Set up the Model 5328B as shown in Figure 4-17.

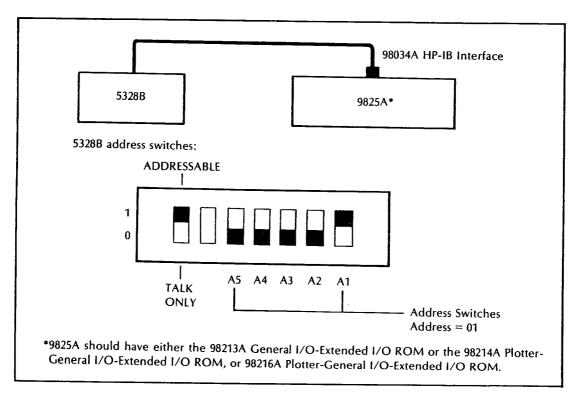
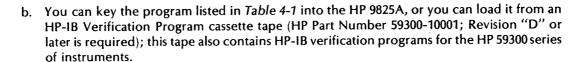


Figure 4-17. HP-IB Verification — Test Connections and Address Switch Settings



To run the program on the cassette -

- 1) Insert the cassette into the HP 9825A.
- 2) Load file "0".
- 3) Press the Model 9825A's RUN button.
- 4) Enter "5328B" when the instrument model number is requested.

The Model 9825A will then load the Model 5328B verification program into its memory.

The Model 5328B HP-IB Verification Program goes through twelve check points for the HP-IB Interface, an additional check point for the (optional) Digital Voltmeter, an additional check point for the (optional) Channel "C" module, and six additional check points for the Channel "A" and Channel "B" Input Module. The information in Table 4-2 tells what occurs during each test and what you should see if the test runs to successful completion. At the end of each test, the program stops and displays the current check point. To advance to the next text, simply press the Model 9825A's CONTINUE key. If you want to repeat a test, set the variable "L" to "1" via the Model 9825A's keyboard ("1-> L EXECUTE)"), then press CONTINUE. To go on to the next test after looping, set "L" back to "0" when the program halts ("0-> L EXECUTE"), then press CONTINUE.

When the Model 9825A displays -

- "DVM OPTIONS 020 OR 021?" at the end of check point 12,
- "CHANNEL C OPTIONS 030 OR 031?" at the end of check point 13, —

enter "YES" CONTINUE if the Model 5328B has the option in question. Enter "NO" CONTINUE if the Model 5328B does not have the option in question.

NOTES

- 1. If the Model 5328B had Option 050, enter "YES" CONTINUE to each of the above questions.
- 2. ALWAYS answer "YES" to the "OPTION 041?" question at the end of check point 14, since this test exercises hardware that is part of the standard Model 5328B.

Figure 4-18 is a sample printout of the program from the HP 9825A.







5328A HP-IB TEST		
CHECK POINT 1 *REMOTE	CHECK POINT 10 *START A/STOP	CHECK POINT 16 *IMPEDANCE
CHECK POINT 2 *CHECK= + 10.00 3E+6	CHECK POINT 11 *START CLOCK	CHECK POINT 17 *ATTENUATOR
CHECK POINT 3 RESOLUTION + 10.00E+6 + 10.000E+6	CHECK POINT 12 *OVM *DVM/A *DVM, A+8	CHECK POINT 18 *SEPARATE/COMMON *NORMAL/INVERTED
+ 10.0000E+6 + 10.000000E+6 + 10.000000E+6 +10.0000000E+6 0+0.00000000E+6	CHECK POINT 13 *FREQ C *RATIO C/A *EVENTS C.A+B	CHECK POINT 19 *COUPLING
CHECK POINT 4 *FREQ A	ALVENIO CHAZD	CHECK POINT 20 *CHECK MODE
CHECK POINT 5 *RATIO B/A	CHECK POINT 14 *SAMPLE RATE *SINGLE/MULTIPLE MEASMNT	CHECK POINT 21 *TRIGGER LEVELS CHNL B:+SLOPE CHNL B:-SLOPE
CHECK POINT 6 *PERIOD A	BUS COMMANDS:	CHNL A;+SLOPE CHNL A;-SLOPE
CHECK POINT 7	*GROUP EXECUTE TRIGGER *SERIAL POLL	END OF TEST
CHECK POINT 8 *T.I.A÷B	STATUS BYTE= 64.00 *GO TO LOCAL	
CHECK POINT 9 *T.I.AVG.A>B		

Figure 4-18. Model 9825A Program Printout

Table 4-1. Model 9825A Program Listing (Part 1 of 4)

```
0: dim C$[40];dsp "MODEL 3328A FREQUENCY COUNTER"
1: prt "532SA HP-IB TEST"; spc 2
2: "code":ent "select code?",S
3: if S=721;dsp "error: calculator address";wait 1000;gto "code"
4: if 3>730;dsp "out of address range+high";wait 1000;gto "code"
5: if S<700;dsp "out of address range+low";wait 1000;gto "code"
ú: dev "fc",S
7: prt "-----", "CHECK POINT 1"
3: rem "fc"
9: prt "*REMOTE"; beep; spc 2
10: dsp "CHECK POINT 1--PRESS CONTINUE"; sto
11: if L=1;qto -4
12: ort "-----", "CHECK POINT 2"
13: wrt "fc", "FF<G3S13R"
14: red "fc",C$;prt "*CHECK=",C$;beep;spc 2
15: dsp "CHECK POINT 2--PRESS CONTINUE"; stp
15: if L=1;gto -4
17: prt "------", "CHECK POINT 3", "RESOLUTION"
18: 1+X
19: "LOUP":fmt 2,"G",f.0,"R"
20: wrt "fc.2",X
21: red "fc",C$;prt C$
22: X+1+X
23: if X=8; gtc +2
24: gto "LUOP"
25: dsp "CHECK POINT 3--PRESS CONTINUE"; beep; stp
26: if L=1;gto -9
27: prt "-----", "CHECK POLAT 4"
28: wrt "fc", "F4R"
29: prt "*FREQ A"; beep; spc 2
30: dsp "CHECK POINT 4--PRESS CONTINUE"; stp
31: if L=1;qto -4
32: prt "-----", "CHECK POINT 5"
33: wrt "fc", "F9R"
34: prt "*RATIO B/A"; beep; spc 2
35: dsp "CHECK POINT 5--PRESS CONTINUE"; stp
36: if L=1;gto -4
37: prt "-----", "CHECK POINT 6"
38: wrt "fc", "F6R"
39: prt "*PERIOD A"; beep; spc 2
40: dsp "CHECK POINT 6--PRESS CONTINUE"; stp
41: if L=1;gtc -4
42: ort "-----", "CHECK POINT 7"
43: wrt "fc", "F7R"
44: prt "*PER.AVG.A"; beep; spc 2
45: dsp "CHECK POINT 7--PRESS CONTINUE"; stp
46: if L=1;qto -4
 47: prt "-----", "CHECK POINT 8"
```

Table 4-1. Model 9825A Program Listing (Part 2 of 4)

```
48: wrt "fc", "F8R"
49: prt "*T.I.A+3"; heep; scc 2
50: dsp "CHECK POINT 8--PRESS CONTINUE"; sto
51: if L=1;gto -4
J2: prt "------", "CHECK 20INT 9"
53: wrt "fc", "r:R"
54: prt "*T.I.AvG.A+B"; beep; spc 2
55: dsp "CHECK POINT 9--PRESS CONTINUE"; stp
55: if L=1; gtc -4.
57: prt "-----", "CHECK POINT 10"
58: wrt "fc", "F1R"
59: wait 1000
60: wrt "fc", "FOR"
61: prt "*START A/STOP"; beep; spc 2
62: dsp "CHECK POINT 10--PRESS CONTINUE"; stp
63: if L=1;gto -5
64: prt "-----", "CHECK POINT 11"
35: wrt "fc", "F2R"
66: prt "*START CLOCK"; beep; spc 2
67: dsp "CHECK POINT 11--PRESS CONTINUE"; stp
68: if L=1;qtc -4
69: prt "-----", "CHECK POINT 12"
70: ent "DVM OPTIONS 020 OR 021?", C$; if C$="NO"; qto +12
71: if C$="NO";gto +11
72: wrt "fc", "F?R"; beep
73: prt "*DVM"
74: wait 5000
75: wrt "fc", "F3R"; beep
76: prt "*DV11/A"
77: wait 5000
78: wrt "fc", "F5R"; beep
79: prt "*DVM,A+B";beep;spc 2
80: dsp "CHECK POINT 12--PRESS CONTINUE"; stp
81: if L=1;gto -12
82: prt "-----", "CHECK POINT 13"
83: ent "CHANNEL C OPTIONS 030 OR 031?", C$; if C$="NO"; qto +11
84: wrt "fc", "F>R"; beep
85: ort "*FREQ C"
86: wait 5000
87: wrt "fc", "F=R"; beep
88: prt "*RATIO C/A"
89: wait 5000
90: wrt "fc", "F; R"
91: prt "*EVENTS C,A+B"; beep; spc 2
92: dsp "CHECK POINT 13--PRESS CONTINUE"; stp
93: if L=1;qto -11
94: prt "-----", "CHECK POINT 14"
95: wrt "fc", "F<G1S137R"
96: dsp "MANUAL OK? -- PRESS CONTINUE"; stp
97: prt "*SAMPLE RATE"
98: wrt "fc", "S6SOR"
```

Table 4-1. Model 9825A Program Listing (Part 3 of 4)

```
99: deb "GATE LIGHT OFF?-PRESS CONTINUE"; sto
100: prt "*JINGLE/MULTIPLE MEASENT"; beep; spc 2
101: dsp "CHECK POINT 14--PRESS CONTINUE"; stp
102: if L=1;gto -8
103: "15":prt "-----", "CHECK POINT 15"
104: prt "BUS COMMANDS:"
105: rem "fc";dsc "PEHOTE?-PRESS CONTINUE";stp
106: 110 7;dsp "LOCAL LOCKOUT?-PRESS CONTINUE";stp
107: prt "*LOCAL LOCKOUT"
108: wrt "fc", "PF < S13G3R"
109: dsp "10.000MHZ?-PRESS CONTINUE"; stp
110: clr 7; prt "*DEVICE CLEAR"; beep
111: dsp "DCL-PRESS CONTINUE"; stp
112: wrt "fc", "PF<S13G3R"
113: dsp "10.000MHZ?-PRESS CONTINUE"; stp
114: clr "fc";prt "*SELECTED DEVICE CLEAR";beep
115: dsp "SDC-PRESS CONTINUE"; stp
116: wrt "fc", "PF <G 3S 0 3R"
117: dsp "IN HOLD?-PRESS CONTINUE"; stp
118: wait 1000; trg 7; beep; wait 2000; trg "fc"; beep; wait 1000
119: prt "*GROUP EXECUTE TRIGGER"
120: dsp "GET-PRESS CONTINUE"; stp
121: wrt "fc", "PF<G7S12R"
122: rds("fc")+A;csp A
123: if A=0;gto -1
124: prt "*SERIAL POLL", "STATUS BYTE=", A
125: dsp "SERIAL POLL-PRESS CONTINUE"; stp
126: 1cl 7;dsp "COUNTER IN LOCAL?-PRESS CONTINUE";stp
127: prt "*GO TO LOCAL"; spc 2
128: rem "fc"
129: dsp "CHECK POINT 15-PRESS CONTINUE"; stp
130: if L=1;qtc "15"
131: ent "OPT 041 PROG INPUT?",C$;if C$="NO";gto "END"
132: "16":prt "------, "CHECK POINT 16"
133: wrt "fc", "PF4G4S13R"
134: dsp "STEPS 1,2-PRESS CONTINUE"; stp
135: wrt "fc", "A137E"; wait 2000
136: wrt "fo", "AOR"
137: dsp "REPEAT FOR CHAL B-PRESS CONTINUE"; stp
138: wrt "fc"," b137R"; wait 2000
139: wrt "fc","BOR"
140: prt "*IMPEDANCE"; beep; spc 2
141: dsp "CHECK POINT 16--PRESS CONTINUE"; stp
142: if L=1;gtc -10
143: prt "-----", "CHECK POINT 17"
144: wrt "fc", "PF4G4S13A1379B137R"
145: dsp "STEP 3---PRESS CONTINUE"; stp
 146: wrt "fc", "PF4G4S13A139B13R"; wait 2000
 147: prt "*ATTENUATOR"; been; spc 2
4143: dsm "CHECK POINT 17-PRESS CONTINUE"; stp
149: if L=1;9to -6
```

Table 4-1. Model 9825A Program Listing (Part 4 of 4)

```
150: prt "-----", "CHECK POINT 18"
151: dsp "STEPS 4,5,6-PRESS CONTINUE"; stp
152: wrt "fc", "PF903S13A79E7R"
153: dsp "SEPRT/CHN-FRESS CONTINUE"; stp
154: wrt "fc","PF4G5S13E79R";wait 2000
155: dsp "NORML/INV-PRESS CONTINUE"; stp
156: prt "*SEPARATE/COMMON", "*NORMAL/INVERTED"; beep; spc 2
157: dsp "CHECK POINT 18-PRESS CONTINUE"; stp
158: if L=1;gto -8
159: prt "-----", "CHECK POINT 19"
160: wrt "fc", "PF4G4S13A79B7R"
161: dsp "STEPS 7,8--PRESS CONTINUE"; stp
162: wrt "fc", "PF4G4S13A379B37R"
163: prt "*COUPLING"; beep; spc 2
164: dsp "CHECK POINT 19-PRESS COUTINUE"; sto
165: if L=1;qto -6
156: ort "-----", "CHECK POINT 20"
167: dsp "STEP 9--PRESS CONTINUE"; stp
168: wrt "fc", "PF4G3S137A?R"
169: prt "*CHECK MODE"; beep; spc 2
170: dsp "CHECK POINT 20-PRESS CONTINUE"; stp
171: if L=1;gtc -5
172: prt "-----", "CHECK POINT 21"
173: wrt "fc", "PF4G6S136A379+000*E37+000*R"
174: dep "STEPS 10,11-PRESS CONTINUE"; stp
175: wrt "fc", "PF4G6S136A379+040*B37+040*R"
176: prt "*TAIGGER LEVELS"
177: dsp "TRIGGER LVLS-PRUSS CONTINUE"; stp
178: dsp "Shaps 12,13,14,15-PRESS CONTINUE"; stp
179: wrt "fc","PF:S137A379+040*537+050*R"
180: prt "CHAL B,+SLOPE"
181: dsp "CHARNEL B,+5LOPE--PRESS CONTINUE"; stp
182: wrt "fc", "PP:S137A379+040*B375+050*R"
183: ort "CHIL B,-SLOPE"
184: dsp "CHML D,-SLOPE--PRESS CONTINUE"; stp
185: wrt "Ec", "PF: S137A379+050*B375+040*R"
186: ort "CHAL A, +SLOPE"
187: Osb "CHAL A, +SLOPE-PRESS CONTIAUT"; str
198: wrt "fc", "ef: 813742795+050*0375+040*8"
189: prt "CHAL A,-SLOPE"; spc 2
190: dsp "CaNL A, - 3LOPE - PRESS CONTINUE"; sto
191: dsp "Check POINT 21-PRESS CONTINUE"; sto
192: if L=1;qtc "16"
193: "END":dsp "END OF TEST"
194: prt "END OF TEST"; beep; spc 5
195: end
*28955
```

Table 4-2. Model 9825A Program Description (Part 1 of 2)

CHECK	TEST	OBSERVE ON 5328B
1	REMOTE	
2	CHECK	Front panel (RMT) annunciator should be ON. Counter should read 10.000 MHz.
3	RESOLUTION	The 9825A should print and counter display the 18 MHz check
4	FREQ A	signal with resolutions from 10 kHz to 0.01 Hz.
5	RATIO B/A	Counter display should read 0.0000 kHz.
6	PERIOD A	Counter display should read 0.0000000.
7	PER. AVG. A	Counter displays should read Ø.Ø s.
8	T.I. A→B	Counter display should read 0.000000 ns.
9	T.I. AVG. A→B	Counter display should read Ø.Ø s.
10	START A/STOP	Counter display should read 0.000000 ns.
		Counter display should read 0.00 n.
11	START CLOCK	In this mode (GATE) and (n) annunciators should be on and instrument should commence counting starting at 0.00.
		DVM OPTIONS 021, 050
12	DVM/A DVM, A→B	The counter will first display $\pm 0.000000V$ for 5 seconds and print DVM, then display $\pm 0.000000~\mu$ for 5 seconds and print DVM/A and finally display $\pm 0.00000~\text{and}$ print DVM, A \rightarrow B.
		CHANNEL C OPTIONS 031, 050
13	FREQ C RATIO C/A EVENTS C, A→B	The count will first display for 5 seconds 0.0000 kHz and print FREQ C, then display 0.00000000 for 5 seconds and print RATIO C/A and finally display 0.0.0.0.0.0.0.0.0.and print EVENTS C, A—B.
14	SAMPLE RATE SINGLE/MULTIPLE MEASUREMENT	When calculator display MANUAL OK?, verify that front panel SAMPLE RATE control can be manually adjusted as seen from GATE LIGHT flashing rate. When calculator display GATE LIGHT OFF?, verify that GATE LIGHT is truly OFF.
		HP-IB BUS COMMANDS
15	LOCAL LOCKOUT (LLO)	When the 9825A CONTINUE key is pressed, verify that counter (RMT) annunciator is ON.
		When the 9825A CONTINUE key is again pressed, verify Local lockout by pressing front panel RESET button and ensuring counter doesn't go into Local operation. (RMT annunciator OFF.)
	DEVICE CLEAR (DCL)	When the 9825A CONTINUE key is pressed, verify that counter displays 10.000 MHz. When the 9825A CONTINUE key is again pressed, counter will reset to its Remote Program Initialize mode and display (0.).
	SELECTED DEVICE CLEAR (SDC)	When the 9825A CONTINUE key is pressed, verify that counter displays 10.000 MHz. When the 9825A CONTINUE key is again pressed, counter will reset to its Remote Program Initialize mode and display (0.).
	GROUP EXECUTE TRIGGER (GET)	When the 9825A CONTINUE key is pressed, verify that counter is in Hold (GATE LIGHT OFF). When the 9825A CONTINUE key is pressed, GATE LIGHT should flash twice and counter should display 10.000 MHz.
	SERIAL POLE (SPE/SPD)	When the 9825A CONTINUE key is pressed, counter should display (0.0000000 MHz) and GATE LIGHT should go OFF. Calculator should print (STATUS BYTE = 64.00).
	GO TO LOCAL (GTL)	When the 9825A CONTINUE key is pressed, verify that counter is in Local (RMT annunciator OFF). When 9825A CONTINUE key is again pressed, counter will go into remote.







Table 4-2. Model 9825A Program Description (Part 2 of 2)

generator to an output of 1 kHz, 100 mV p-p centered at 8V-seen on the oscilloscope. Connect the function generator to moutput to Channel A of the counter's LEVEL A B to PRESET. 2	CHECK POINT	STEP	TEST	OBSERVE ON 5328B
scope display reduced to approximately half its original amplitude level. Repeat for Channel B. 17 3 ATTENUATOR Connect function generator's output to Channel A of the cter. When the 9825A CONTINUE key is pressed, observe counter's trigger light stop blinking. 18 4 SEPARATE/ COMMON NORMAL/ INVERTED 5 When the 9825A CONTINUE key is pressed, observe counter. 6 Connect function generator's output to Channel A of counter. 6 Connect function generator to Channel B. When the 9 CONTINUE key is pressed, counter should display approxim 1.00 kHz. 19 7 COUPLING Set the function generator to a triangular pulse output of 1kt 300 mV p-p with a +0.4V dc offset (Figure 4-20). Connect sign counter's Channel A input. Counter's Channels A and B tri lights should be blinking. 8 When the 9825A CONTINUE key is pressed, observe the counter's trigger lights stop blinking. 20 9 CHECK MODE When the 9825A CONTINUE key is pressed, counter she display 100.000 MHz ±1 count. SAMPLE RATE control ca adjusted to obtain a stable display. 21 10 SLOPE/TRIGGER LEVEL Set the function generator to a triangular pulse output of 1k 300 mV p-p with a +0.4V dc offset (Figure 4-20). Connect the function generator's output to Channel A of the counter. Set counter's LEVEL A and B to PRESET. 11 SLOPE/TRIGGER When the 9825A CONTINUE key is pressed, counter she display approximately 150.00 µs ±75 µs (wide tolerance). When the 9825A CONTINUE key is pressed, counter she display approximately 150.00 µs ±75 µs (wide tolerance).	16	1	IMPEDANCE	Set up the equipment as shown in Figure 4-19. Set the function generator to an output of 1 kHz, 100 mV p-p centered at ØV dc as seen on the oscilloscope. Connect the function generator's output to Channel A of the counter. Set the counter's LEVEL A and B to PRESET.
ter. When the 9825A CONTINUE key is pressed, observed counter's trigger light stop blinking. 18 4 SEPARATE/ COMMON NORMAL/ INVERTED 5 When the 9825A CONTINUE key is pressed, observe coudisplay 1.000. 6 Connect function generator to Channel B. When the 9 CONTINUE key is pressed, observe coudisplay 1.000. 6 Connect function generator to Channel B. When the 9 CONTINUE key is pressed, counter should display approxim 1.00 kHz. 19 7 COUPLING Set the function generator to a triangular pulse output of 1 kt 300 mV p-p with a + 0.4V dc offset (Figure 4-20). Connect sign counter's Channel A input. Counter's Channels A and B tri lights should be blinking. 8 When the 9825A CONTINUE key is pressed, observe the counter's trigger lights stop blinking. 20 9 CHECK MODE When the 9825A CONTINUE key is pressed, counter she display 100.000 MHz ±1 count. SAMPLE RATE control ca adjusted to obtain a stable display. 21 10 SLOPE/TRIGGER LEVEL Set the function generator to a triangular pulse output of 1 kt 300 mV p-p with a + 0.4V dc offset (Figure 4-20). Connect the tion generator's output to Channel A of the counter. Set counter's LEVEL A and B to PRESET. 11 SLOPE/TRIGGER LEVEL When the 9825A CONTINUE key is pressed, observe Char A and B trigger lights commence blinking. 12 CHANNEL B +SLOPE When the 9825A CONTINUE key is pressed, counter she display approximately 150.00 \(\mu \text{s} \text{ trigger ights commence blinking.} \)		2		
COMMON NORMAL/ INVERTED 5 When the 9825A CONTINUE key is pressed, observe condisplay 1.000. 6 Connect function generator to Channel B. When the 9 CONTINUE key is pressed, counter should display approximation 1.00 kHz. 19 7 COUPLING Set the function generator to a triangular pulse output of 1 km 300 mV p-p with a +0.4V dc offset (Figure 4-20). Connect sign counter's Channel A input. Counter's Channels A and B trilights should be blinking. 8 When the 9825A CONTINUE key is pressed, observe the counter's trigger lights stop blinking. 20 9 CHECK MODE When the 9825A CONTINUE key is pressed, counter she display 100.000 MHz ±1 count. SAMPLE RATE control cated adjusted to obtain a stable display. 21 10 SLOPE/TRIGGER LEVEL Set the function generator to a triangular pulse output of 1 km 300 mV p-p with a +0.4V dc offset (Figure 4-20). Connect the function generator's output to Channel A of the counter. Set counter's LEVEL A and B to PRESET. 11 SLOPE/TRIGGER LEVEL When the 9825A CONTINUE key is pressed, observe Charana A and B trigger lights commence blinking. 12 CHANNEL B +SLOPE When the 9825A CONTINUE key is pressed, counter she display approximately 150.00 µs ±75 µs (wide tolerance). 13 CHANNEL B -SLOPE When the 9825A CONTINUE key is pressed, counter she display approximately 150.00 µs ±75 µs (wide tolerance).	17	3	ATTENUATOR	Connect function generator's output to Channel A of the counter. When the 9825A CONTINUE key is pressed, observe the counter's trigger light stop blinking.
display 1.000. Connect function generator to Channel B. When the 9 CONTINUE key is pressed, counter should display approxim 1.00 kHz. Set the function generator to a triangular pulse output of 1 kt 300 mV p-p with a + 0.4V dc offset (Figure 4-20). Connect sign counter's Channel A input. Counter's Channels A and B tri lights should be blinking. When the 9825A CONTINUE key is pressed, observe the counter's trigger lights stop blinking. When the 9825A CONTINUE key is pressed, counter sh display 100.000 MHz ±1 count. SAMPLE RATE control ca adjusted to obtain a stable display. 21 10 SLOPE/TRIGGER LEVEL Set the function generator to a triangular pulse output of 1 kt 300 mV p-p with a + 0.4V dc offset (Figure 4-20). Connect the fitting generator's output to Channel A of the counter. Set counter's LEVEL A and B to PRESET. 11 SLOPE/TRIGGER When the 9825A CONTINUE key is pressed, observe Chan A and B trigger lights commence blinking. 12 CHANNEL B +SLOPE When the 9825A CONTINUE key is pressed, counter sh display approximately 150.00 \(\mu\text{s} \text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} \text{, counter sh display approximately 150.00 \(\mu\text{ is pressed} , counter sh display appro	18	4	COMMON NORMAL/	Connect function generator's output to Channel A of the counter.
CONTINUE key is pressed, counter should display approxim 1.00 kHz. Set the function generator to a triangular pulse output of 1kk 300 mV p-p with a + 0.4V dc offset (Figure 4-20). Connect sign counter's Channel A input. Counter's Channels A and B tri lights should be blinking. When the 9825A CONTINUE key is pressed, observe the counter's trigger lights stop blinking. When the 9825A CONTINUE key is pressed, counter sh display 100.000 MHz ±1 count. SAMPLE RATE control ca adjusted to obtain a stable display. Set the function generator to a triangular pulse output of 1k 300 mV p-p with a + 0.4V dc offset (Figure 4-20). Connect the f tion generator's output to Channel A of the counter. Set counter's LEVEL A and B to PRESET. SLOPE/TRIGGER LEVEL When the 9825A CONTINUE key is pressed, observe Char A and B trigger lights commence blinking. When the 9825A CONTINUE key is pressed, counter sh display approximately 150.00 µs ±75 µs (wide tolerance). When the 9825A CONTINUE key is pressed, counter sh		5		When the 9825A CONTINUE key is pressed, observe counter display 1.000.
300 mV p-p with a +0.4V dc offset (Figure 4-20). Connect sign counter's Channel A input. Counter's Channels A and B tri lights should be blinking. 8 When the 9825A CONTINUE key is pressed, observe the counter's trigger lights stop blinking. 20 9 CHECK MODE When the 9825A CONTINUE key is pressed, counter she display 100.000 MHz ±1 count. SAMPLE RATE control cat adjusted to obtain a stable display. 21 10 SLOPE/TRIGGER LEVEL Sound of the fine generator to a triangular pulse output of 1 k and 300 mV p-p with a +0.4V dc offset (Figure 4-20). Connect the fine generator's output to Channel A of the counter. Set counter's LEVEL A and B to PRESET. 11 SLOPE/TRIGGER LEVEL A and B to PRESET. 12 CHANNEL B +SLOPE When the 9825A CONTINUE key is pressed, counter she display approximately 150.00 μs ±75 μs (wide tolerance). 13 CHANNEL B -SLOPE When the 9825A CONTINUE key is pressed, counter she display approximately 150.00 μs ±75 μs (wide tolerance).		6		Connect function generator to Channel B. When the 9825A CONTINUE key is pressed, counter should display approximately 1.00 kHz.
 CHECK MODE When the 9825A CONTINUE key is pressed, counter she display 100.000 MHz ±1 count. SAMPLE RATE control cat adjusted to obtain a stable display. SLOPE/TRIGGER Set the function generator to a triangular pulse output of 1k 300 mV p-p with a +0.4V dc offset (Figure 4-20). Connect the fittion generator's output to Channel A of the counter. Set counter's LEVEL A and B to PRESET. SLOPE/TRIGGER When the 9825A CONTINUE key is pressed, observe Chan A and B trigger lights commence blinking. CHANNEL B +SLOPE When the 9825A CONTINUE key is pressed, counter she display approximately 150.00 μs ±75 μs (wide tolerance). CHANNEL B -SLOPE When the 9825A CONTINUE key is pressed, counter she display approximately 150.00 μs ±75 μs (wide tolerance). 	19	7	COUPLING	Set the function generator to a triangular pulse output of 1 kHz at 300 mV p-p with a +0.4V dc offset (Figure 4-20). Connect signal to counter's Channel A input. Counter's Channels A and B trigger lights should be blinking.
display 100.000 MHz ±1 count. SAMPLE RATE control ca adjusted to obtain a stable display. 21 10 SLOPE/TRIGGER LEVEL Set the function generator to a triangular pulse output of 1k 300 mV p-p with a +0.4V dc offset (Figure 4-20). Connect the find generator's output to Channel A of the counter. Set counter's LEVEL A and B to PRESET. 11 SLOPE/TRIGGER LEVEL When the 9825A CONTINUE key is pressed, observe Char A and B trigger lights commence blinking. 12 CHANNEL B +SLOPE When the 9825A CONTINUE key is pressed, counter sh display approximately 150.00 μs ±75 μs (wide tolerance). 13 CHANNEL B -SLOPE When the 9825A CONTINUE key is pressed, counter sh		8		
 LEVEL 300 mV p-p with a +0.4V dc offset (Figure 4-20). Connect the f tion generator's output to Channel A of the counter. Set counter's LEVEL A and B to PRESET. SLOPE/TRIGGER When the 9825A CONTINUE key is pressed, observe Char A and B trigger lights commence blinking. CHANNEL B +SLOPE When the 9825A CONTINUE key is pressed, counter sh display approximately 150.00 μs ±75 μs (wide tolerance). CHANNEL B -SLOPE When the 9825A CONTINUE key is pressed, counter sh 	20	9	CHECK MODE	When the 9825A CONTINUE key is pressed, counter should display 100.000 MHz ± 1 count. SAMPLE RATE control can be adjusted to obtain a stable display.
LEVEL A and B trigger lights commence blinking. 12 CHANNEL B +SLOPE When the 9825A CONTINUE key is pressed, counter sh display approximately 150.00 μs ±75 μs (wide tolerance). 13 CHANNEL B -SLOPE When the 9825A CONTINUE key is pressed, counter sh	21	10		Set the function generator to a triangular pulse output of 1 kHz at 300mV p-p with a $+0.4\text{V}$ dc offset (Figure 4-20). Connect the function generator's output to Channel A of the counter. Set the counter's LEVEL A and B to PRESET.
display approximately 150.00 μ s \pm 75 μ s (wide tolerance). 13 CHANNEL B –SLOPE When the 9825A CONTINUE key is pressed, counter sh		11		When the 9825A CONTINUE key is pressed, observe Channels A and B trigger lights commence blinking.
13 CHANNEL B –SLOPE When the 9825A CONTINUE key is pressed, counter sh display approximately 400.00 μ s \pm 75 μ s (wide tolerance).		12	CHANNEL B +SLOPE	When the 9825A CONTINUE key is pressed, counter should display approximately 150.00 μ s $\pm 75~\mu$ s (wide tolerance).
1		13	CHANNEL B -SLOPE	When the 9825A CONTINUE key is pressed, counter should display approximately 400.00 μ s \pm 75 μ s (wide tolerance).
14 CHANNEL A +SLOPE When the 9825A CONTINUE key is pressed, counter sh display approximately 400.00 μ s \pm 75 μ s (wide tolerance).	:	14	CHANNEL A +SLOPE	When the 9825A CONTINUE key is pressed, counter should display approximately 400.00 μ s \pm 75 μ s (wide tolerance).
15 CHANNEL A –SLOPE When the 9825A CONTINUE key is pressed, counter sh display approximately 150.00 μs ±75 μs (wide tolerance).		15	CHANNEL A -SLOPE	First Country of First

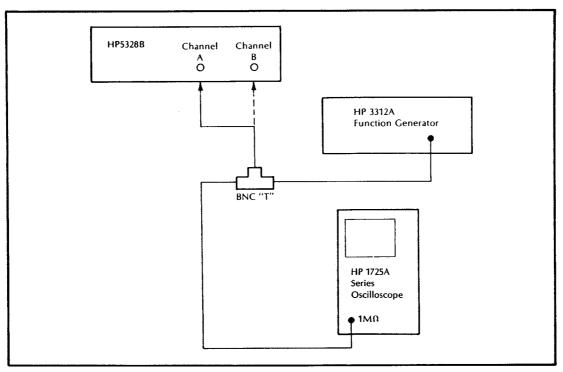


Figure 4-19. Programmable Input Module — Test Setup

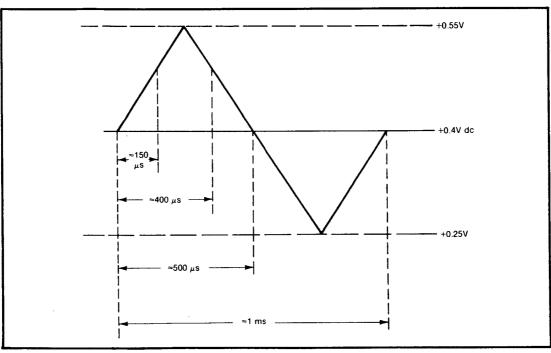


Figure 4-20. Test Waveform

4-21. REMOTE TRIGGER LEVEL TEST

Specification: $\pm 2.5 \text{ volts } \pm 35 \text{ mV}$

Description: The Model 5328B is remotely programmed at a specified level, and the respective trigger level LED should begin blinking. The MARKER OUTPUT signal should intersect the applied signal at the programmed level, ±35 mV.

a. Connect the test setup as shown in Figure 4-21.

NOTE

Initially, connect the Model 5328B's Channel "A" MARKER OUTPUT to the Oscilloscope's Channel "B" input.

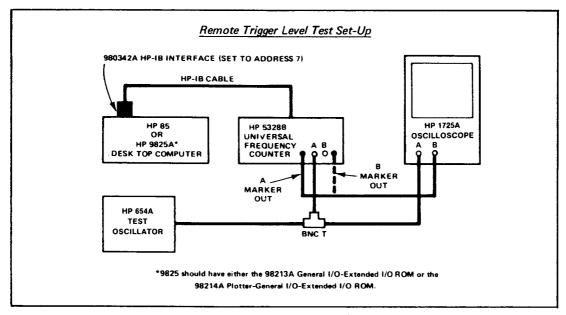


Figure 4-21. Remote Trigger Level Test Set-up

b. Set the Model 5328B's address switches as shown in Figure 4-22.

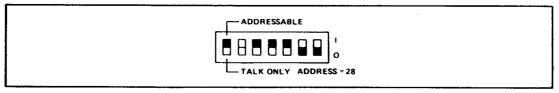


Figure 4-22. Switch Settings for Remote Trigger Level Test

c. Execute the following statement from the controller's keyboard —

HP 9825A: wrt 728, "PF4G6513A1379+000*B137+000*R"

HP 85A: output 728; "PF4G6513A1379+000*B137+000*R"

d. Set the Signal Generator for a 100 Hz signal at six volts peak-to-peak. Center the signal on the Oscilloscope's Channel "A" display.

e. Adjust the Oscilloscope's Channel "B" controls so the top of the Model 5328B's Channel "A" MARKER OUTPUT waveform just barely intersects the positive-slewing and negative-slewing portions of the 100 Hz sine wave as shown in *Figure 4-23*. Verify that this occurs at zero volts on the 100-Hz sine wave.

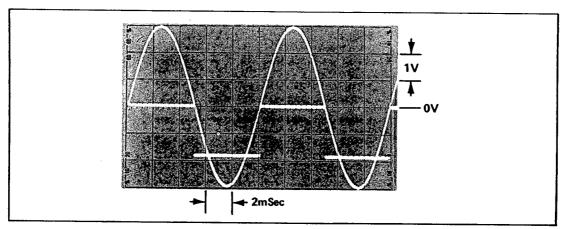


Figure 4-23. Triggering Level Test Waveforms — I

- f. Connect the Model 5328B's Channel "B" marker to the Oscilloscope's Channel "B" input.
- g. Adjust the Oscilloscope's Channel "B" controls so the top of the Model 5328B's Channel "B" MARKER OUTPUT waveform just barely intersects the positive-slewing and negative-slewing portions of the 100 Hz sine wave as shown in *Figure 4-23*. Verify that this occurs at zero volts on the 100-Hz sine wave.
- h. Execute the following statement from the controller's keyboard

HP 9825A: wrt 728, "PF4G6513A1379+200*B137+200*R"

HP 85A: output 728; 'PF4G6513A1379+200*B137+200*R"

i. Adjust the Oscilloscope's Channel "B" controls so the top of the Model 5328B's Channel "B" MARKER OUTPUT waveform just barely intersects the positive-slewing and negative-slewing portions of the 100 Hz sine wave as shown in Figure 4-24. Verify that this occurs at +2 volts on the 100-Hz sine wave.

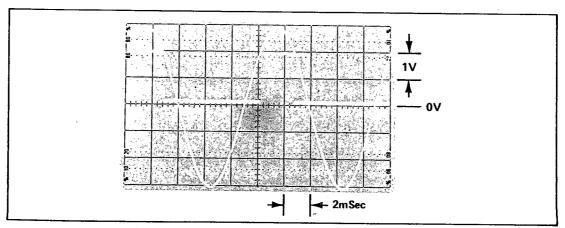


Figure 4-24. Triggering Level Test Waveforms — II







- j. Connect the Model 5328B's Channel "A" marker to the Oscilloscope's Channel "B" input.
- k. Adjust the Oscilloscope's Channel "B" controls so the top of the Model 5328B's Channel "B" MARKER OUTPUT waveform just barely intersects the positive-slewing and negative-slewing portions of the 100 Hz sine wave as shown in *Figure 4-25*. Verify that this occurs at +2 volts on the 100-Hz sine wave.
- I. Execute the following statement from the controller's keyboard —

HP 9825A: wrt 728, "PF4G6513A1379-200*B137-200*R"

HP 85A: output 728; "PF4G6513A1379-200*B137-200*R"

m. Adjust the Oscilloscope's Channel "B" controls so the top of the Model 5328B's Channel "A' MARKER OUTPUT waveform just barely intersects the positive-slewing and negative-slewing portions of the 100 Hz sine wave as shown in *Figure 4-25*. Verify that this occurs at -2 volts on the 100-Hz sine wave.

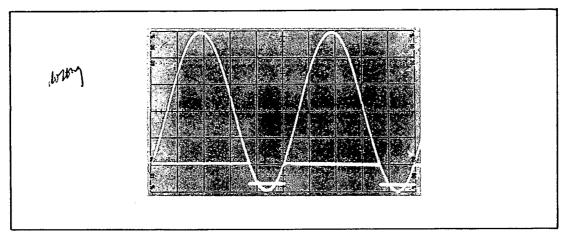


Figure 4-25. Triggering Level Test Waveforms — III

- n. Connect the Model 5328B's Channel "B" MARKER OUTPUT to the Oscilloscope's Channel "B" input.
- o. Adjust the Oscilloscope's controls to position the Channel "B" marker as shown in Figure 4-25. Verify that the Channel "B" marker intersects the 100-Hz sine wave at -2 volts.
- p. Record the results of your tests on the test record card provided.

PERFORMANCE TEST RECORD

	TERI ORMIANCE TEST RECORD	<u> </u>	
Hewlett	-Packard Model 5328B Universal Counter		
Serial N	umber		
Tests Pe	rformed by		
Date _	//		
Test Ref	Description	Res Pass	ults Fail
4-11	SELF CHECK		
4-12	FREQUENCY RESPONSE AND SENSITIVITY — CHANNEL "A"		
	DC 10 Hz to 10 MHz	*****	
	AC 20 Hz to 10 MHz		
	DC 10 MHz to 40 MHz		
	DC 40 MHz to 100 MHz		
	AC 10 MHz to 40 MHz		
	AC 40 MHz to 100 MHz		
4-13	FREQUENCY RESPONSE AND SENSITIVITY — CHANNEL "B"		4 247
	DC 10 Hz to 10 MHz		
	AC 20 Hz to 10 MHz		
	DC 10 MHz to 100 MHz		
	10.000000 ±1		
	40.000000 ±1		
	70.000000 ± 1	****	
	100.0 ± 1		
	AC 10 MHz to 100 MHz		
	10.000000 ± 1		
	40.000000 ± 1		
	70.000000 ± 1	····	***
	100.0 ±1		
4-14	PERIOD AND PERIOD AVERAGE		
	PER A 0.10 μ S $\pm 0.01~\mu$ S		
	PER AVG A 100.0 nS +1 nS		

PERFORMANCE TEST RECORD (Continued)

Test Ref	Description	Results Pass Fail
4-15	TIME INTERVAL AND TIME INTERVAL AVERAGE	
	T.I. A \rightarrow B 0.05 μ S \pm 0.01 μ S	
	T.I. AVG A \rightarrow B 52.6 μ S \pm 2 nS (A+, B-)	
	T.I. AVG A – B 52.6 μ S \pm 2 nS (A-, B+)	
4-16	RATIO B/A AND C/A	71
	B/A 10.000 ±1	
	C/A (OPTION 030 OR OPTION 050) 130.00 ± 1	
4-17	CHANNEL C (OPTION 031 OR OPTION 050)	
	90 MHz	
	300 MHz	
	600 MHz	
	900 MHz	
	1300 MHz	
4-18	TOTALIZE	
	START A/STOP 300.00 m	
4-19	DIGITAL VOLTMETER (OPTION 021 OR 050)	
	SHORT CONNECTORS 0.0000V ±0.4 mV	
	±9.5000V ±3.3 mV	
	±95.000V ±33 mV	
	±1000.00V ±0.91V	
	READ A	
	READ B	
4-20	HP-1B INTERFACE VERIFICATION	
4-21	REMOTE TRIGGER LEVEL TEST	





SECTION V ADJUSTMENTS

5-1. INTRODUCTION

This section describes the adjustments required to keep the Model 5328B operating within its specifications. Any adjustment listed in this manual section should be made only if required, either because the Model 5328B failed its performance check (see Section 4 of this manual) or because a part affecting the adjustment was replaced.

5-2. EQUIPMENT REQUIRED

The test equipment required for the adjustment procedure(s) given in this section is listed in *Table 1-4*, Recommended Test Equipment. Equipment other than the specified model may be substituted, provided it meets the "critical specifications" listed.

WARNING

BE SURE ALL ELECTRICALLY OPERATED EQUIPMENT YOU USE IS PROPERLY GROUNDED.

CAUTION

Be sure to turn electrical power off whenever you are connecting or disconnecting test equipment, installing or removing parts, etc.

5-3. FACTORY-SELECTED COMPONENTS

Some of the values in the parts lists (in Section 6) are selected during manufacturing to meet circuit requirements. These parts are marked with an asterisk (*) in the parts lists and schematic diagrams, with the average value shown.

5-4. ADJUSTMENT LOCATIONS

Adjustment locations are identified in the component locators given in Section 8 of this manual.

5-5. SAFETY CONSIDERATIONS

This section contains warnings and cautions that must be followed for your protection and to avoid damage to the equipment.

WARNING

MAINTENANCE DESCRIBED HEREIN IS PERFORMED WITH POWER SUPPLIED TO THE INSTRUMENT, AND PROTECTIVE COVERS REMOVED. SUCH MAINTENANCE SHOULD BE PERFORMED ONLY BY SERVICE-TRAINED PERSONNEL WHO ARE AWARE OF THE HAZARDS (FIRE AND/OR ELECTRICAL SHOCK, FOR EXAMPLE) INVOLVED. WHERE MAINTENANCE CAN BE PERFORMED WITHOUT POWER APPLIED, THE POWER SHOULD BE REMOVED.

BEFORE ANY REPAIR IS COMPLETED, ENSURE THAT ALL SAFETY FEATURES ARE INTACT AND FUNCTIONING, AND THAT ALL NECESSARY PARTS ARE CONNECTED TO THEIR PROTECTIVE GROUNDING MEANS.

5-6. Power Supply Adjustment

- a. With the power cord disconnected, remove the top cover of the counter.
- b. Insure the 5328B line voltage selector switch is set for the desired ac line voltage.
- c. Connect the ac line cord to the counter.
- d. Probe the solder dot labeled +5 near the space labeled "XA13" with a DVM probe.
- e. Adjust the power supply reference adjustment A2R27 for reading of ± 5.00 V dc ± 0.05 V. Access A1R27 through the HP-IB board via the hole provided in the right rear of the HP-IB board, in front of the fan.
- f. Check the following voltages on the motherboard near XA13:
 - -5.2V dc ±.050V
 - $+15V dc \pm .400V$
 - $-15V dc \pm .400V$
- g. The power supply adjustments are now complete.

5-7. OSCILLATOR ADJUSTMENT

NOTE

The same procedure can be used for the standard oscillator or for the High-Stability Oven Oscillator (Option 010) — however, for the Option 010 oscillator, you should allow a 24-hour warm-up period before performing the adjustment procedure.

TRIGGERED SWEEP **OSCILLOSCOPE** PATTERN MOVEMENT: 5328B OSC FREQ HIGH 5328B OSC FREQ LOW OSCILLATOR **OUTPUT** REFERENCE **FREQUENCY** 5328B **EXT STANDARD** UNIVERSAL **SYNC** INPUT INPUT COUNTER **OUTPUT**

a. Set up the equipment as shown in Figure 5-1.

Figure 5-1. Oscillator Adjustment Setup

- b. Connect the Reference Frequency Standard (multiple of 10 MHz) to the Oscilloscope's EXTERNAL SYNC INPUT.
- c. Set the Model 5328B's rear-panel OSC INT/EXT to INT.
- d. Connect the oscillator output BNC to the Oscilloscope's Channel "A" input.
- e. Adjust the Model 5328B's oscillator frequency adjustment for minimal sideways movement of the 10 MHz displayed signal.

NOTES

- 1. If the Model 5238B has the standard oscillator, its frequency adjustment is A1C18 (on the A1 board).
- 2. If the Model 5328B has the Option 010 High-Stability Oven Oscillator, the frequency adjustment control is accessible from outside the oven's can, through the hold labelled "FREQ ADJ".
- f. You can determine the approximate offset of the Model 5328B's oscillator by timing the sideways movement of the waveform on the oscilloscope's screen. *Table 5-1* provides the calibration you can use; an example follows.

Rate of Waveform Movement	Oscillator Offset, Based on Oscilloscope Sweep Speed of			
	1 us/div	0.1 us/div	0.01 us/div	
1 div/1s	1 × 106	1 × 10 ⁷	1 × 108	
1 div/10s	1 × 10 ⁷	1 × 108	1 × 109	
1 div/100s	1 × 108	1 × 109	1 × 1010	

Table 5-1. Oscillator Offset Timing

For example, if the trace moves one division in ten seconds, and the Oscilloscope's sweep speed is set to 0.01 us/division, the Oscillator's signal is within 1×10^9 of the reference frequency, as you can see from *Table 5-1*, above. You can verify this by making the following calculation —

$$\frac{\Delta t}{t} = \frac{\Delta f}{f}$$

$$\frac{0.01 \text{ us/div}}{10 \text{ s/div}} = 1 \times 10^9$$

5-8. INPUT MODULE

5-9. Input Sensitivity

Adjust the Channel "A" and Channel "B" INPUT sensitivity as follows -

- a. Remove the Model 5328B's top cover to gain access to variable resistors A12R28 and A12R26 on Amplifier Assembly A12 (see component/adjustment locator).
- b. Set the Model 5328B's front-panel controls as follows:

FUNCTION FREQ A
RESOLUTION 105, 10 Hz
SLOPE (A) +
AC/DC (A) DC
ATTEN (A) 1
LEVEL (A) PRESET
CHK/COM A/SEP SEP
SAMPLE RATE mid-range
INPUT IMPEDANCE 50-ohms

c. Set the Model 5328B's rear-panel controls as follows:

STORAGE	ON
OSC	ON
ARM	OFF

- d. Connect the Signal Generator (or equivalent) to the Model 5328B's Channel "A" INPUT.
- e. Set the Signal Generator to 100MHz at 50 mV rms (140 mV peak-to-peak).
- f. Slowly decrease the Signal Generator's output level to 25 mV rms (70 mV peak-to-peak), while adjusting variable resistor A12R28 to obtain a stable correct display on the counter. Continue to reduce the 100MHz signal level and re-adjust A12R28 until maximum sensitivity is achieved for Channel "A".

This ends the Channel "A" sensitivity adjustment. The step below begins the Channel "B" sensitivity adjustment.

g. Set the Model 5328B's front-panel controls as follows:

FUNCTION	RATIO B/A
RESOLUTION	103, 10 kHz
CHK/COM A/SEP	SEP
I EVEL B	PRESET

- h. With the Signal Generator set to 40 MHz at 50 mV rms at the Channel "B" INPUT, connect a second Signal Generator (set to 10 MHz at 100 mV rms to the Channel "A" INPUT).
- i. Slowly decrease the level of the signal connected to the Model 5328B's Channel "B" Input, while adjusting A12 26 to obtain a stable correct display of about "4.000" on the counter. Continue reducing the Channel "B" Input signal and adjusting A12R26 until maximum sensitivity is obtained for Channel "B".

5-10. 100 MHz Phase-Locked Loop Adjustment

a. Set the Model 5328B's front-panel controls as follows:

FUNCTION	FREQ A
RESOLUTION	106, 1 Hz
SAMPLE RATE	fully counter-clockwise
CHK/COM A/SEP	CHK

- b. Externally trigger the Oscilloscope from the Model 5328B's 10 MHz rear-panel output (set the Model 5328B's rear-panel switch to "INT").
- c. Connect a probe from channel A on the oscilloscope to A14TP1.
- d. The Oscilloscope display of the signal at A14TP1 should look like the waveform shown in Figure 5-2. This should be a 10 MHz TTL signal with no visible phase jitter.
- e. Measure the dc voltage at A14TP5, and adjust A14C18 until this voltage is between +1.6V and +1.8V.
- f. Observe the Model 5328B's display. The counter's display should show "100 MHz" ±1 count ("OF" 00000.000 kHz).

NOTE

Change the resolution switch momentarily to 10⁵ 10 Hz to observe 100,000,000kHz (100MHz without overflow indication).

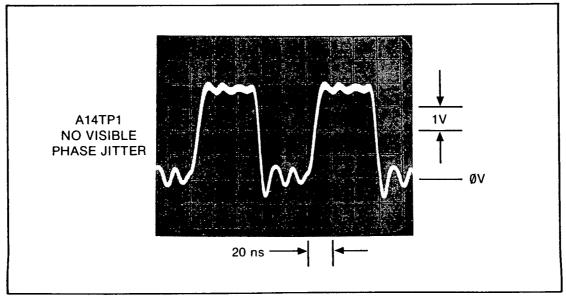


Figure 5-2. 10 MHz Time Base Waveform

5-11. Phase Modulator and Noise Generator Adjustments

- a. Set the HP 5328B FUNCTION switch to T.I. AVG $A \rightarrow B$.
- b. Connect a true rms voltmeter, such as the HP3400A, to the (-) side of A14C29. Adjust A14C18 for a reading of approximately 0.8V rms (the needle of the HP3400A will bounce around but adjust for an average around 0.8V rms).
- c. Observe the noise waveform at the (-) side of A14C29 with the oscilloscope. Large noise spikes should not be present. See Figure 5-3.

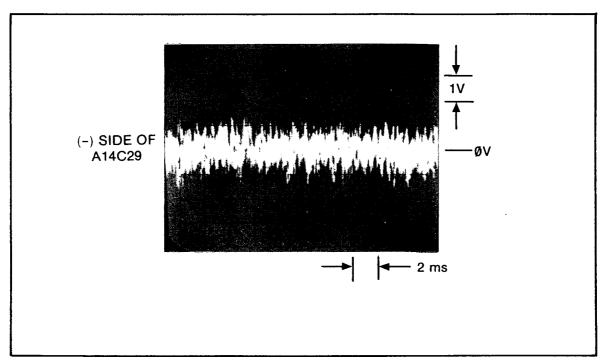


Figure 5-3. Noise Generator Waveform

d. Observe the signal at A14TP6 with an oscilloscope (still externally triggered from the HP5328B 10 MHz output). Adjust A14C23 so that the phase jitter on the sine wave signal is the same on the positive slope as on the negative slope as shown in Figure 5-4.

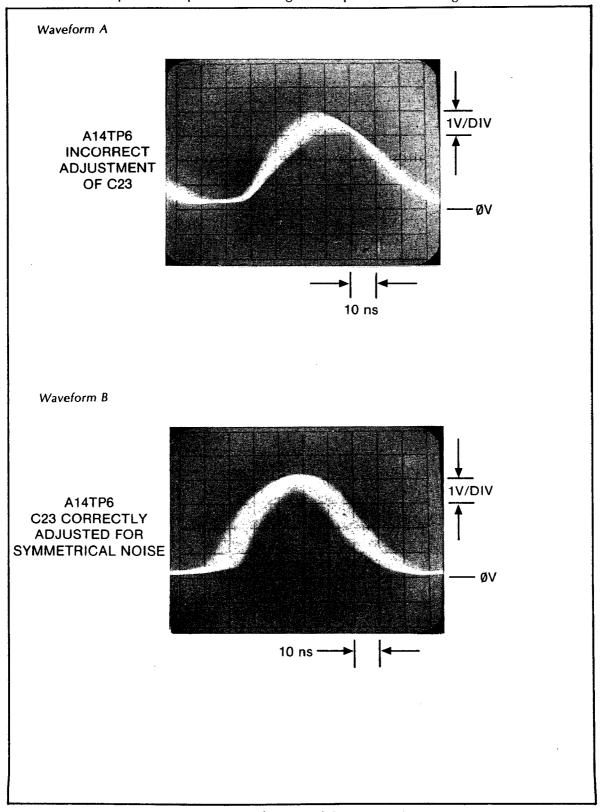


Figure 5-4. Phase Modulator Waveform

e. Observe the 10 MHz signal at A14TP1 with the oscilloscope (still externally triggered). Observe approximately 8 nanoseconds peak-to-peak jitter on the TTL 10 MHz signal as shown in Figure 5-5.

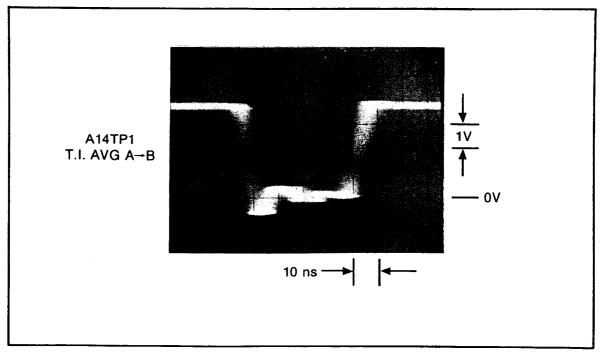


Figure 5-5. TTL 10 MHz Time Base Waveform

EXPLANATION

The phase jitter is added to the 10 MHz Time BAse signal only in the T.I. AVG A \rightarrow B function to insure averaging even when there is coherence between the repetition rate of the time intervals and the internal 10 MHz clock. To determine if the phase jitter circuit is adjusted properly, apply a stable, low frequency signal to the HP5328B A INPUT with the HP5328B in COM A. Select T.I. A \rightarrow B function, RESOLUTION = 1 MHz, N = 1, (-) slope for A and (+) slope for B, and adjust trigger levels for stable triggering. The reading should be stable to \pm 1 count. Switch to T.I. AVG A \rightarrow B (N still = 1) and observe the display. The display should be stable to \pm 2 counts.

A good source for the stable low frequency signal is the rear panel TIME BASE OUT from another 5328B. Set the FUNCTION switch to START CLOCK (a phantom function one clockwise position from STOP) and set RESOLUTION to N = 104 for a 1 kHz TIME BASE OUT signal.

5-12. Digital-To-Analog Converter Adjustment Procedure

5-13. The following adjustment procedure adjusts the A11 D-to-A Converter outputs for accurate rate programmed trigger levels. Measuring the DAC outputs with a DVM is NOT an equivalent procedure. Since the gain through the HP5328B input amplifiers is not exactly equal to 1.00, the signal arriving at the A12U4 comparator is not identical to the signal at the counter's input. As an example, assume the input amplifier is 0.95. Further assume an input signal which goes from 0 volts to 1.0 volt and it is desired to trigger at the 1.0 volt level. Since the signal arriving at A12U4 goes from 0 volt to 0.95 volts (due to the gain of 0.95), the trigger level specified by the DAC to A12U4 must be 0.95 volts. Triggering at 0.95 volts on the A12U4 input signal is the same as triggering at the 1.0 volt level on the original signal. The procedure described in the following paragraphs takes into account the fact that the input amplifier gain is less than 1.0.

- 5-14. The procedure offsets an input signal to the HP5328B by 0 volts, +2.5, and -2.5 volts and programs the A and B channel Trigger levels for 0 volts, +2.5 and -2.5 volts, respectively. For each offset, adjustments are made by observing the A (and B) Channel marker outputs and adjusting for a 50% duty cycle. A 50% duty cycle indicates that the programmed trigger level (which is the center of the hysteresis band) is exactly equal to the dc offset at the signal to the A12U4 comparator.
- 5-15. It is essential for the A and B INPUT Channels to be set for maximum sensitivity before performing the D-to-A Converter adjustment procedure.

Set up the equipment as shown in Figure 5-6. Set the rear panel address switches on the HP5328B as shown in Figure 5-7.

Set the HP654A test oscillator to 20 kHz at a level of 25 mV rms (70 mV peak-to-peak).

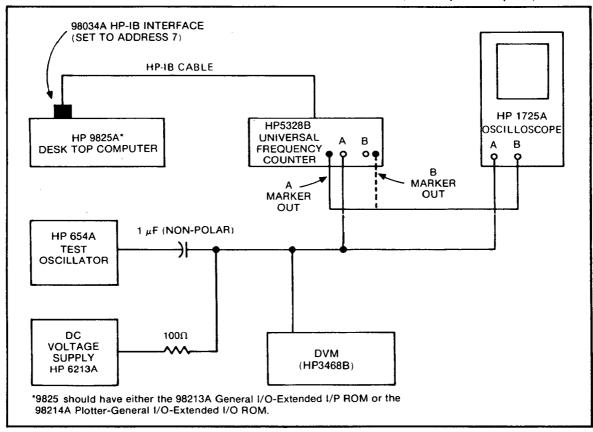


Figure 5-6. Test Setup for DAC Adjustment

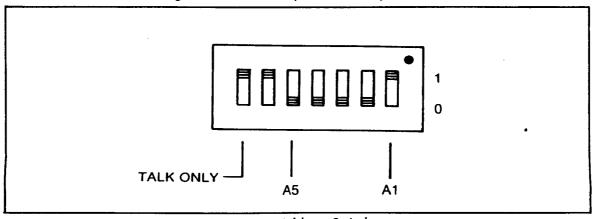


Figure 5-7. Address Switches

Set the oscilloscope A channel for ac coupling and 50 mV per division. Verify that the 20 kHz signal into the counter is 70 mV peak-to-peak.

a. Disconnect the dc supply for a 0.0 volt dc offset on the input signal. Execute from the keyboard of the HP9825A the following:

wrt 701, "PF4G5S13A379+000*B37+000*R"

Monitoring the HP5328B MARKER A OUTPUT on the oscilloscope, adjust A11R21 for a 50% duty cycle in the MARKER OUTPUT signal as shown in *Figure 5-8*. See locations of adjustments on A11 circuit board.

- b. Connect the HP5328B MARKER B OUTPUT to the B Channel of the oscilloscope. Adjust A11R20 for a 50% duty cycle in the MARKER B OUTPUT signal. (The counter has been programmed for COM A.)
- c. Connect the power supply as in Figure 5-6 and adjust for a dc level of 2.50 volts ($\pm 2 \,\text{mV}$) as read on the DVM.
- d. Execute the following from the keyboard on the 9825A:

wrt 701, "PF4G5S13A379+250*B37+250*R"

(Press RECALL on 9825A and simply change DAC voltages as required)

- e. Adjust A11R18 for a 50% duty cycle on the HP5328B MARKER B OUTPUT signal.
- f. Connect the 5328B CHANNEL A MARKER OUTPUT signal to the B Channel of the oscilloscope. Adjust A11R24 for a 50% duty cycle on the A MARKER OUTPUT signal.
- g. Reconfigure the dc power supply for negative voltages and set the voltage for -2.50 volts (± 2 mV).
- h. Execute the following from the keyboard of the 9825A:

wrt 701, "PF4G5S1S3A379-250*B37-250*R"

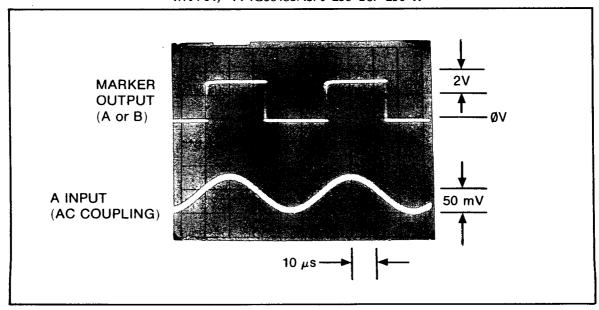


Figure 5-8. Input Signal vs Marker Out

- i. Adjust A11R26 for a 50% duty cycle on the A MARKER OUTPUT signal.
- j. Connect the HP5328B B MARKER OUTPUT to the B Channel of the oscilloscope. Adjust A11R17 for a 50% duty cycle on the B MARKER OUTPUT signal.

5-16. DIGITAL VOLTMETER (OPTION 021 OR PART OF OPTION 050)

- a. Set the 5328B FUNCTION switch to DVM; RESOLUTION switch to 1 Hz, 106; DVM RANGE switch to 10 and READ A, READ B buttons out.
- b. Place a jumper across the DVM's input connectors and adjust resistor A6R28 for a display of 0.0000 ± 1 count.
- c. Disconnect the jumper from the input terminals and connect a dc standard to the DVM's input.
- d. Set the dc standard for +9.5000-volt output and adjust resistor A7R13 for a counter display of +9.5000 ±1 count.
- e. Set the dc standard for a -9.5000-volt output and adjust resistor A7R6 for a display of -9.5000 ±1 count.
- f. Set the DVM RANGE switch to 100.
- g. Set the dc standard for a ± 95.000 -volt output and adjust resistor A6R29 for a display of $\pm 95.000 \pm 1$ count.
- h. Set the DVM RANGE switch to 1000.
- i. Set the dc standard for a +950-volt output and adjust resistor A6R15.
- 5-17. If transformer T1, capacitor C14 or C18 are changed on the A5 PS Auto Range assembly, adjust capacitor C14 as follows:
- a. Connect an Oscilloscope probe to the ground connection on A5 and connect the 10:1 probe to the floating ground (A) on A5.
- b. Set the oscilloscope controls as follows:

TIME/DIV	5 us
VOLTS/DIV	0.02
AC/DC	
Trigger Source Into	ernal

c. Adjust capacitor A5C14 for minimum signal on the floating ground.

5-18. C CHANNEL (OPTION 031 OR PART OF OPTION 050)

- a. Remove the top cover of the 5328B to gain access to variable resistors A8R12, A8A1R13, and R22.
- b. Place the A8 assembly on two extender boards (HP Part No. 05328-62016).
- c. Set the counter front panel controls as follows:

FUNCTION	FREQ C
RESOLUTION	0.1 kHz, 104
SAMPLE RATE	midrange
CHANNEL C ATTN	fully cw

- d. Set A8R12 fully ccw.
- e. Connect the HP8660C Signal Generator (or equivalent) to INPUT C. Set the signal genrator to 1.299 GHz at 100 mV rms (-7dBm).
- f. Slowly decrease the signal generator output level while adjusting variable resistor A8A1R22 for maximum sensitivity (at least 20 mv rms, -21 dBm) and a correct stable display.
- g. Set the signal generator to 90 MHz and an output level of 100 mV rms. Slowly decrease the generator's output level until the counter displays erroneous readings above 100 MHz. (For example, 101.nnnnn).
- h. Adjust resistor A8R12 cw until the counter stops gating.
- i. Set the signal to 90 MHz at 125 mV rms (approximatley -5 dBm).
- j. Connect the voltmeter from A8A1 (pin 1) to ground.
- k. Adjust resistor A8 A1R13 until the voltage at pin 1 switches from approximately 12-15 volts to less than 10 volts.

SECTION VI REPLACEABLE PARTS

6-1. INTRODUCTION

This section contains listings of replaceable parts for the Model 5328B Universal Counter. Table 6-1 provides a reference index to this section's parts list tables.

6-2. EXCHANGE ASSEMBLIES

At Hewlett-Packard's option, some replacement parts for the HP 5328B may be available on an "exchange" basis. Exchange parts have been factory-repaired and tested. They are available only on an "exchange" or "trade-in" basis, and require that a defective equivalent part be returned for credit; thus, you can't order an "exchange" for spare parts stocking — you will have to order the new-part number.

Because a part may be added to or removed from the exchange program at any time, without notice, it is not practical to list exchange parts in this manual. If you find a replacement part is needed, you can ask your nearest Hewlett-Packard Sales Office if an exchange part is available.

If you are ordering an exchange part, you should keep the old part until the new one arrives. You can then use the new part's shipping container to return the old part to Hewlett-Packard.

6-3. PARTS LISTS

Replaceable parts for the HP 5328B and its sub-assemblies are listed as follows:

• Parts for the entire product are listed in Tables 6-2 through 6-11.

Table 6-1. Circuit Diagrams and Parts Lists

Assy Ref Des	Description	HP Part No.	Circuit Diagram	Parts List Table
A1	Motherboard Assembly	05328-60049	8-A-10	6-2
A2	Power Supply Assembly	05328-60034	8-D-2	6-3
-A3	Support Board Assembly	05328-60018	8-A-16	6-4
	Oven Oscillator — 10 MHz	10811-60111	8-A-22	6-4
A4	Function Selector Assembly	05328-60005	8-A-13	6-5
A5	PS Autorange Assembly (See NOTE 1)	05328-60012	8-BB-2	6-6
A6	Buffer Amplifier Assembly (See NOTE 1)	05328-60013	8-BB-3	6-6
A7	VF Converter Assembly (See NOTE 1)	05328-60014	8-BB-4	6-6
A8	1300 MHz C Channel Assembly (See NOTE 2)	05328-60025	8-BC-3	6-7
A8A1	1.3 GHz Board Assembly (See NOTE 2)	05305-60006	8-BC-2	6-7

Table 6-1. Circuit Diagrams and Parts Lists (continued)

Assy Ref Des	Description	HP Part No.	Circuit Diagram	Parts List Table
A9	Not Assigned			
A10	Synchronizer Assembly	05328-60020	8-BA-9	6-8
A11	DAC Assembly	05328-60023	8-BA-11	6-8
A12	Amplifier Assembly	05328-60041	8-BA-8	6-8
A13	Not Assigned			
A14	Multiplier Assembly	05328-60010	8-BA-10	6-8
A15	HP-IB Interface Assembly	05328-60043	8-C-2	6-9
A16	Display Assembly	05328-60033	8-A-14	6-10
A17	DVM Front Board Assembly (See NOTE 1)	05328-60015		6-6
A18	Not Assigned			
A19	Switch Control Assembly	05328-60021	8-BA-7	6-8

NOTES -

- 1. Part of (optional) Digital Voltmeter (Option 021 or part of Option 050)
- 2. Part of (optional) 90-1300 MHz "C" Channel (Option 031 or part of Option 050)

Within each Parts List table below:

- a. Electrical components and mechanical parts are listed in alphanumeric order by reference designation.
- b. Some mechanical parts closely related to a specific electrical component may be listed after the component's entry, with or without a separate reference designation.

The information given for each part consists of:

- a. The Reference Designator ("Ref. Des.") for the part.
- b. The Hewlett-Packard number.
- c. A "Check Digit", in the "CD" column.

The "Check Digit" is based on the HP Part Number, and is used to help reduce part-number problems when parts are ordered.

d. The number of times ("Qty") each part (by HP Part Number) occurs in the list.

The quantity information for each part is given only once — at the first occurrence of the HP Part Number in the list. Later entries having the same HP Number will have no number in the "Qty" column.

The "Qty" information in any list is for that list only.

- e. The description of the part.
- f. A typical manufacturer of the part, in a five-digit code.
 - A Manufacturer's-code-to-name conversion list is provided in Table 6-11.
- g. The manufacturer's number for the part.

6-4. HOW TO ORDER A PART

To order a part that is listed in one of the Parts Lists:

- a. Give the Hewlett-Packard part number.
- b. Indicate the quantity required.
- c. Address the order to the nearest Hewlett-Packard office.

To order a part that is not listed in any of the Parts Lists:

- a. Give the unit's Model Number ("HP 5328B").
- b. Give the unit's complete Serial Number.

(See information at the front of this manual about where to find the Serial Number.)

- c. Give a description of the part and its use.
- d. Give the quantity of parts required.
- e. Address your order to the Hewlett-Packard office closest to you.

6-5. HEWLETT-PACKARD'S DIRECT MAIL ORDER SYSTEM

Within the United States, Hewlett-Packard can supply parts through a direct mail order system. Advantages of using the system are:

- a. Ordering and shipment are done directly, via the HP Parts Center in Mountain View, California.
- b. There is no maximum or minimum amount for any mail order. (There is a minimum order amount for parts ordered through a local HP office when the order requires billing and invoicing).
- c. Transportation charges are pre-paid. (However, there is a handling charge for each order).
- d. There are no invoices. To provide these advantages, a check or money order must accompany each order.

For mail-order forms and specific information about the direct mail order system, contact the Hewlett-Packard office closest to you. Addresses and phone numbers of these HP offices are given in Appendix F of this manual.

6-6. SPARE PARTS KIT

Stocking spare parts for an instrument is often done to ensure quick return to service after a malfunction occurs. Hewlett-Packard has a Spare Parts Kit available for this purpose. The kit consists of selected replaceable assemblies and components for this instrument. The contents of the kit and the Recommended Spares list are based on failure reports and repair data, and parts support for one year. A recommended Spare Parts Kit may be ordered through your nearest Hewlett-Packard office.

Table 6-2. Replaceable Parts

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A1	05328-60049	4	1	BD AY-MOTHER	28480	05328-60049
A1C1 A1C2 A1C3 A1C4 A1C5	0180-0100 0160-0161 0180-0106	3 4 9	1 3 7	NOT ASSIGNED NOT ASSIGNED CAPACITOR-FXD 4.7UF+-10% 35VDC TA CAPACITOR-FXD .01UF +-10% 200VDC POLYE CAPACITOR-FXD GOUF+-20% 6VDC TA	56289 28480 56289	150D475X9035B2 0160-0161 150D606X0006B2
A1C6 A1C7 A1C8 A1C9	0140-0177 0170-0024 0180-0195	0 9 6	1 2 1	CAPACITOR-FXD 400PF +-1% 300VDC MICA CAPACITOR-FXD .022UF +-20% 200VDC POLYE CAPACITOR-FXD .33UF+-20% 35VDC TA NOT ASSIGNED	72136 28480 56289	DM15F401F0300WV1CR 0170-0024 150D334X0035A2
A1C10 A1C11-	0160-0314	9	1	CAPACITOR-FXD .01UF '-5% 400VDC POLYE	84411	663UW10354W2
A1C16 A1C17* A1C18 A1C19	0160-2264 0121-0060	2	1	NOT ASSIGNED CAPACITOR-FXD 20PF +-5% 500VDC CER 0+-30 CAPACITOR-V TRMR-CER 2-8PF 350V PC-MTG NOT ASSIGNED	28480 52763	0160-2264 304322 2/8PF NPO
A1C20 A1C21 A1C22 A1C23-	0160-0161 0180-0210 0160-2055	4 6 9	5 10	CAPACITOR-FXD .01UF +-10% 200VDC POLYE CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480 56289 28480	0160-0161 1500335X0015A2 0160-2055
A1C28 A1C29 A1C30 A1C31 A1C32	0180-0230 0160-0153 0180-0230	0 4 0	4	NOT ASSIGNED CAPACITOR-FXD 1UF+-20% SOVDC TA CAPACITOR-FXD 1000PF +-10% 200VDC POLYE CAPACITOR-FXD 1UF+-20% SOVDC TA NOT ASSIGNED	56289 28480 56289	150D105X0050A2 0160-0153 150D105X0050A2
A1C33 A1C34 A1C35 A1C36 A1C37 A1C37	0180-0210 0160-4554 0180-0210 0160-4554 0180-0106	7 6 7 6 7 9	7	CAPACITOR-FXD .01UF +-20% SOVDC CER CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD .01UF +-20% SOVDC CER CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD .01UF +-20% SOVDC CER CAPACITOR-FXD .01UF+-20% SOVDC TA	56289 28480 56289 28480 56289 28480 56289	0160-4554 150D335X0015A2 0160-4554 150D335X0015A2 0160-4554 150D606X0006B2
A1C38 A1C39 A1C40 A1C41 A1C42	0160-2055 0160-4554 0180-0155 0160-4554 0180-0155	9 7 8 7 8	6	CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 50VDC CER CAPACITOR-FXD 2.2UF+-20% 20VDC TA CAPACITOR-FXD .01UF +-20% 50VDC CER CAPACITOR-FXD 2.2UF+-20% 20VDC TA	28480 28480 56289 28480 56289	0160-2055 0160-4554 150D225X0020A2 0160-4554 150D225X0020A2
A1C43 A1C44 A1C45 A1C46 A1C47	0160-4554 0160-4554 0180-0210 0180-0210 0160-4084	7 7 6 6 8	1	CAPACITOR-FXD .01UF +-20% SOVDC CER CAPACITOR-FXD .01UF +-20% SOVDC CER CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD .1UF +-20% 50VDC CER	28480 28480 56289 56289 28480	0160-4554 0160-4554 150D335X0015A2 150D335X0015A2 0160-4084
A1C48 A1C49 A1C50 A1C51 A1C52	0180-0155 0180-0155 0160-3879 0160-3879 0180-0155	.8 8 7 7 8	5	CAPACITOR-FXD 2.2UF+-20% 20VDC TA CAPACITOR-FXD 2.2UF+-20% 20VDC TA CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 2.2UF+-20% 20VDC TA	56289 56289 28480 28480 56289	150D225X0020A2 150D225X0020A2 0160-3879 0160-3879 150D225X0020A2
A1CS3	0180-0155	8		CAPACITOR-FXD 2.2UF+-20% 20VDC TA	56289	150D225X0020A2
A1CR1 A1CR2 A1CR3 A1CR4 A1CR5	1901-0040 1901-0999 1901-0050	9	19	DIODE-SWITCHING 30V SOMA 2NS DO-35 NOT ASSIGNED DIODE-SCHOTTKY 20V 10NS DO-35 NOT ASSIGNED DIODE-SWITCHING 80V 200MA 2NS DO-35	28480 28480 28480	1901-0040 1901-0999 1901-0050
A1CR6 A1CR7 A1CR8 A1CR9 A1CR10	1901-0050 1901-0535 1902-3082 1901-0040 1901-0040	3 9 9 1	1	DIODE-SWITCHING 80V 200MA 2NS DO-35 DIODE-SM SIG SCHOTTKY DIODE-ZNR 4.64V 5% DO-35 PD=.4W DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35	28480 28480 28480 28480 28480	1901-0050 1901-0535 1902-3082 1901-0040 1901-0040
A1CR11 A1CR12 A1CR13 A1CR14 A1CR15	1901-0040 1901-0040 1901-0040 1901-0040 1901-0999	1 1 1 9		DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SCHOTTKY 20V 10NS DO-35	28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0040 1901-0999

Table 6-2. Replaceable Parts (continued)

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A1CR16 A1CR17 A1CR18 A1CR19 A1CR20	1901-0040 1901-0040 1901-0999 1901-0040 1901-0040	1 1 9 1 1		DIODE-SWITCHING 30V SOMA 2NS DO-35 DIODE-SWITCHING 30V SOMA 2NS DO-35 DIODE-SCHOTTKY 20V 10NS DO-35 DIODE-SWITCHING 30V SOMA 2NS DO-35 DIODE-SWITCHING 30V SOMA 2NS DO-35	28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0999 1901-0040 1901-0040
A1CR21	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040
A1L1	9100-2276	9	1	INDUCTOR RF-CH-MLD 100UH 10% .105DX.26LG	28480	9100-2276
A1Q1 A1Q2 A1Q3 A1Q4 A1Q5	1854-0071 1853-0015 1853-0015 1854-0071 1854-0071	77777	10 2	TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR PNP SI PD=200MW FT=500MHZ TRANSISTOR PNP SI PD=200MW FT=500MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480 28480 28480 28480	1854-0071 1853-0015 1853-0015 1854-0071 1854-0071
A1Q6 A1Q7 A1Q8 A1Q9 A1Q10	1854-0092 1854-0071 1854-0071 1854-0071 1853-0036	2 7 7 7 2		TRANSISTOR NPN SI PD=200MW FT=600MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR PNP SI PD=310MW FT=250MHZ	28480 28480 28480 28480 28480	1854-0092 1854-0071 1854-0071 1854-0071 1853-0036
A1R1 A1R2 A1R3 A1R4 [®] A1R5	0698-3444 1810-0055 0683-2725 0683-3355 1810-0055	1 5 8 2 5		RESISTOR 316 1% .125W F TC=0+-100 NETWORK-RES 9-SIP10.0K OHM X 8 RESISTOR 2.7K 5% .25W FC TC=-400/+700 RESISTOR 3.3M 5% .25W FC TC=-900/+1100 NETWORK-RES 9-SIP10.0K OHM X 8	24546 28480 01121 01121 28480	C4-1/8-T0-316R-F 1810-0055 CB2725 CB3355 1810-0055
A1R6 A1R7 A1R8 A1R9 A1R10	0683-3315 0683-1035 0683-4725 0683-1525 0683-1025	4 1 2 4 9	35 8 4	RESISTOR 330 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 4.7K 5% .25W FC TC=-400/+700 RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB3315 CB1035 CB4725 CB1525 CB1025
A1R11 A1R12 A1R13 A1R14 A1R15	0683-1025 0683-1025 0683-1035 0683-4725 1810-0041	9 9 1 2 9		RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 4.7K 5% .25W FC TC=-400/+700 NETWORK-RES 9-SIP2.7K 0HM X 8	01121 01121 01121 01121 28480	CB1025 CB1025 CB1035 CB4725 1810-0041
A1R16 A1R17 A1R18 A1R19 A1R20	0683-1035 0683-1525 1810-0055 0683-1525	1 4 5 4		NOT ASSIGNED RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 1.5K 5% .25W FC TC=-400/+700 NETWORK-RES 9-SIP10.0K OHM X 8 RESISTOR 1.5K 5% .25W FC TC=-400/+700	01121 01121 28480 01121	CB1035 CB1525 1810-0055 CB1525
A1R21 A1R22 A1R23 A1R24 A1R25	0698-4037 0683-2715 0683-2015 0683-1025 0683-1525	0 6 9 9	8 5	RESISTOR 46.4 1% .125W F TC=0+-100 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 200 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1.5K 5% .25W FC TC=-400/+700	24546 01121 01121 01121 01121	C4-1/8-T0-46R4-F CB2715 CB2015 CB1025 CB1525
A1R26 A1R27 A1R28 A1R29 A1R30	0683-1025 0757-0928 1810-0055 0757-0952 0683-1035	9 6 5 6	1	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1.5K 2% .125W F TC=0+-100 NETWORK-RES 9-SIP10.0K OHM X 8 RESISTOR 15K 2% .125W F TC=0+-100 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 24546 28480 24546 01121	CB1025 C4-1/8-T0-1501-G 1810-0055 C4-1/8-T0-1502-G CB1035
A1R31 A1R32 A1R33 A1R34 A1R35	0683-5105 0683-1035 0683-1035 0683-2715 0683-5115	4 1 1 6		RESISTOR 51 5% .25W FC TC=-400/+500 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 510 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB5105 CB1035 CB1035 CB2715 CB5115
A1R36 A1R37 A1R38 A1R39 A1R40	0683-1035 0683-2715 0683-1035 0683-1035 0683-1035	1 6 1 1		RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB1035 CB2715 CB1035 CB1035 CB1035
A1R41 A1R42 A1R43 A1R44 A1R45	0683-1035 1810-0055 0683-1025 0683-1035 0683-1035	1 5 9		RESISTOR 10K 5% .25W FC TC=-400/+700 NETWORK-RES 9-SIP10.0K OHM X 8 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 28480 01121 01121 01121	CB1035 1810-0055 CB1025 CB1035 CB1035

Table 6-2. Replaceable Parts (continued)

	T	1		c o 2. Replaceable Falls (continue	·	
Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A1R46 A1R47	0683-1545	8	1	RESISTOR 150K 5% .25W FC TC=-800/+900 NOT ASSIGNED	01121	CB1545
A1R48 A1R49-	0683-1035	1		RESISTOR 10K 5% .25W FC TC=-400/+700	01121	CB1035
A1R51				NOT ASSIGNED		
A1R52 A1R53	0683-5635	5	1	RESISTOR 56K 5% .25W FC TC=-400/+800 NOT ASSIGNED	01121	CB5635
A1R54 A1R55	0683-1025	9		NOT ASSIGNED RESISTOR 1K 5% .25W FC TC=-400/+600	01121	CB1025
A1R56	0683-4725	2		RESISTOR 4.7K 5% .25W FC TC=-400/+700	01121	CB4725
A1R57 A1R58	0683-6815 0683-1025	5 9	1	RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121	CB6815 CB1025
A1R59 A1R60	0683-5605 0683-5605	9	10	RESISTOR 56 5% .25W FC TC=-400/+500 RESISTOR 56 5% .25W FC TC=-400/+500	01121 01121	CB5605 CB5605
A1R61	0683-5605	9		RESISTOR 56 5% .25W FC TC=-400/+500	01121	CB5605
A1R62 A1R63	0683-5605 0683-5605	9		RESISTOR 56 5% .25₩ FC TC=-400/+500 RESISTOR 56 5% .25₩ FC TC=-400/+500	01121 01121	CB5605 CB5605
A1R64 A1R65	0683-5605 0683-5605	9		RESISTOR 56 5% .25W FC TC=-400/+500 RESISTOR 56 5% .25W FC TC=-400/+500	01121 01121	CB5605 CB5605
A1R66	0683-5605	9		RESISTOR 56 5% .25W FC TC=-400/+500	01121	CB5605 CB5605
A1R67 A1R68	0683-5605 0683-1035	9		RESISTOR 56 5% .25W FC TC=-400/+500 RESISTOR 10K 5% .25W FC TC=-400/+700	01121	CB1035
A1R69 A1R70	0757-0935 0757-0950	5	1	RESISTOR 3K 2% .125W F TC=0+-100 RESISTOR 12K 2% .125W F TC=0+-100	24546 24546	C4-1/8-T0-3001-G C4-1/8-T0-1202-G
A1R71	0757-0279 0757-0931	0	1	RESISTOR 3.16K 1% .125W F TC=0+-100 RESISTOR 2K 2% .125W F TC=0+-100	24546 24546	C4-1/8-T0-3161-F C4-1/8-T0-2001-G
A1R72 A1R73 A1R74	0683-1035 0683-1035		'	RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121	CB1035 CB1035
A1R75 A1R76	0683-1035 0683-1035 0683-1015	1 7	1	RESISTOR 10K 5% .25W FC 1C=-400/+700 RESISTOR 10K 5% .25W FC 1C=-400/+700 RESISTOR 100 5% .25W FC 1C=-400/+500	01121 01121 01121	CB1035 CB1015
A1R77	0683-5605	9	· ·	RESISTOR 56 5% .25W FC TC=-400/+500	01121	CB5605
A1R78 A1R79	0683-4315 0683-1025	6 9	1	RESISTOR 430 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121	CB4315 CB1025
A1R80 A1R81	0683-1035 0683-1035	1		RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121	CB1035 CB1035
A1R82	0683-6825	7	1	RESISTOR 6.8K 5% .25W FC TC=-400/+700	01121	CB6825
A1R83 A1R84	0698-4037 0757-0422	5	1	RESISTOR 46.4 1% .125W F TC=0+-100 RESISTOR 909 1% .125W F TC=0+-100	24546 24546	C4-1/8-T0-46R4-F C4-1/8-T0-909R-F
A1S1	3101-1977	1	3	SWITCH-SL DPDT SUBMIN .5A 125VAC PC	28480	3101-1977
A1S2 A1S3	3101-1977 3101-1977	;		SWITCH-SL DPDT SUBMIN .5A 125VAC PC SWITCH-SL DPDT SUBMIN .5A 125VAC PC	28480 28480	3101-1977 3101-1977
A1U1 A1U2	1820-0055 1820-1056	6	2	IC CNTR TTL DECD SYNCHRO POS-EDGE-TRIG IC SCHMITT-TRIG TTL NAND QUAD 2-INP	01295 01295	SN7490AN SN74132N
A1U3 A1U4*	1820-0175 1820-1401	1 8	1	IC INV TTL HEX 1-INP	01295 28480	SN7405N 1820-1401
A1US	1820-0513	Ĭ	1	IC GATE ITL AND QUAD 2-INP	01295	SN7409N
A1U6 A1U7	1820-0282 1820-0511	1 9	4	IC GATE TTL EXCL-OR QUAD 2-INP IC GATE TTL AND QUAD 2-INP	01295 01295	SN7486N SN7408N
A1U8 A1U9	1820-0174 1820-0661	0	6 2	IC INV TTL HEX IC GATE TTL OR QUAD 2-INP	01295 01295	SN7404N SN7432N
A1U10	1820-2316	6	1	IC CNTR IIL DECD HEX	28480	IDB2-4
A1U11 A1U12	1820-0513 1820-3237	1 2	1	IC GATE TIL AND QUAD 2-INP IC LCH TIL ALS TRANSPARENT NEG-EDGE-TRIG	01295 28480	SN7409N 1820-3237
A1U13 A1U14	1820-1442	7	1 1	NOT ASSIGNED IC CNIR TIL LS DECD ASYNCHRO	01295	SN74LS290N
A1U15	1820-0803	2	1	IC GATE ECL OR-NOR TPL	04713	MC10105P
A1U16 A1U17 A1U18	1820-0537 1820-0068 1820-0174	9	1 2	IC SCHMITT-TRIG TTL NAND DUAL 4-INP IC GATE TTL NAND TPL 3-INP IC INV TTL HEX	01295 01295 01295	SN7413N SN7410N SN7404N
A1U19 A1U20	1820-0174 1820-0077 1820-0055	2 6	2	IC TAY TIL MEX IC FF TIL D-TYPE POS-EDGE-TRIG CLEAR IC CNTR TIL DECD SYNCHRO POS-EDGE-TRIG	01295 01295	SN7474N SN7490AN
A1U21	1820-2317	7	1	IC MISC IIL	28480	IDB9-TL
A1U22 A1U23	1820-0269 1820-0328	4	2 2	IC GATE TIL NAND QUAD 2-INP IC GATE TIL NOR QUAD 2-INP	01295 01295	SN7403N SN7402N
A1U24 A1U25	1820-0282 1820-0301	1 5	5	IC GATE TTL EXCL-OR QUAD 2-INP IC LCH TTL D-TYPE 4-BIT	01295 01295	SN7486N SN7475N
			L	<u> </u>		

Table 6-2. Replaceable Parts (continued)

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A1U26 A1U27 A1U28 A1U29 A1U30	1820-0301 1820-0301 1820-0538 1820-0282 1820-0282	5 5 0 1	2	IC LCH TTL D-TYPE 4-BIT IC LCH TTL D-TYPE 4-BIT IC GATE TTL NOR DUAL 4-INP IC GATE TTL EXCL-OR QUAD 2-INP IC GATE TTL EXCL-OR QUAD 2-INP	01295 01295 01295 01295 01295	SN7475N SN7475N SN7423N SN7486N SN7486N
A1U31 A1U32 A1U33 A1U34 A1U35	1820-0301 1820-0538 1820-0174 1820-0174 1820-0174	5 0 0 0		IC LCH TTL D-TYPE 4-BIT IC GATE TTL NÓR DUAL 4-INP IC INV TTL HEX IC INV TTL HEX IC INV TTL HEX	01295 01295 01295 01295 01295	SN7475N SN7423N SN7404N SN7404N SN7404N
A1U36 A1U37 A1U37	1820-0174 1818-2252 1818-2274	0 4 0	1	IC INV TTL HEX IC-ROM-NMOS 4K USED WITHOUT OPTION 031 OR 050 IC NMOS 4096 (4K) ROM USED WITH OPTION 031 OR 050	01295 28480 28480	SN7404N 1818-2252 1818-2274
A1U38 A1U39 A1U40 A1U41	1820-0661 1820-0214 1820-0054 1820-1644	0 9 5 1	1 1	IC GATE TTL OR QUAD 2-INP IC DCDR TTL BCD-TO-DEC 4-TO-10-LINE IC GATE TTL NAND QUAD 2-INP IC DCDR TTL LS BCD-TO-7-SEG 4-TO-7-LINE	01295 01295 01295 01295	SN7432N SN7442AN SN7400N SN74LS248N
A1 W1 A1 W2 A1 W3 A1 W4 A1 W5	8159-0005 8159-0005 8159-0005	0 0 0		NOT ASSIGNED RESISTOR-ZERO OHMS 22 AWG LEAD DIA NOT ASSIGNED RESISTOR-ZERO OHMS 22 AWG LEAD DIA RESISTOR-ZERO OHMS 22 AWG LEAD DIA RESISTOR-ZERO OHMS 22 AWG LEAD DIA	28480 28480 28480	8159-0005 8159-0005 8159-0005
A1W6	8159-0005	0		RESISTOR-ZERO OHMS 22 AWG LEAD DIA	28480	8159-0005
A1 XA1 A1 XA2 A1 XA3 A1 XA4A A1 XA4B	1251-2035 1251-2026 1251-2026	9 8 8	4 9	NOT ASSIGNED NOT ASSIGNED CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS	28480 28480 28480	1251-2035 1251-2026 1251-2026
A1 XA5 A1 XA5B A1 XA6 A1 XA7 A1 XA8A A1 XA8B	1251-2035 1251-2026 1251-2035 1251-2035 1251-2026 1251-2026	989988		CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS	28480 28480 28480 28480 28480 28480	1251 - 2035 1251 - 2026 1251 - 2035 1251 - 2035 1251 - 2026 1251 - 2026
A1XA10 A1XA11 A1XA12 A1XA13	1251-2026 1251-2026 1251-2026	8 8 8		CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS NOT ASSIGNED	28480 28480 28480	1251-2026 1251-2026 1251-2026
A1XA14 A1XA-Y1	1251-2026	8	1	CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS SOCKET-XSTR 3-CONT TO-5 DIP-SLDR	28480 28480	1251-2026 1200-0153
A1XJ1	1200-0193		2	SOCKET-IC 28-CONT DIP DIP-SLDR	28480	1200-0193
A1 XU1 - A1 XU3 A1 XU4 A1 XU5 - A1 XU1 1	1200-0825	4	1	NOT ASSIGNED SOCKET-IC 20-CONT DIP DIP-SLDR NOT ASSIGNED	28480	1200-0825
A1XU12 A1XU13-	1200-0473	8	2	SOCKET-IC 16-CONT DIP DIP-SLDR	28480	1200-0473
A1XU20 A1XU21 A1XU22- A1XU36	1200-0473	8		NOT ASSIGNED SOCKET-IC 16-CONT DIP DIP-SLDR NOT ASSIGNED	28480	1200-0473
A1XU37	1200-0567	,		SOCKET-IC 28-CONT DIP DIP-SLDR	28480	1200-0567
A1Y1	0410-0405	0	1	CRYSTAL-QUARTZ 10.000 MHZ	28480	0410-0405
	0360-0124 0380-0640	3 0	13 1	A1 MISCELLANEOUS CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND STANDOFF-RND .5-IN-LG 6-32THD .25-IN-OD	28480 00000	0360-0124 ORDER BY DESCRIPTION

Table 6-3. Replaceable Parts

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A2	05328-60034	7	1	BD AY - PWR SUP	28480	05328-60034
A2C1 A2C2 A2C3* A2C4* A2C5*	0180-2842 0180-2842 0160-0576 0160-0576 0160-2203	4 4 5 9	2 3 1	CAPACITOR-FXD 4500UF+-20% 35VDC AL CAPACITOR-FXD 4500UF+-20% 35VDC AL CAPACITOR-FXD .1UF +-20% 50VDC CER CAPACITOR-FXD .1UF +-20% 50VDC CER CAPACITOR-FXD 91PF +-5% 300VDC MICA 0+70	28480 28480 28480 28480 28480	0180-2842 0180-2842 0160-0576 0160-0576 0160-2203
A2C6* A2C7 A2C8 A2C9 A2C10	0160-0945 0180-0562 0160-3879 0180-2827 0180-2827	2 1 7 5 5	1 1 2	CAPACITOR-FXD 910PF +-5% 100VDC MICA CAPACITOR-FXD 33UF+-20% 10VDC TA CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 47UF+100-10% 40VDC AL CAPACITOR-FXD 47UF+100-10% 40VDC AL	28480 56289 28480 28480 28480	0160-0945 1960336X0010KA1 0160-3879 0180-2827 0180-2827
A2C11 A2C12 A2C13 A2C14 A2C15	0180-2832 0180-2832 0180-0418 0160-0570 0160-2199	2 2 6 9 2	1	CAPACITOR-FXD 1000UF+100-10% 12VDC AL CAPACITOR-FXD 1000UF+100-10% 12VDC AL CAPACITOR-FXD 1UF+-20% 35VDC TA CAPACITOR-FXD 220PF +-20% 100VDC CER CAPACITOR-FXD 30PF +-5% 300VDC MICA	28480 28480 28480 20932 28480	0180-2832 0180-2832 0180-0418 5024EM100RD221M 0160-2199
A2C16 A2C17 A2C18 A2C19 A2C20*	0160-2199 0180-0587 0180-0587 0160-0576 0160-3879	2 0 0 5 7	2	CAPACITOR-FXD 30PF +-5% 300VDC MICA CAPACITOR-FXD 47UF+100-10% 25VDC AL CAPACITOR-FXD 47UF+100-10% 25VDC AL CAPACITOR-FXD .1UF +-20% 50VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER	28480 56289 56289 28480 28480	0160-2199 6720476H025CC58 6720476H025CC58 0160-0576 0160-3879
A2C21 A2C22 A2C23 A2C24 A2C25	0160-0128 0160-0128 0180-2821 0180-2821 0180-2821	3 3 9 9	3	CAPACITOR-FXD 2.2UF +-20% 50VDC CER CAPACITOR-FXD 2.2UF +-20% 50VDC CER CAPACITOR-FXD 22UF+-20% 35VDC TA CAPACITOR-FXD 22UF+-20% 35VDC TA CAPACITOR-FXD 22UF+-20% 35VDC TA	28480 28480 28480 28480 28480	0160-0128 0160-0128 0180-2821 0180-2821 0180-2821
A2C26	0160-4355	6	1	CAPACITOR-FXD .01UF +-10% 250VAC(RMS)	28480	0160-4355
A2CR1 A2CR2 A2CR3 A2CR4 A2CR5	1902-0774 1902-0774 1901-1086 1901-1086	0 0 7 7	2	DIODE-ZNR 12.1V 10% DO-15 PD=1W DIODE-ZNR 12.1V 10% DO-15 PD=1W DIODE-PWR RECT 50V SA 200NS DIODE-PWR RECT 50V SA 200NS NOT ASSIGNED	28480 28480 04713 04713	1902-0774 1902-0774 MR820 MR820
A2CR6 A2CR7 A2CR8 A2CR9 A2CR10	1902-0522 1902-0522 1901-0040 1901-0040	6 6 1	2	NOT ASSIGNED DIODE-ZNR 1N5340B 6V 5% PD=5W IR=1UA DIODE-ZNR 1N5340B 6V 5% PD=5W IR=1UA DIODE-SWITCHING 30V 50MA 2NS D0-35 DIODE-SWITCHING 30V 50MA 2NS D0-35	04713 04713 28480 28480	1N5340B 1N5340B 1901-0040 1901-0040
A2CR11 A2CR12 A2CR13	1902-0632 1902-0632 1901-0638	9		DIODE-ZNR 1NS354B 17V 5% PD=5W TC=+75% DIODE-ZNR 1NS354B 17V 5% PD=5W TC=+75% DIODE-FW BRDG 100V 4A	04713 04713 04713	1N53548 1N5354B MDA-970-2
A2F1 A2F2	2110-0002 2110-0002	9	2	FUSE 2A 250V NTD 1.25X.25 UL FUSE 2A 250V NTD 1.25X.25 UL	75915 75915	312002 312002
A2L1 A2L2 A2L3	9100-3017 9100-3017 9100-3139	1	2 3 1	300 MH AT 5 AMP DC 300 MH AT 5 AMP DC INDUCTOR 75UH 15% .5DX.875LG	28480 28480 28480	9100-3017 9100-3017 9100-3139
A2Q1 A2Q2 A2Q3 A2Q4 A2Q5	1853-0363 1854-0635 1853-0326 1854-0634 1854-0492		2 3 3 1 8 1 6	TRANSISTOR PNP SI PD=50W FT=20MHZ TRANSISTOR NPN SI PD=50W FT=20MHZ TRANSISTOR PNP SI PD=1W FT=50MHZ TRANSISTOR NPN SI PD=1W FT=50MHZ TRANSISTOR NPN SI PD=350MW FT=250MHZ	03508 03508 04713 04713	X45H281 D44H5 MPS-U51 MPS-U01 MPS3643
A2Q6 A2Q7 A2Q8 A2Q9 A2Q10	1853-0016 1853-0363 1854-0635 1854-0492 1853-0016		8 2 8 9 6 8	TRANSISTOR PNP SI TO-92 PD=300MW TRANSISTOR PNP SI PD=50W FT=20MHZ TRANSISTOR NPN SI PD=50W FT=20MHZ TRANSISTOR NPN SI PD=350MW FT=250MHZ TRANSISTOR PNP SI TO-92 PD=300MW	28480 03508 03508 04713 28480	1853-0016 X45H281 D44H5 MPS3643 1853-0016
A2Q11 A2Q12	1854-0635 1884-0055		9 1	TRANSISTOR NPN SI PD=50W FT=20MHZ THYRISTOR-TRIAC	03508 28480	D44H5 1884-0055
A2R1 A2R2 A2R3 A2R4 A2R5	0812-0094 0812-0094 0683-1015 0683-1015 0683-1025		5 2 5 7 3 7 9 11	RESISTOR 390 3% 3W PW TC=0+-20	28480 28480 01121 01121 01121	0812-0094 0812-0094 CB1015 CB1015 CB1025

Table 6-3. Replaceable Parts (continued)

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A2R6 A2R7 A2R8 A2R9 A2R10	0683-1025 0683-6815 0683-6815 0698-3620 0698-3620	95555	2	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR 100 5% .2W MO TC=0+-200 RESISTOR 100 5% .2W MO TC=0+-200	01121 01121 01121 01121 28480 28480	CB1025 CB6815 CB6815 CB6815 0698-3620 0698-3620
A2R11 A2R12 A2R13 A2R14 A2R15	0683-8245 0683-1055 0683-4725 0683-1025 0683-1025	9 5 2 9	1 1 2	RESISTOR 820K 5% .25W FC TC=-800/+900 RESISTOR 1M 5% .25W FC TC=-800/+900 RESISTOR 4.7K 5% .25W FC TC=-400/+700 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB8245 CB1055 CB4725 CB1025 CB1025
A2R16 A2R17 A2R18 A2R19 A2R20	0698-3160 0757-0428 0757-0454 0683-1025 0683-1025	8 1 3 9	1 1	RESISTOR 31.6K 1% .125W F TC=0+-100 RESISTOR 1.62K 1% .125W F TC=0+-100 RESISTOR 33.2K 1% .125W F TC=0+-100 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600	24546 24546 24546 01121 01121	C4-1/8-T0-3162-F C4-1/8-T0-1621-F C4-1/8-T0-3322-F C81025 C81025
A2R21 A2R22 A2R23 A2R24 A2R25	0683-1025 0683-1025 0757-0283 0757-0283 0757-0280	99663	2	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 2K 1% .125W F TC=0+-100 RESISTOR 2K 1% .125W F TC=0+-100 RESISTOR 1K 1% .125W F TC=0+-100	01121 01121 24546 24546 24546	CB1025 CB1025 C4-1/8-T0-2001-F C4-1/8-T0-2001-F C4-1/8-T0-1001-F
A2R26 A2R27 A2R28 A2R29 A2R30	0757-0280 2100-1738 0757-0280 0683-4725 0683-1135	39322	1	RESISTOR 1K 1% .125W F TC=0+-100 RESISTOR-TRMR 10K 10% C TOP-ADJ 1-TRN RESISTOR 1K 1% .125W F TC=0+-100 RESISTOR 4.7K 5% .25W FC TC=-400/+700 RESISTOR 11K 5% .25W FC TC=-400/+800	24546 73138 24546 01121 01121	C4-1/8-T0-1001-F 82PR10K C4-1/8-T0-1001-F CB4725 CB1135
A2R31 A2R32 A2R33 A2R34 A2R35	0683-2715 0811-3050 0683-1015 0812-0021 0812-0021	6 7 8 8	1 1 2	RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR .75 5% .5W PW TC=0+-150 RESISTOR 100 5% .25W FC TC=-400/+500 RESISTOR .47 5% 3W PW TC=0+-90 RESISTOR .47 5% 3W PW TC=0+-90	01121 75042 01121 91637 91637	CB2715 BW20-1-3/4-J CB1015 CW281-3-T2-47/100-J CW281-3-T2-47/100-J
A2R36 A2R37 A2R38 A2R39 A2R40	0683-1025 0683-1025 0683-1025 0683-1815 0698-3441	99958	1	RESISTOR 1K 5% .25W FC IC=-400/+600 RESISTOR 1K 5% .25W FC IC=-400/+600 RESISTOR 1K 5% .25W FC IC=-400/+600 RESISTOR 180 5% .25W FC IC=-400/+600 RESISTOR 215 1% .125W F IC=0+-100	01121 01121 01121 01121 01121 24546	CB1025 CB1025 CB1025 CB1815 C4-1/8-T0-215R-F
A2U1 A2U2 A2U3 A2U4 A2U5	1826-0065 1826-0065 1820-0477 1820-0477 1820-0439	0 0 6 6 0	2 2	IC COMPARATOR PRCN 8-DIP-P PKG IC COMPARATOR PRCN 8-DIP-P PKG IC OP AMP GP 8-DIP-P PKG IC OP AMP GP 8-DIP-P PKG IC OP AMP GP 8-DIP-P PKG IC V RGLTR 14-DIP-P	S0545 S0545 S0545 S0545 O7263	UPC311C UPC311C UPC301AC UPC301AC 723PC
A2U6 A2XF1	1990-0845 2110-0269	1	1 2	OPTO-ISOLATOR IF=50MA-MAX VAX=250V	28480	1990-0845
A2XF2	2110-0269	ŏ		FUSEHOLDER-CLIP TYPE.25D-FUSE FUSEHOLDER-CLIP TYPE.25D-FUSE	28480 28480	2110-0269 2110-0269
A2XQ1 A2XQ2 A2XQ3-	1200-0666 1200-0666	1	5	SOCKET-XSTR 3-CONT SOCKET-XSTR 3-CONT	28480 28480	1200-0666 1200-0666
A2XQ6 A2XQ7	1200-0666	1		NOT ASSIGNED SOCKET-XSTR 3-CONT	28480	1200-0666
A2XQ8 A2XQ9	1200-0666	1		SOCKET-XSTR 3-CONT NOT ASSIGNED	28480	1200-0666
A2XQ10 A2XQ11 A2XQ12	1200-0666 1205-0011	1 0	1	NOT ASSIGNED SOCKET-XSTR 3-CONT HEAT SINK TO-5/TO-39-CS	28480 28480	1200-0666 1205-0011

Table 6-4. Replaceable Parts

Reference HF						
Designation Nu	IP Part Iumber	OD	Qty	Description	Mfr Code	Mfr Part Number
A3 053 A3A1 101 A3C1 01: A3C2 01: A3C3 01: A3C4* 01: A3C5 01 A3J1 12 A3L1 91 A3L2 91 A3R1 06 A3R2 07 A3R3 07 A3R4 06 A3R5 06 A3U1 18	15328-60018 10811-60111 10180-2125 1160-3466 10180-2818 10180-0116 1251-2035 19100-1788 10380-0310 10683-0825 10757-0200 10757-0439 10683-1225 10683-1015 11820-0439 18159-0005		1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Description BD AY-OVEN OSC OPTION 010 QUARTZ OSC 10MHZ CAPACITOR-FXD 15UF+-5% 20VDC TA CAPACITOR-FXD 1.0F +-10% 1KVDC CER CAPACITOR-FXD 1.1F +-20% 50VDC CER CAPACITOR-FXD 6.8UF+-10% 35VDC TA CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS INDUCTOR RF-CH-MLD 10MH 5% .25DX.75LG CORE-FERRITE CHOKE-WIDEBAND; IMP:>680 STANDOFF-RVI-ON .75-IN-LG 6-32THD RESISTOR 8.2 5% .25W FC TC=-400/+500 RESISTOR 6.81K 1% .125W F TC=0+-100 RESISTOR 6.81K 1% .125W F TC=0+-100 RESISTOR 1.2K 5% .25W FC TC=-400/+500 IC V RGLTR 14-DIP-P RESISTOR-ZERO OHMS 22 AWG LEAD DIA		Mfr Part Number 05328-60018 10811-60111 1500156X5020B2 0160-3466 0160-4484 0180-2818 1500685X9035B2 1251-2035 9140-0131 9100-1788 ORDER BY DESCRIPTION CB82G5 C4-1/8-T0-5621-F C4-1/8-T0-6811-F CB1225 CB1015 723PC 8159-0005

Table 6-4. Replaceable Parts (continued)

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Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number				
A3A1	10811-60115	2	1	CIRCUIT BOARD COMPONENTS SERIES 2332A	28480	10811-60115				
A3A1C1	0121-0511	6	1	•	1					
A3A1C2	0160-0576	5	9	TUNING CAPACITOR CAPACITOR-FXD .1UF +-20% 50VDC CER	28480 28480	0121-0511 0160-0576				
A3A1C3 A3A1C4	0160-5109 0160-0576	0 5	1	CAPACITOR-FXD 15PF +-5% 50VDC CER 0+-30 CAPACITOR-FXD .1UF +-20% 50VDC CER	28480 28480	0160-5109 0160-0576				
A3A1C5	0160-0576	5		CAPACITOR-FXD .1UF +-20% 50VDC CER	28480	0160-0576				
A3A1C6 A3A1C7	0160-4935 0160-0576	8 5	1	CAPACITOR-FXD 510PF +-1% 100VDC CER CAPACITOR-FXD .1UF +-20% 50VDC CER	28 4 80 28480	0160-4935 0160-0576				
A3A1C8 A3A1C9	0160-5110 0160-4019	3	1 2	CAPACITOR-FXD 62PF +-1% 50VDC CER 0+-30 CAPACITOR-FXD .01UF +-20% 100VDC CER	28480 28480	0160-5110				
A3A1C10	0160-3874	2	2	CAPACITOR-FXD 10PF +5PF 200VDC CER	28480	0160-4019 0160-3874				
A3A1C11	0160-4019	9	_	CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-4019				
A3A1C12 A3A1C13	0160-4512 0160-3879	7	2 4	CAPACITOR-FXD 120PF +-5% 200VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER	28480 28480	0160-4512 0160-3879				
A3A1C14 A3A1C15	0160-0576 0180-2617	5	1	CAPACITOR-FXD .1UF +-20% 50VDC CER CAPACITOR-FXD 6.8UF+-10% 35VDC TA	28480 25088	0160-0576 D6R8GS1B35K				
A3A1C16	0160-0576	5		CAPACITOR-FXD .1UF +-20% 50VDC CER	28480	0160-0576				
A3A1C17 A3A1C18	0160-3874 0160-4947	2	1	CAPACITOR-FXD 10PF +5PF 200VDC CER CAPACITOR-FXD 2UF +-20% 50VDC MET-POLYE	28480 28480	0160-3874 0160-4947				
A3A1C19 A3A1C20	0160-3879 0160-3879	77	,	CAPACITOR-FXD .01UF +-20% 100VDC CER	28480	0160-3879				
A3A1C21	0160-0576	5		CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .1UF +-20% 50VDC CER	28480	0160-3879				
A3A1C22	0160-0576	5		CAPACITOR-FXD .1UF +-20% SOVDC CER	28480 28480	0160-0576 0160-0576				
A3A1C23 A3A1C24	0160-4512 0160-0576	7		CAPACITOR-FXD 120PF +-5% 200VDC CER CAPACITOR-FXD .1UF +-20% 50VDC CER	28480 28480	0160-4512 0160-0576				
A3A1C25	0160-3277	9	. 1	CAPACITOR-FXD .01UF +-20% 50VDC CER	51642	150-050-X7R-103M				
A3A1C26 A3A1C27	0160-3879 0160-3872	7 0	1	CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 2.2PF +25PF 200VDC CER	28480 28480	0160-3879 0160-3872				
A3A1CR1 A3A1CR2	0122-0244 1901-0869	4	1 1	DIODE-VVC 100PF 5% C4/C25-MIN=2 BVR=30V DIODE-CUR RGLTR 1N5297 DO-7	28480 04713	0122-0244 1N5297				
A3A1CR3 A3A1CR4	1902-0984	4	1	DIODE-ZNR 6.4V 2% DO-7 PD=.4W TC=+.002%	28480	1902-0984				
A3A1CR5	1901-0535 1901-0535	9	3	DIODE-SM SIG SCHOTTKY DIODE-SM SIG SCHOTTKY	28480 28480	1901-0535 1901-0535				
A3A1CR6	1901-0535	9		DIODE-SM SIG SCHOFTKY	28480	1901-0535				
A3A1E1	9170-0029	3	1	FERRITE BEAD (FOR Q1)	28480	9170-0029				
A3A1F1	2110-0617	2	1	FUSE-THERMAL	28480	2110-0617				
A3A1L1 A3A1L2	9100-2280 9140-0352	5	5 1	INDUCTOR RF-CH-MLD 220UH 10% .105DX.26LG INDUCTOR RF-CH-MLD 330NH 1% .105DX.26LG	28480 28480	9100-2280 9140-0352				
A3A1L3 A3A1L4	9140-0353 9100-2276	3	1	INDUCTOR RF-CH-MLD 430NH 1% .105DX.26LG INDUCTOR RF-CH-MLD 100UH 10% .105DX.26LG	28480 28480	9140-0353 9100-2276				
A3A1L5	9100-2280	5		INDUCTOR RF-CH-MLD 220UH 10% .105DX.26LG	28480	9100-2280				
A3A1L6 A3A1L7	9100-2280	5		INDUCTOR RF-CH-MLD 220UH 10% .105DX.26LG	28480	9100-2280				
A3A1L8	9100-2280 9100-2280	5		INDUCTOR RF-CH-MLD 220UH 10% .105DX.26LG INDUCTOR RF-CH-MLD 220UH 10% .105DX.26LG	28480 28480	9100-2280 9100-2280				
A3A1Q1	1854-0853	3	3	TRANSISTOR, SPL 2N5179	28480	1854-0853				
A3A1Q2 A3A1Q3	1854-0853 1854-0853	3		TRANSISTOR, SPL 2N5179 TRANSISTOR, SPL 2N5179	28480 28480	1854-0853 1854-0853				
A3A1Q4 A3A1Q5	1854-0831 1854-0831	7	2	TRANSISTOR NPN 2N6429A TO-92 PD=625MW TRANSISTOR NPN 2N6429A TO-92 PD=625MW	04713 04713	2N6429A 2N6429A				
A3A1Q6	1854-0023	9	1	TRANSISTOR NPN SI TO-18 PD=360MW	28480	1854-0023				
A3A1Q7 A3A1Q8	1854-0701 1854-0701	0	2	TRANSISTOR NPN SI DARL TO-220AB PD=70W TRANSISTOR NPN SI DARL TO-220AB PD=70W	04713 04713	MJE2100 MJE2100				
A3A1Q9	1854-0833	9	1	TRANSISTOR NPN PD=600MW FT=600MHZ	28480	1854-0833				
A3A1R1 A3A1R2	0698-7284 0698-7284	5	5	RESISTOR 100K 1% .05W F TC=0+-100 RESISTOR 100K 1% .05W F TC=0+-100	24546 24546	C3-1/8-T0-1003-F C3-1/8-T0-1003-F				
A3A1R3 A3A1R4	0699-0073	8	2	RESISTOR 10M 1% .125W F TC=0+-150	28480	0699-0073				
A3A1R5	0699-0073 0698-7263	8	1	RESISTOR 10M 1% .125W F TC=0+-150 RESISTOR 13.3K 1% .05W F TC=0+-100	28480 24546	0699-0073 C3-1/8-T0-1332-F				
A3A1R6	2100-2522	1	1	RESISTOR-TRMR 10K 10% C SIDE-ADJ 1-TRN	30983	ET50X103				
A3A1R7 A3A1R8	0698-7272 0698-7232	3	1 3	RESISTOR 31.6K 1% .05W F TC=0+-100 RESISTOR 681 1% .05W F TC=0+-100	24546 24546	C3-1/8-T0-3162-F C3-1/8-T0-681R-F				
A3A1R9 A3A1R10	0698-7256 0698-7256	1	2	RESISTOR 6.81K 1% .05W F TC=0+-100 RESISTOR 6.81K 1% .05W F TC=0+-100	24546 24546	C3-1/8-T0-6811-F C3-1/8-T0-6811-F				
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Table 6-4. Replaceable Parts (continued)

			iau	ie 6-4. – Kepiaceabie Parts (continu	eu)	
Reference Designation	HP Part Number	C	Qty	Description	Mfr Code	Mfr Part Number
A3A1R11 A3A1R12 A3A1R13 A3A1R14 A3A1R15	0698-7244 0698-7261 0698-7224 0698-7280 0698-7284	7 8 3 1 5	1 1 1 1	RESISTOR 2.15K 1% .05W F TC=0+-100 RESISTOR 11K 1% .05W F TC=0+-100 RESISTOR 316 1% .05W F TC=0+-100 RESISTOR 68.1K 1% .05W F TC=0+-100 RESISTOR 100K 1% .05W F TC=0+-100	24546 24546 24546 24546 24546	C3-1/8-T0-2151-F C3-1/8-T0-1102-F C3-1/8-T0-316R-F C3-1/8-T0-6812-F C3-1/8-T0-1003-F
A3A1R16 A3A1R17 A3A1R18 A3A1R19 A3A1R20*	0698-7235 0698-7260 0698-3903 0698-3903	6 7 7 7	1 2 5	RESISTOR 909 1% .05W F TC=0+-100 RESISTOR 10K 1% .05W F TC=0+-100 RESISTOR 8.6K .1% .05W F TC=0+-10 RESISTOR 8.6K .1% .05W F TC=0+-10 JUMPER	24546 24546 28480 28480	C3-1/8-T0-909R-F C3-1/8-T0-1002-F 0698-3903 0698-3903
A3A1R20* A3A1R21 A3A1R22 A3A1R23 A3A1R24 A3A1R25	0698-7239 0698-3903 0698-8827 0698-8827 0699-0071 0698-7273	7 4 4 6 2	1 2 1 1	RESISTOR 1.33K 1% .05W F TC=0+-100 RESISTOR 8.6K .1% .05W F TC=0+-10 RESISTOR 1M 1% .125W F TC=0+-100 RESISTOR 1M 1% .125W F TC=0+-100 RESISTOR 4.64M 1% .125W F TC=0+-100 RESISTOR 34.8K 1% .05W F TC=0+-100	24546 28480 28480 28480 28480 24546	C3-1/8-T0-1331-F 0698-3903 0698-8827 0698-8827 0699-0071 C3-1/8-T0-3482-F
A3A1R26 A3A1R27 A3A1R28 A3A1R29 A3A1R30	0698-3903 0698-3903 0698-7265 0698-7260 0698-7267	7 7 2 7 4	1	RESISTOR 8.6K .1% .05W F TC=0+-10 RESISTOR 8.6K .1% .05W F TC=0+-10 RESISTOR 16.2K 1% .05W F TC=0+-100 RESISTOR 10K 1% .05W F TC=0+-100 RESISTOR 19.6K 1% .05W F TC=0+-100	28480 28480 24546 24546 24546	0698-3903 0698-3903 C3-1/8-T0-1622-F C3-1/8-T0-1002-F C3-1/8-T0-1962-F
A3A1R31 A3A1R32 A3A1R33 A3A1R34 A3A1R35	0698-7220 0698-7250 0698-7284 0698-7247 0698-7250	9 5 5 0 5	1 2	RESISTOR 215 1% .05W F TC=0+-100 RESISTOR 3.83K 1% .05W F TC=0+-100 RESISTOR 100K 1% .05W F TC=0+-100 RESISTOR 2.87K 1% .05W F TC=0+-100 RESISTOR 3.83K 1% .05W F TC=0+-100	24546 24546 24546 24546 24546	C3-1/8-T0-215R-F C3-1/8-T0-3831-F C3-1/8-T0-1003-F C3-1/8-T0-2871-F C3-1/8-T0-3831-F
A3A1R36 A3A1R37 A3A1R38 A3A1R39 A3A1R40	0698-7284 0698-7232 0698-8812 0698-8812 0698-7229	5 3 7 7 8	2	RESISTOR 100K 1% .05W F TC=0+-100 RESISTOR 681 1% .05W F TC=0+-100 RESISTOR 1 1% .125W F TC=0+-100 RESISTOR 1 1% .125W F TC=0+-100 RESISTOR 511 1% .05W F TC=0+-100	24546 24546 28480 28480 24546	C3-1/8-T0-1003-F C3-1/8-T0-681R-F 0698-8812 0698-8812 C3-1/8-T0-511R-F
A3A1R41 A3A1R42	0698-7215 0698-7232	2 3	1	RESISTOR 133 1% .05W F TC=0+-100 RESISTOR 681 1% .05W F TC=0+-100	24546 24546	C3-1/8-T0-133R-F C3-1/8-T0-681R-F
A3A1T1	9100-0423	4	1	TRANSFORMER RF; WIND 9T PRI & 3T SEC	28480	9100-0423
A3A1U1 A3A1U2 A3A1U3	1826-0611 1826-0316 1826-0072	2 4 9	1 1	IC OP AMP GP DUAL 8-DIP-P PKG V REF TO-5 IC OP AMP LOW-BIAS-H-IMPD TO-99 PKG	04713 27014 07263	LM2904N LH0070-1H UA208H
A3A1XF1	1251-1556	7	2	CONNECTOR-SGL CONT SKT .018-IN-BSC-SZ	28480	1251-1556
A3A1Y1	10811-60108	3	1	CRYSTAL-10MHZ W/R20 TEMP SET (REPAIR ONLY)	28480	10811-60108
	2260-0009	3	1	NUT-HEX-W/LKWR 4-40-THD .094-IN-THK	00000	ORDER BY DESCRIPTION
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Table 6-4. Replaceable Parts (continued)

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
				CIRCUIT BOARD MISCELLANEOUS PARTS		
	1200-0868 0360-1244 3050-0588 0360-1682 10811-40002	5 0 9 0 4	1 3 2 2 1	SOCKET-STRP 7-CONT DIP-SLDR TERMINAL-STUD SPCL-FDTHRU PRESS-MTG WASHER-FL NM NO. 6 .145-IN-ID .23-IN-OD TERMINAL-STUD SGL-TUR PRESS-MTG FOAM SHEET-(BELOW OVEN CONTROLLER ASSY)	28480 28480 28480 28480 28480 28480	1200-0868 0360-1244 3050-0588 0360-1682 10811-40002
	2200-0103	2	1	TRANSISTOR MOUNTING HARDWARE SCREW-MACH 4-40 .25-IN-LG PAN-HD-POZI	28480	2200-0103
	2190-0004 3050-1021 0340-0864	9 7 6	1 1	WASHER-LK INTL T NO. 4 .115-IN-ID WASHER-SHLDR NO.4 .116-IN-ID .215-IN-OD INSULATOR-XSTR THRM-CNDCT	28480 28480 28480	2190-0004 3050-1021 0340-0864
MP 25	10811-601 0 6	,	1	MASS ASSEMBLY AND COVERS OVEN MASS ASS. W/THERMISTOR	28480	10811-60106
пР 26 MP 27	10811-20202 10811-20203 0520-0166	4	1 1	MASS COVER W/XISTOR CUT-OUTS MASS COVER W/TUNING CAP HOLE SCREW-MACH 2-56 .375-IN-LG 82 DEG	28480 28480 00000	10811-20202 10811-20203 ORDER BY DESCRIPTION
MO00	10011.20206	8		MECHANICAL PARTS	28480	10811-20206
мр28 мр29 мр30	10811-20206 10811-20211 10811-40001	5	1 1	OUTER HOUSING (FOAMED) BOTTOM COVER FOAM COOKIE UNDER BOT COV	28480 28480	10811-20211 10811-40001
	ļ			MISCELLANEOUS PARTS		
	3030-0827 2200-0101 7120-7912 7121-0331	7 0 0 5	1 1 1 1	SCREW-SKT BIN HD CAP 4-40 .188-IN-LG ALY SCREW-MACH 4-40 .188-IN-LG PAN-HD-POZI INFO LABEL 10811A INFO LABEL 10811-60101	00000 00000 28480 28480	ORDER BY DESCRIPTION ORDER BY DESCRIPTION 7120-7912 7121-0331
A3A2	10811-60003	7	. 1	EDGE CONNECTOR AND CABLE W3	28480	10811-60003
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Table 6-5. Replaceable Parts

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A4	05328-60005	2	1	BD AY-FUNCT SEL	28480	05328-60005
A4C1 A4C2 A4C3 A4C4 A4C6	0160-3879 0160-0576 0140-0215 0140-0215	7 5 7	1 1 2	NOT ASSIGNED CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .1UF +-20% 50VDC CER CAPACITOR-FXD 80PF +-2% 300VDC MICA CAPACITOR-FXD 80PF +-2% 300VDC MICA	28480 28480 72136 72136	0160-3879 0160-0576 DM15E800G0300WY1CR DM15E800G0300WY1CR
A4C6 A4C7 A4C8 A4C9 A4C10	0160-2055 0180-0210 0160-2055 0180-0210 0160-2055	96969	6 2	CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480 56289 28480 56289 28480	0160-2055 150D335X0015A2 0160-2055 150D335X0015A2 0160-2055
A4C11 A4C12 A4C13 A4C14 A4C15	0160-0342 0140-0214 0160-2055 0160-2055 0160-2055	3 6 9 9	1 1	CAPACITOR-FXD 800PF +-1% 300VDC MICA CAPACITOR-FXD 60PF +-5% 300VDC MICA CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480 72136 28480 28480 28480	0160-0342 DM15E600J0300WV1CR 0160-2055 0160-2055 0160-2055
A4C16 A4C17	0140-0202 0160-4040	2 6	1 1	CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD 1000PF +-5% 100VDC CER	72136 28480	DM15C150J0500WV1CR 0160-4040
A4MP1 A4MP2	4040-0752 1480-0116	9	1	EXTR-PC BD YEL POLYC .062-BD-THKNS PIN-GRV .062-IN-DIA .25-IN-LG STL	28480 28480	4040-0752 1480-0116
A4Q1 A4Q2 A4Q3	1854-0215 1854-0215 1854-0215	1 1 1	3	TRANSISTOR NPN SI PD=350MW FT=300MHZ TRANSISTOR NPN SI PD=350MW FT=300MHZ TRANSISTOR NPN SI PD=350MW FT=300MHZ	04713 04713 04713	2N3904 2N3904 2N3904
A4R1 A4R2	0683-5115 0683-5115	6	5	RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 510 5% .25W FC TC=-400/+600	01121 01121	CB5115 CB5115
A4R3 A4R4 A4R5	0683-5115 0683-5115	6		NOT ASSIGNED RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 510 5% .25W FC TC=-400/+600	01121 01121	CB5115 CB5115
A4R6 A4R7 A4R8 A4R9 A4R10	0683-2025 0683-1815 0683-2025 0683-1815 0683-2025	1 5 1 5	7	RESISTOR 2K 5% .25W FC TC=-400/+700 RESISTOR 180 5% .25W FC TC=-400/+600 RESISTOR 2K 5% .25W FC TC=-400/+700 RESISTOR 180 5% .25W FC TC=-400/+600 RESISTOR 2K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB2025 CB1815 CB2025 CB1815 CB2025
A4R11 A4R12 A4R13 A4R14 A4R15	0683-1815 0683-2025 0683-1815 0683-1825 0683-4315	5 1 5 7 6	11	RESISTOR 180 5% .25W FC TC=-400/+600 RESISTOR 2K 5% .25W FC TC=-400/+700 RESISTOR 180 5% .25W FC TC=-400/+600 RESISTOR 1.8K 5% .25W FC TC=-400/+700 RESISTOR 430 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB1815 CB2025 CB1815 CB1825 CB4315
A4R16 A4R17 A4R18 A4R19 A4R20	0683-1825 0683-1815 0683-1815 0683-1825 0683-8205	7 5 5 7		RESISTOR 1.8K 5% .25W FC TC=-400/+700 RESISTOR 180 5% .25W FC TC=-400/+600 RESISTOR 180 5% .25W FC TC=-400/+600 RESISTOR 1.8K 5% .25W FC TC=-400/+700 RESISTOR 82 5% .25W FC TC=-400/+500	01121 01121 01121 01121 01121	CB1825 CB1815 CB1815 CB1825 CB8205
A4R21 A4R22 A4R23 A4R24 A4R25	0683-1315 0683-1825 0683-1825 0683-1825 0683-1035	0 7 7 7		RESISTOR 130 5% .25W FC TC=-400/+600 RESISTOR 1.8K 5% .25W FC TC=-400/+700 RESISTOR 1.8K 5% .25W FC TC=-400/+700 RESISTOR 1.8K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB1315 CB1825 CB1825 CB1825 CB1035
A4R26 A4R27 A4R28 A4R29 A4R30	0683-4315 0683-2715 0683-3315 0683-1825 0683-1825	6 6 4 7 7	3	RESISTOR 430 5% .25W FC TC=-400/+600 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 330 5% .25W FC TC=-400/+600 RESISTOR 1.8K 5% .25W FC TC=-400/+700 RESISTOR 1.8K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB4315 CB2715 CB3315 CB1825 CB1825
A4R31 A4R32 A4R33 A4R34 A4R35	0683-2725 0683-2725 0683-2725 0683-1825 0683-1815	8 8 7 5		RESISTOR 2.7K 5% .25W FC TC=-400/+700 RESISTOR 2.7K 5% .25W FC TC=-400/+700 RESISTOR 2.7K 5% .25W FC TC=-400/+700 RESISTOR 1.8K 5% .25W FC TC=-400/+700 RESISTOR 180 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB2725 CB2725 CB2725 CB1825 CB1815
A4R36 A4R37 A4R38 A4R39 A4R40	0683-8205 0683-1315 0683-5115 0683-2715 0683-2715	1 0 6 6		RESISTOR 82 5% .25W FC TC=-400/+500 RESISTOR 130 5% .25W FC TC=-400/+600 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 270 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB8205 CB1315 CB5115 CB2715 CB2715

Table 6-5. Replaceable Parts (continued)

Table 6-5. Replaceable Farts (continued)									
Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number			
A4R41 A4R42 A4R43 A4R44 A4R45	0683-6815 0683-1825 1810-0041 1810-0080 0698-3447	5 7 9 6 4	1 1 1	RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR 1.8K 5% .25W FC TC=-400/+700 NETWORK-RES 9-SIP2.7K OHM X 8 NETWORK-RES 8-SIP500.0 OHM X 7 RESISTOR 422 1% .125W F TC=0+-100	01121 01121 28480 28490 24546	CB6815 CB1825 1810-0041 1810-0080 C4-1/8-T0-422R-F			
A4R46	0683-1825	7		RESISTOR 1.8K 5% .25W FC TC=-400/+700	01121	CB1825			
A4U1 A4U2 A4U3 A4U4 A4U5	1820-1225 1820-1052 1820-0629 1820-0629 1820-0622	4 5 0 0 3	1 1 2	IC FF ECL D-M/S DUAL IC XLTR ECL ECL-TO-TTL QUAD 2-INP IC FF TTL S J-K NEG-EDGE-TRIG IC FF TTL S J-K NEG-EDGE-TRIG IC MUXR/DATA-SEL TTL 8-TO-1-LINE 8-INP	04713 04713 01295 01295 01295	MC10231P MC10125L SN74S112N SN74S112N SN74151AN			
A4U6 A4U7 A4U8 A4U9 A4U10	1820-0829 1820-0809 1820-0802 1820-0328 1820-0691	2 8 1 6 6	1 1 1 1	IC MUXR/DATA-SEL ECL 8-TO-1-LINE 8-INP IC RCVR ECL LINE RCVR QUAD 2-INP IC GATE ECL NOR QUAD 2-INP IC GATE TIL NOR QUAD 2-INP IC GATE TIL S AND-OR-INV	04713 04713 04713 01295 01295	MC10164L MC10115P MC10102P SN7402N SN74S64N			

Table 6-6. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A5	05328-60012	1	1	BD AY-PS AUTO RN	28480	05328-60012
A5C1 A5C2 A5C3 A5C4 A5C5	0170-0066 0140-0208 0160-2146 0160-2146 0140-0208	9 8 9 9 8	1 2 2	CAPACITOR-FXD .027UF +-10% 200VDC POLYE CAPACITOR-FXD 680PF +-5% 300VDC MICA CAPACITOR-FXD .02UF +80-20% 100VDC CER CAPACITOR-FXD .02UF +80-20% 100VDC CER CAPACITOR-FXD 680PF +-5% 300VDC MICA	28480 72136 28480 28480 72136	0170-0066 DM15F681J0300WV1CR 0160-2146 0160-2146 DM15F681J0300WV1CR
A5C6 A5C7 A5C8 A5C9 A5C10	0180-1746 0180-0197 0180-0106 0180-0106 0180-0106	5 8 9 9	7 2	CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 2.2UF+-10% 20VDC TA CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD 60UF+-20% 6VDC TA	56289 56289 56289 56289 56289	150D156X9020B2 150D225X9020A2 150D606X0006B2 150D606X0006B2 150D606X0006B2
A5C11 A5C12 A5C13 A5C14 A5C15	0160-3060 0180-1746 0180-1746 0121-0168 0180-0197	8 5 5 9 8	1	CAPACITOR-FXD .1UF +-20% 25VDC CER CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-V TRMR-PSTN .2-1.5PF 600V CAPACITOR-FXD 2.2UF+-10% 20VDC TA	28480 56289 56289 28480 56289	0160-3060 150D156X9020B2 150D156X9020B2 0121-0168 150D225X9020A2
ASC16 ASC17 ASC18 ASC19 ASC20	0150-0050 0160-2055 0150-0011 0160-2055 0160-2055	99299	1	CAPACITOR-FXD 1000PF +80-20% 1KVDC CER CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD 1.5PF +-20% 500VDC TI DIOX CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480 28480 28480 28480 28480	0150-0050 0160-2055 0150-0011 0160-2055 0160-2055
A5C21	0180-0230	0		CAPACITOR-FXD 1UF+-20% 50VDC TA	56289	150D105X0050A2
A5CR1 A5CR2 A5CR3 A5CR4 A5CR5	1901-0999 1901-0999 1901-0040 1901-0040 1901-0050	9 9 1 1 3		DIODE-SCHOTTKY 20V 10NS DO-35 DIODE-SCHOTTKY 20V 10NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 80V 200MA 2NS DO-35	28480 28480 28480 28480 28480	1901-0999 1901-0999 1901-0040 1901-0040 1901-0050
ASCR6 ASCR7 ASCR8 ASCR9 ASCR10	1901-0050 1902-3224 1902-3224 1901-0050 1901-0050	3 1 1 3 3	2	DIODE-SWITCHING 80V 200MA 2NS DO-35 DIODE-ZNR 17.8V 5% DO-35 PD=.4W DIODE-ZNR 17.8V 5% DO-35 PD=.4W DIODE-SWITCHING 80V 200MA 2NS DO-35 DIODE-SWITCHING 80V 200MA 2NS DO-35	28480 28480 28480 28480 28480	1901-0050 1902-3224 1902-3224 1901-0050 1901-0050
ASCR11 ASCR12 ASCR13 ASCR14 ASCR15	1901-0050 1901-0050 1901-0050 1901-0050 1901-0040	3 3 3 1		DIODE-SWITCHING 80V 200MA 2NS DO-35 DIODE-SWITCHING 80V 200MA 2NS DO-35 DIODE-SWITCHING 80V 200MA 2NS DO-35 DIODE-SWITCHING 80V 200MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35	28480 28480 28480 28480 28480	1901-0050 1901-0050 1901-0050 1901-0050 1901-0040
ASCR16 ASCR17 ASCR18 ASCR19 ASCR20	1902-0126 1902-0126 1901-0999 1901-0999 1901-0999	66999	2	DIODE-ZNR 2.61V 5% DO-7 PD=.4W TC=072% DIODE-ZNR 2.61V 5% DO-7 PD=.4W TC=072% DIODE-SCHOTTKY 20V 10NS DO-35 DIODE-SCHOTTKY 20V 10NS DO-35 DIODE-SCHOTTKY 20V 10NS DO-35	28480 28480 28480 28480 28480	1902-0126 1902-0126 1901-0999 1901-0999 1901-0999
A5L1	9140-0114	4	1	INDUCTOR RF-CH-MLD 10UH 10% .166DX.385LG	28480	9140-0114
ASMP1 ASMP2	4040-0753 1480-0116	0 8	1 2	EXTR-PC BD GRN POLYC .062-BD-THKNS PIN-GRV .062-IN-DIA .25-IN-LG STL	28480 28480	4040-0753 1480-0116
A5P1 A5P2	1251-3768	7	1	NOT ASSIGNED CONTACT-CONN U/W-POST-TYPE MALE DPSLDR	28480	1251-3768
A5Q1 A5Q2 A5Q3 A5Q4 A5Q5	1854-0514 1854-0514 1854-0215 1853-0036 1854-0071	3 3 1 2 7	13	TRANSISTOR NPN SI PD=1W FT=150MHZ TRANSISTOR NPN SI PD=1W FT=150MHZ TRANSISTOR NPN SI PD=350MW FT=300MHZ TRANSISTOR PNP SI PD=310MW FT=250MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 04713 28480 28480	1854-0514 1854-0514 2N3904 1853-0036 1854-0071
A5Q6 A5Q7 A5Q8 A5Q9	1855-0367 1854-0071 1854-0071 1854-0071	6 7 7 7		TRANSISTOR-UJT P ON N TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480 28480	1855-0367 1854-0071 1854-0071 1854-0071
A5R1 A5R2 A5R3 A5R4 A5R5	0683-3915 0683-4715 0683-4715 0683-4325 0683-4325	0 0 0 8 8	2 2	RESISTOR 390 5% .25W FC TC=-400/+600 RESISTOR 470 5% .25W FC TC=-400/+600 RESISTOR 470 5% .25W FC TC=-400/+600 RESISTOR 4.3K 5% .25W FC TC=-400/+700 RESISTOR 4.3K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB3915 CB4715 CB4715 CB4325 CB4325

Table 6-6. Replaceable Parts (continued)

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A5R6	0683-3315	4	2	RESISTOR 330 5% .25W FC TC=-400/+600	01121	CB3315
ASR7 ASR8 ASR9 ASR10	0683-3315 0683-5625 0683-1135 0683-1025	4 3 2 9	2 2 11	RESISTOR 330 5% .25W FC TC=-400/+600 RESISTOR 5.6K 5% .25W FC TC=-400/+700 RESISTOR 11K 5% .25W FC TC=-400/+800 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121	CB3315 CB5625 CB1135 CB1025
ASR11 ASR12 ASR13 ASR14 ASR15	0683-1025 0683-1135 0683-5625 0683-1025 0683-1025	9 2 3 9 9		RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 11K 5% .25W FC TC=-400/+800 RESISTOR 5.6K 5% .25W FC TC=-400/+700 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB1025 CB1135 CB5625 CB1025 CB1025
A5R16 A5R17 A5R18 A5R19 A5R20	0683-1025 0683-1025 0683-1025 0683-1025 0683-1025	99999		RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB1025 CB1025 CB1025 CB1025 CB1025
ASR21 ASR22 ASR23 ASR24 ASR25	0683-1025 0683-6825 0683-3025 0683-6825 0683-3025	9 7 3 7 3	6 9	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 6.8K 5% .25W FC TC=-400/+700 RESISTOR 3K 5% .25W FC TC=-400/+700 RESISTOR 6.8K 5% .25W FC TC=-400/+700 RESISTOR 3K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB1025 CB6825 CB3025 CB6825 CB3025
A5R26 A5R27 A5R28 A5R29 A5R30	0683-3025 0683-6825 0683-4745 0683-1035 0683-1025	3 7 6 1 9	1 4	RESISTOR 3K 5% .25W FC TC=-400/+700 RESISTOR 6.8K 5% .25W FC TC=-400/+700 RESISTOR 470K 5% .25W FC TC=-800/+900 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB3025 CB6825 CB4745 CB1035 CB1025
A5R31 A5R32- A5R34	0683-3025	3		RESISTOR 3K 5% .25W FC TC=-400/+700	01121	CB3025
A5R35 A5R36	0683-3025 0683-3025	3		RESISTOR 3K 5% .25W FC TC=-400/+700 RESISTOR 3K 5% .25W FC TC=-400/+700	01121 01121	CB3025 CB3025
ASR37 A5R38 A5R39 A5R40 A5R41	0683-3025 0683-3025 0683-4715 0683-3025 0683-1015	3 0 3 7	2	RESISTOR 3K 5% .25W FC TC=-400/+700 RESISTOR 3K 5% .25W FC TC=-400/+700 RESISTOR 470 5% .25W FC TC=-400/+600 RESISTOR 3K 5% .25W FC TC=-400/+700 RESISTOR 100 5% .25W FC TC=-400/+500	01121 01121 01121 01121 01121	CB3025 CB3025 CB4715 CB3025 CB1015
A5R42 A5R43 A5R44 A5R45 A5R46	0683-6825 0683-1035 0683-6825 0683-6825 0683-1015	7 1 7 7		RESISTOR 6.8K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 6.8K 5% .25W FC TC=-400/+700 RESISTOR 6.8K 5% .25W FC TC=-400/+700 RESISTOR 100 5% .25W FC TC=-400/+500	01121 01121 01121 01121 01121	CB6825 CB1035 CB6825 CB6825 CB6015
A5R47 A5R48 A5R49	0683-1035 0698-0021	1 4	1	RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 3.3 10% .5W CC TC=0+412 NOT ASSIGNED	01121 01121	CB1035 EB33G1
ASRS0	0683-1035	1		RESISTOR 10K 5% .25W FC TC=-400/+700	01121	CB1035
A5T1 A5U1 A5U2 A5U3 A5U4 A5U5	9100-3575 1820-0077 1820-0537 1820-0584 1826-0174 1820-0584	3 2 9 6 2 6	1 1 1 2 1	TRANSFORMER FREQ=60KHZ;PRI=17V;SEC=23.5 IC FF TIL D-TYPE POS-EDGE-TRIG CLEAR IC SCHMITT-TRIG TIL NAND DUAL 4-INP IC GATE TIL L NOR QUAD 2-INP IC COMPARATOR GP QUAD 14-DIP-P PKG IC GATE TIL L NOR QUAD 2-INP	28480 01295 01295 01295 28480 01295	9100-3575 SN7474N SN7413N SN74L02N 1826-0174 SN74L02N
A5U6 A5U7 A5U8 A5U9 A5U10	1820 - 0281 1820 - 0907 1820 - 0054 1820 - 0907 1820 - 0054	0 7 5 7 5	1 2 2	IC FF TTL J-K M/S PULSE CLEAR DUAL IC GATE TTL NAND TPL 3-INP IC GATE TTL NAND QUAD 2-INP IC GATE TTL NAND TPL 3-INP IC GATE TTL NAND QUAD 2-INP	01295 01295 01295 01295 01295	SN74107N SN7412N SN7400N SN7412N SN7400N
A5U11 A5U12 A5U13 A5U14	1820 - 0175 1990 - 0543 1990 - 0543 1990 - 0543	1 6 6	1 3	IC INV TTL HEX 1-INP OPTO-ISOLATOR LED-PXSTR IF=150MA-MAX OPTO-ISOLATOR LED-PXSTR IF=150MA-MAX OPTO-ISOLATOR LED-PXSTR IF=150MA-MAX	01295 01295 01295 01295	SN7405N TIL116 TIL116 TIL116

Table 6-6. Replaceable Parts (continued)

	Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A6	05328-60013	2	1	BD AY-BUFF AMP	28480	05328-60013
A6C1	0160-0168 0160-2055	1 9	1 7	CAPACITOR-FXD .1UF +-10% 200VDC POLYE CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480 28480	0160-0168 0160-2055
A6C3	0160-2055	9		CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480 28480	0160-2055 0160-2055
A6C4 A6C5	0160-2055 0160-2055	9		CAPACITOR-FXD .010F +80-20% 100VDC CER	28480	0160-2055
A6C6	0160-2055	9	i	CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480 28480	0160-2055 0160-2055
A6C7 A6C8	0160-2055 0150-0050	9	1	CAPACITOR-FXD 1000PF +80-20% 1KVDC CER	28480	0150-0050
A6C9 A6C10	0160-2055	9		NOT ASSIGNED CAPACITOR-FXD .01UF +80-20% 100VDC CER	28480	0160-2055
A6CR1	1901-0376	6	2	DIODE-GEN PRP 35V 50MA DO-35	28480	1901-0376
A6CR2	1901-0376	6		DIODE-GEN PRP 35V 50MA 00-35	28480	1901-0376
A6K1 A6K2	0490-1406 0490-0764	2	1 2	RELAY-REED 1A 1A 1200VDC SVDC-COIL 10VA RELAY-REED 1A 100MA 125VDC 4.5VDC-COIL	28480 28480	0490-1406 0490-0764
A6K3	0490-0764	2		RELAY-REED 1A 100MA 125VDC 4.5VDC-COIL	28480	0490-0764
A6MP1 A6MP2	4040-0754 1480-0116	8	1	EXTR-PC BD BLU POLYC .062-BD-THKNS PIN-GRV .062-IN-DIA .25-IN-LG STL	28480 28480	4040-0754 1480-0116
A6MP3 A6MP4	0370-2486 0370-2486	5 5	2	PUSHBUTTON .230X.390X.397 IN H: JADE PUSHBUTTON .230X.390X.397 IN H: JADE	28480 28480	0370-2486 0370-2486
A6P1	1			NOT ASSIGNED		
A6P2	1251-3768	7	3	CONTACT-CONN U/W-POST-TYPE MALE DPSLDR	28480	1251-3768
A6Q1 A6Q2	1853-0020 1853-0020	4	3	TRANSISTOR PNP SI PD=300MW FT=150MHZ TRANSISTOR PNP SI PD=300MW FT=150MHZ	28480 28480	1853-0020 1853-0020
A6Q3	1853-0020	4		TRANSISTOR PNP SI PD=300MW FT=150MHZ	28480	1853-0020
A6R1 A6R2	0686-3045 0686-3045	3	3	RESISTOR 300K 5% .5W CC TC=0+882 RESISTOR 300K 5% .5W CC TC=0+882	01121 01121	EB3045 EB3045
A6R3 A6R4	0686-3045 0698-7618	3	14	RESISTOR 300K 5% .5W CC TC=0+882 RESISTOR 888K 1% .25W F TC=0+-50	01121 19701	EB3045 MF52C1/4-T2-8883-F
A6R5	0698-7618	9	14	RESISTOR 888K 1% .25W F TC=0+-50	19701	MF52C1/4-T2-8883-F
A6R6 A6R7	0698-7618 0698-7618	9		RESISTOR 888K 1% .25W F TC=0+-50 RESISTOR 888K 1% .25W F TC=0+-50	19701 19701	MF52C1/4-T2-8883-F MF52C1/4-T2-8883-F
A6R8	0698-7618	9		RESISTOR 888K 1% .25W F TC=0+-50	19701	MF52C1/4-T2-8883-F
A6R9 A6R10	0698-7618 0698-7618	9		RESISTOR 888K 1% .25W F TC=0+-50 RESISTOR 888K 1% .25W F TC=0+-50	19701 19701	MF52C1/4-T2-8883-F MF52C1/4-T2-8883-F
A6R11	0698-7618	9		RESISTOR 888K 1% .25W F TC=0+-50	19701	MF52C1/4-T2-8883-F
A6R12 A6R13	0698-7618 0698-7618	9		RESISTOR 888K 1% .25W F TC=0+-50 RESISTOR 888K 1% .25W F TC=0+-50	19701 19701	MF52C1/4-T2-8883-F MF52C1/4-T2-8883-F
A6R14 A6R15	0698-6626 2100-2503	8	1 2	RESISTOR 345K 1% .125W F TC=0+-25 RESISTOR-TRMR 20K 10% C SIDE-ADJ 17-TRN	28480 32997	0698-6626 3009P-1-203
A6R16	0698-7618	9		RESISTOR 888K 1% .25W F TC=0+-50	19701	MF52C1/4-T2-8883-F
A6R17 A6R18	0698-7618 0698-3152	9	1	RESISTOR 888K 1% .25W F TC=0+-50 RESISTOR 3.48K 1% .125W F TC=0+-100	19701 24546	MF52C1/4-T2-8883-F C4-1/8-T0-3481-F
A6R19 A6R20	0698-3550	0	i	RESISTOR 127K 1% .125W F TC=0+-25	28480	0698-3550
A6R21	0698-7618	9		RESISTOR 888K 1% .25W F TC=0+-50	19701	MF52C1/4-T2-8883-F MF52C1/4-T2-8883-F
A6R22	0698-7618 0683-7545	9	1	RESISTOR 888K 1% .25W F TC=0+-50 RESISTOR 750K 5% .25W FC TC=-800/+900	01121	CB7545
A6R23 A6R24	0683-9145	0	2	NOT ASSIGNED RESISTOR 910K 5% .25W FC TC=~800/+900	01121	CB9145
A6R25	0000 0115			NOT ASSIGNED	01101	CDOLAG
A6R26 A6R27	0683-9145 0683-2725	8	1	RESISTOR 910K 5% .25W FC TC=-800/+900 RESISTOR 2.7K 5% .25W FC TC=-400/+700	01121 01121	CB9145 CB2725
A6R28 A6R29	2100-2706 2100-2503	8	1	RESISTOR-TRMR 10K 10% C SIDE-ADJ 17-TRN RESISTOR-TRMR 20K 10% C SIDE-ADJ 17-TRN	32997 32997	3009P-1-103 3009P-1-203
A6S1	3101-1884	9	1	SWITCH-PB 2-STATION 10MM C-C SPACING	28480	3101-1884
A6U1	1826-0250	5	1	IC OP AMP TO-99 PKG	27014	LH0042CH(SEL)
A6W1	05328-60102		1	CBL AY-HP DVM HI	28480	05328-60102
A6W2 A6W3	05328-60103 8159-0005	1 0	1	CBL AY-HP DVM LO RESISTOR-ZERO OHMS 22 AWG LEAD DIA	28480 28480	05328-60103 8159-0005
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Table 6-6. Replaceable Parts (continued)

Reference Designation	HP Part Number	C	Qty	Description	Mfr Code	Mfr Part Number
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Α7	05328-60014	3	1	BD AY-V/F CONV	28480	05328-60014
A7C1 A7C2 A7C3 A7C4 A7C5	0180-0197 0160-2150 0160-2150 0180-0197 0160-2249	8 5 5 8 3	4 3 1	CAPACITOR-FXD 2.2UF+-10% 20VDC TA CAPACITOR-FXD 33PF +-5% 300VDC MICA CAPACITOR-FXD 33PF +-5% 300VDC MICA CAPACITOR-FXD 2.2UF+-10% 20VDC TA CAPACITOR-FXD 4.7PF +25PF 500VDC CER	56289 28480 28480 56289 28480	150D225X9020A2 0160-2150 0160-2150 150D225X9020A2 0160-2249
A7C6 A7C7 A7C8 A7C9 A7C10	0170-0055 0160-2150 0170-0055 0160-2055 0140-0196	6 5 6 9 3	2 1 1	CAPACITOR-FXD .1UF +-20% 200VDC POLYE CAPACITOR-FXD 33PF +-5% 300VDC MICA CAPACITOR-FXD .1UF +-20% 200VDC POLYE CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD 150PF +-5% 300VDC MICA	28480 28480 28480 28480 72136	0170-0055 0160-2150 0170-0055 0160-2055 DM15F151J0300WV1CR
A7C11 A7C12* A7C13	0160-2205 0160-2307	1 4	1	CAPACITOR-FXD 120PF +-5% 300VDC MICA CAPACITOR-FXD 47PF +-5% 300VDC MICA NOT ASSIGNED	28480 28480	0160-2205 0160-2307
A7C14 A7C15	0180-0197 0180-0197	8		CAPACITOR-FXD 2.2UF+-10% 20VDC TA CAPACITOR-FXD 2.2UF+-10% 20VDC TA	56289 56289	150D225X9020A2 150D225X9020A2
A7CR1 A7CR2 A7CR3 A7CR4 A7CR5	1902-0680 1901-0040 1901-0040 1902-0680 1901-0179	7 1 1 7 7	2 5 6	DIODE-ZNR 1N827 6.2V 5% DO-7 PD=.4W DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-ZNR 1N827 6.2V 5% DO-7 PD=.4W DIODE-SWITCHING 15V 50MA 750PS DO-7	24046 28480 28480 24046 28480	1N827 1901-0040 1901-0040 1N827 1901-0179
A7CR6 A7CR7 A7CR8 A7CR9 A7CR10	1901-0179 1901-0179 1901-0179 1901-0040 1901-0040	7 7 7 1 1		DIODE-SWITCHING 15V 50MA 750PS D0-7 DIODE-SWITCHING 15V 50MA 750PS D0-7 DIODE-SWITCHING 15V 50MA 750PS D0-7 DIODE-SWITCHING 30V 50MA 2NS D0-35 DIODE-SWITCHING 30V 50MA 2NS D0-35	28480 28480 28480 28480 28480	1901-0179 1901-0179 1901-0179 1901-0040 1901-0040
A7CR11 A7CR12 A7CR13	1901-0040 1901-0179 1901-0179	1 7 7		DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 15V 50MA 750PS DO-7 DIODE-SWITCHING 15V 50MA 750PS DO-7	28480 28480 28480	1901-0040 1901-0179 1901-0179
A7L1 A7L2 A7L3 A7L4 D7 C8b	9140-0144 9100-2265 9140-0144 9100-2268	0609	1	INDUCTOR RF-CH-MLD 4.7UH 10% .105DX.26LG INDUCTOR RF-CH-MLD 10UH 10% .105DX.26LG INDUCTOR RF-CH-MLD 4.7UH 10% .105DX.26LG INDUCTOR RF-CH-MLD 22UH 10% .105DX.26LG	28480 28480 28480 28480	9140-0144 9100-2265 9140-0144 9100-2268
A7MP1 A7MP2 A7MP3	4040-0755 1480-0116 7100-0102	2 8 4	1	EXTR-PC BD VIO POLYC .062-BD-THKNS PIN-GRV .062-IN-DIA .25-IN-LG STL CAN-SQ .688-IN-DP-OUT .566-IN-WD-OUT	28480 28480 28480	4040-0755 1480-0116 7100-0102
A7Q1 A7Q2 A7Q3 A7Q4 A7Q5	1855-0056 1855-0081 1853-0020 1854-0071 1854-0092	0 1 4 7 2	1 1 6	TRANSISTOR J-FET P-CHAN D-MODE TO-92 SI TRANSISTOR J-FET N-CHAN D-MODE SI TRANSISTOR PNP SI PD=300MW FT=150MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=200MW FT=600MHZ	07263 28480 28480 28480 28480	2N4342 1855-0081 1853-0020 1854-0071 1854-0092
A7Q6 A7Q7 A7Q8 A7Q9 A7Q10	1854-0092 1854-0092 1854-0092 1854-0071 1854-0071	2 2 7 7		TRANSISTOR NPN SI PD=200MW FT=600MHZ TRANSISTOR NPN SI PD=200MW FT=600MHZ TRANSISTOR NPN SI PD=200MW FT=600MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480 28480 28480	1854-0092 1854-0092 1854-0092 1854-0071 1854-0071
A7Q11 A7Q12 A7Q13	1854-0071 1854-0071 1854-0071	7777	1	TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480	1854-0071 1854-0071 1854-0071
A7R1* A7R1* A7R1* A7R1* A7R2* A7R2* A7R2* A7R2* A7R2* A7R2*	0757-0276 0757-0316 0757-0384 0757-0399 0698-3159 0698-3160 0698-3160 0698-3160 0757-0460	7 6 8 5 3 5 8 0 1 4	2 2 2 2 2 2 2 2 2	RESISTOR 61.9 1% .125W F TC=0+-100 RESISTOR 42.2 1% .125W F TC=0+-100 RESISTOR 20 1% .125W F TC=0+-100 RESISTOR 20.1% .125W F TC=0+-100 RESISTOR 19.6K 1% .125W F TC=0+-100 RESISTOR 26.1K 1% .125W F TC=0+-100 RESISTOR 26.1K 1% .125W F TC=0+-100 RESISTOR 31.6K 1% .125W F TC=0+-100 RESISTOR 46.4K 1% .125W F TC=0+-100 RESISTOR 46.4K 1% .125W F TC=0+-100 RESISTOR 82.5K 1% .125W F TC=0+-100 RESISTOR 82.5K 1% .125W F TC=0+-100	24546 24546 19701 24546 24546 24546 24546 24546 24546 24546 24546	C4-1/8-T0-6192-F C4-1/8-T0-42R2-F MF4C1/8-T0-20R0-F C4-1/8-T0-82R5-F C4-1/8-T0-1962-F C4-1/8-T0-2612-F C4-1/8-T0-3162-F C4-1/8-T0-6192-F C4-1/8-T0-6192-F C4-1/8-T0-8252-F
A7R3 A7R4 A7R5 A7R6 A7R7	0698-7610 0757-0421 0698-3435 2100-2931 0683-2225	1 4 0 6 3	2 2 2	RESISTOR 1.74K .5% .125W F TC=0+-25 RESISTOR 825 1% .125W F TC=0+-100 RESISTOR 38.3 1% .125W F TC=0+-100 RESISTOR-TRMR 500 10% C SIDE-ADJ 17-TRN RESISTOR 2.2K 5% .25W FC TC=-400/+700	19701 24546 24546 32997 01121	MF4C1/8-T9-1741-D C4-1/8-T0-825R-F C4-1/8-T0-38R3-F 3009P-1-501 CB2225
	1	1	1		1	1

Table 6-6. Replaceable Parts (continued)

Reference	HP Part	С			Mfr	Mfr Part Number
Designation	Number	D	Qty	Description	Code	MIII PAIT NUMBER
A7R8 A7R9 A7R10 A7R11 A7R12	0683-1525 0683-1525 0683-2225 0757-0421 0698-3435	4 4 3 4 0	3	RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 2.2K 5% .25W FC TC=-400/+700 RESISTOR 825 1% .125W F TC=0+-100 RESISTOR 38.3 1% .125W F TC=0+-100	01121 01121 01121 24546 24546	CB1525 CB1525 CB2225 C4-1/8-T0-825R-F C4-1/8-T0-38R3-F
A7R13 A7R14* A7R14* A7R14* A7R14* A7R14* A7R14*	2100-2931 0698-3157 0698-3159 0698-3160 0698-3162 0757-0460 0757-0463	635 8014		RESISTOR-TRMR 500 10% C SIDE-ADJ 17-TRN RESISTOR 19.6K 1% .125W F TC=0+-100 RESISTOR 26.1K 1% .125W F TC=0+-100 RESISTOR 31.6K 1% .125W F TC=0+-100 RESISTOR 46.4K 1% .125W F TC=0+-100 RESISTOR 61.9K 1% .125W F TC=0+-100 RESISTOR 82.5K 1% .125W F TC=0+-100	32997 24546 24546 24546 24546 24546 24546	3009P-1-501 C4-1/8-T0-1962-F C4-1/8-T0-2612-F C4-1/8-T0-3162-F C4-1/8-T0-4642-F C4-1/8-T0-6192-F C4-1/8-T0-8252-F
A7R15 A7R16* A7R16* A7R16* A7R16*	0698-7610 0757-0276 0757-0316 0757-0384 0757-0399	1 7 6 8 5		RESISTOR 1.74K .5% .125W F TC=0+-25 RESISTOR 61.9 1% .125W F TC=0+-100 RESISTOR 42.2 1% .125W F TC=0+-100 RESISTOR 20 1% .125W F TC=0+-100 RESISTOR 82.5 1% .125W F TC=0+-100	19701 24546 24546 19701 24546	MF4C1/8-T9-1741-D C4-1/8-T0-6192-F C4-1/8-T0-42R2-F MF4C1/8-T0-20R0-F C4-1/8-T0-82R5-F
A7R17 A7R18 A7R19 A7R20 A7R21	0683-2225 0683-7515 0683-3615 0683-1225 0683-3615	3 4 7 1 7	4 2 2	RESISTOR 2.2K 5% .25W FC TC=-400/+700 RESISTOR 750 5% .25W FC TC=-400/+600 RESISTOR 360 5% .25W FC TC=-400/+600 RESISTOR 1.2K 5% .25W FC TC=-400/+700 RESISTOR 360 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB2225 CB7515 CB3615 CB1225 CB3615
A7R22 A7R23 A7R24 A7R25 A7R26	0683-1025 0683-1025 0683-1025 0683-1025 0698-7610	9 9 9 9	12	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1.74K .5% .125W F TC=0+-25	01121 01121 01121 01121 19701	CB1025 CB1025 CB1025 CB1025 CB1025 MF4C1/8-T9-1741-D
A7R27 A7R28 A7R29 A7R30 A7R31	0698-7610 0683-3325 0683-2225 0683-1025 0683-1025	1 6 3 9 9	3	RESISTOR 1.74K .S% .125W F TC=0+-25 RESISTOR 3.3K 5% .25W FC TC=-400/+700 RESISTOR 2.2K 5% .25W FC TC=-400/+700 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600	19701 01121 01121 01121 01121	MF4C1/8-T9-1741-D CB3325 CB2225 CB1025 CB1025
A7R32 A7R33 A7R34 A7R35 A7R36	0683-3325 0683-6815 0683-6815 0683-3325 0683-3915	6 5 5 6 0	1	RESISTOR 3.3K 5% .25W FC TC=-400/+700 RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR 3.3K 5% .25W FC TC=-400/+700 RESISTOR 390 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB3325 CB6815 CB6815 CB3325 CB3915
A7R37 A7R38 A7R39 A7R40 A7R41	0683-1025 0683-7515 0683-2225 0683-4715 0683-1025	9 4 3 0 9	1	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 750 5% .25W FC TC=-400/+600 RESISTOR 2.2K 5% .25W FC TC=-400/+700 RESISTOR 470 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB1025 CB7515 CB2225 CB4715 CB1025
A7R42 A7R43 A7R44 A7R45* A7R46	0683-7515 0683-1215 0683-2015 0683-6815 0683-1025	4 9 9 5 9		RESISTOR 750 5% .25W FC TC=-400/+600 RESISTOR 120 5% .25W FC TC=-400/+600 RESISTOR 200 5% .25W FC TC=-400/+600 RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB7515 CB1215 CB2015 CB6815 CB1025
A7R47 A7R48 A7R49 A7R50* A7R51	0683-2015 0683-7515 0683-1225 0683-2225 0683-9115	9 4 1 3 4		RESISTOR 200 5% .25W FC TC=-400/+600 RESISTOR 750 5% .25W FC TC=-400/+600 RESISTOR 1.2K 5% .25W FC TC=-400/+700 RESISTOR 2.2K 5% .25W FC TC=-400/+700 RESISTOR 910 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB2015 CB7515 CB1225 CB2225 CB2115
A7R52 A7R53 A7R54 A7R55 A7R56	0683-1525 0683-1515 0683-1025 0683-1515	4 2 9 2		RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 150 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 150 5% .25W FC TC=-400/+600 NOT ASSIGNED	01121 01121 01121 01121 01121	CB1525 CB1515 CB1025 CB1515
A7R57 A7R58 A7R59 A7R60 A7R61	0683-8205 0757-0465 0683-1025 0683-1025 0683-1005	1 6 9 9 5	1	RESISTOR 82 5% .25W FC TC=-400/+500 RESISTOR 100K 1% .125W F TC=0+-100 RESISTOR 1K 5% .25W FC TC=-400/+600 . RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 10 5% .25W FC TC=-400/+500	01121 24546 01121 01121 01121	CB8205 C4-1/8-T0-1003-F CB1025 CB1025 CB1005
A7U1 A7U2 A7U3 A7U4 A7U5	1820-0174 1820-0802 1820-0477 1820-0477 1820-0099	0 1 6 6 8	1 2	IC INV TIL HEX IC GATE ECL NOR QUAD 2-INP IC OP AMP GP 8-DIP-P PKG IC OP AMP GP 8-DIP-P PKG IC CNIR TIL BIN ASYNCHRO NEG-EDGE-TRIG	01295 04713 S0545 S0545 01295	SN7404N MC10102P UPC301AC UPC301AC SN7493N

Table 6-6. Replaceable Parts (continued)

			labi	e 0-0. Replaceable Falls (Collullus	cuj	
Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A7U6 A7U7 A7U8 A7U9 A7U10	1820-0055 1820-0054 1820-0269 1820-0817	6 5 4 8	1	IC CNTR TTL DECD SYNCHRO POS-EDGE-TRIG IC GATE TTL NAND QUAD 2-INP IC GATE TTL NAND QUAD 2-INP IC FF ECL D-M/S DUAL NOT ASSIGNED	01295 01295 01295 01295 04713	SN7490AN SN7400N SN7403N MC10131P
A7U1 1 A7U1 2 A7U1 3 47U1 4	1820-0809 1820-0809 1826-0059 1820-0077	8 8 2 2	1	IC RCVR ECL LINE RCVR QUAD 2-INP IC RCVR ECL LINE RCVR QUAD 2-INP IC OP AMP GP TO-99 PKG IC FF TTL D-TYPE POS-EDGE-TRIG CLEAR	04713 04713 01295 01295	MC10115P MC10115P LM201AL SN7474N
	0360-0065	,	4	TERMINAL-STUD FKD-TUR SWGFRM-MTG	28480	0360-0065
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Table 6-6. Replaceable Parts (continued)

A17	
A17J1	
A17J2 1251-3833 7 2 CONNECTOR 3-PIN F POST TYPE 28480 1251-383 A17P1 1251-3761 0 CONNECTOR 4-PIN F POST TYPE 28480 1251-376 A17P2 1251-3833 7 CONNECTOR 3-PIN F POST TYPE 28480 1251-383 A17S1 3101-1872 5 1 SWITCH-SL DP4T STD .3A 30VDC PC 28480 3101-18	
A17P2 1251-3833 7 CONNECTOR 3-PIN F POST TYPE 28480 1251-383 A17S1 3101-1872 5 1 SWITCH-SL DP4T STD .3A 30VDC PC 28480 3101-18	3
	3

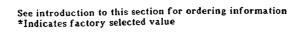


Table 6-7. Replaceable Parts

5 1 1 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +20% 100VDC CER CAPACITOR-FXD 10UF+75-10% 16VDC AL CAPACITOR-FXD 15UF+-10% 20VDC TA DIODE-SCHOTTKY 20V 10NS D0-35 DIODE-SCHOTTKY 30V 50NA 2NS D0-35 DIODE-SMITCHING 30V 50NA 2NS D0-35 DIODE-SMITCHING 30V 50NA 2NS D0-35 DIODE-SMITCHING 30V 50NA 2NS D0-35	28480 28480 56289 56289 28480 72136 72136 72136 28480	05328-60025 05305-60006 150D606X0006B2 150D606X0006B2 0160-2055 DM15C150J0500WV1CR DM15C150J0500WV1CR DM15C150J0500WV1CR 0160-2055 0160-2055 0160-2055 0160-2055 150D105X0050A2 0160-3879 0160-3879 0160-3879 0160-3879 30D1076016DC2 150D156X9020B2 150D156X9020B2 150D156X9020B2 150D156X9020B2 1901-0999 1901-0999 1901-0999 1901-0999 1901-0999 1901-0999 1902-3059 1902-3059
3 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD 01UF +80-20% 100VDC CER CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD 01UF +80-20% 100VDC CER CAPACITOR-FXD 01UF +20% 50VDC TA CAPACITOR-FXD 01UF +20% 100VDC CER CAPACITOR-FXD 01UF +20% 100VDC CER CAPACITOR-FXD 01UF +20% 100VDC CER CAPACITOR-FXD 100F +-5% 300VDC MICA CAPACITOR-FXD 100F +-5% 300VDC MICA CAPACITOR-FXD 100UF+75-10% 16VDC AL CAPACITOR-FXD 15UF+-10% 20VDC TA DIODE-SCHOTTKY 20V 10NS DO-35	28480 56289 56289 28480 72136 72136 72136 72136 28480 28480 28480 28480 28480 28480 56289 56289 56289 56289 56289 56289 56289 56289 56289 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480	05305-60006 150D606X0006B2 150D606X0006B2 0160-2055 DM15C150J0500WV1CR DM15C150J0500WV1CR DM15C150J0500WV1CR 0160-2055 0160-2055 0160-2055 0160-2055 150D105X0050A2 0160-3879 0160-3879 0160-3879 0160-3879 30D107G016DC2 150D156X9020B2 150D156X9020B2 150D156X9020B2 1901-0999 1901-0999 1901-0999 1901-0999 1901-0999 1902-3059 1902-3059
99999999999999999999999999999999999999	CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +20% 50VDC TA CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 10UF+-5% 300VDC MICA CAPACITOR-FXD 10UF+75-10% 16VDC AL CAPACITOR-FXD 15UF+-10% 20VDC TA DIODE-SCHOTTKY 20V 10NS DO-35	56289 56288 28480 72136 72136 72136 28480	150D606X0006B2 150D606X0006B2 0160-2055 DM15C150J0500WV1CR DM15C150J0500WV1CR DM15C150J0500WV1CR 0160-2055 0160-2055 0160-2055 0160-2055 150D105X0050A2 0160-3879 0160-3879 0160-3879 30D107G016DC2 150D156X9020B2 150D156X9020B2 150D156X9020B2 150D156X9020B2 1901-0999 1901-0999 1901-0999 1901-0999 1901-0999 1902-3059 1902-3059 1902-3059
9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD 15PF +-5% 500VDC MICA CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 50VDC TA CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 15UF+-5% 300VDC MICA CAPACITOR-FXD 15UF+-5% 30VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA DIODE-SCHOTTKY 20V 10NS DO-35	56289 28480 72136 72136 72136 72136 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480 28480	150D606X0006B2 0180-2055 DM15C150J0S00WV1CR DM15C150J0S00WV1CR DM15C150J0S00WV1CR 0160-2055 0160-2055 0160-2055 0160-2055 150D105X0050A2 0160-3879 0160-3879 0160-3879 0160-3879 30D107G016DC2 150D156X9020B2 150D156X9020B2 150D156X9020B2 1901-0999 1901-0999 1901-0999 1901-0999 1902-3059 1902-3059
999999999999999999999999999999999999999	CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 100PF +-5% 300VDC MICA CAPACITOR-FXD 100F+75-10% 16VDC AL CAPACITOR-FXD 15UF+75-10% 16VDC AL CAPACITOR-FXD 15UF+70% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA DIODE-SCHOTTKY 20V 10NS D0-35 DIODE-SMITCHING 30V 50MA 2NS D0-35 DIODE-SMITCHING 30V 50MA 2NS D0-35 DIODE-ZNR 2.87V 5% D0-7 PD=.4W TC=07%	28480 28480 28480 28480 28480 29480 28480 28480 56289 56289 56289 56289 28480 28480 28480 28480 28480	0160-2055 0160-2055 0160-2055 160-2055 150D105X0050A2 0160-3879 0160-3879 0160-2204 0160-3879 30D107G016DC2 150D156X9020B2 150D156X9020B2 150D156X9020B2 1901-0999 1901-0999 1901-0999 1902-3059 1902-3059 1901-0040 1901-0535
777777777777777777777777777777777777777	CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 100PF +-5% 300VDC MICA CAPACITOR-FXD 100PF +-5% 300VDC MICA CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA DIODE-SCHOTTKY 20V 10NS D0-35 DIODE-SCHOTTKY 3.83V 5% D0-35 PD=.4U DIODE-ZNR 3.83V 5% D0-35 PD=.4U DIODE-SWITCHING 30V 50MA 2NS D0-35 DIODE-SMITCHING 30V 50MA 2NS D0-35 DIODE-ZNR 2.87V 5% D0-7 PD=.4U TC=07%	28480 28480 28480 28480 56289 56289 56289 56289 28480 28480 28480 28480 28480 28480 28480	0160-3879 0160-3879 0160-2204 0160-3879 30D1076016DC2 150D156X9020B2 150D156X9020B2 150D156X9020B2 1901-0999 1901-0999 1901-0999 1901-0999 1902-3059 1902-3059 1902-3059
5 5 5 9 9 9 9 9 9 9 0 1	CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 15UF+-10% 20VDC TA DIODE-SCHOTTKY 20V 10NS D0-35 DIODE-ZNR 3.83V 5% D0-35 PD=.4U DIODE-ZNR 3.83V 5% D0-35 PD=.4U DIODE-SWITCHING 30V 50MA 2NS D0-35 DIODE-SH SIG SCHOTTKY DIODE-ZNR 2.87V 5% D0-7 PD=.4U TC=07%	56289 56289 56289 28480 28480 28480 28480 28480 28480 28480 28480	150D156X9020B2 150D156X9020B2 150D156X9020B2 1901-0999 1901-0999 1901-0999 1902-3059 1902-3059 1902-3059
9 9 9 9 0 2 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	DIODE-SCHOTTKY 20V 10NS D0-35 DIODE-SCHOTTKY 20V 10NS D0-35 DIODE-SCHOTTKY 20V 10NS D0-35 DIODE-ZNR 3.83V 5% D0-35 PD=.4W DIODE-ZNR 3.83V 5% D0-35 PD=.4W DIODE-SWITCHING 30V 50MA 2NS D0-35 DIODE-SH SIG SCHOTTKY DIODE-ZNR 2.87V 5% D0-7 PD=.4W TC=07%	28480 28480 28480 28480 28480 28480 28480 28480	1901-0999 1901-0999 1901-0999 1902-3059 1902-3059 1901-0040 1901-0535
1 9 9 1	DIODE-SWITCHING 30V 50MA 2NS D0-35 DIODE-SH SIG SCHOTTKY DIODE-ZNR 2.87V 5% D0-7 PD=.4W TC=07%	28480 28480	1901-0040 1901-0535
	FUSE .125A 125V .281X.093		•
	FUSE .125A 125V .281X.093	28480 28480	2110-0301 2110-0301
3 3	CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND NOT ASSIGNED NOT ASSIGNED	28480 28480 28480	0360-0124 0360-0124 0360-0124
7 2 7 6 1	INDUCTOR RF-CH-MLD 68UH 10% .105DX.26LG	28480 28480 28480	9100-2274 9100-2274 9100-1788
2 1 8 3 2 3 2 1	PIN-GRV .062-IN-DIA .25-IN-LG STL SCREW-MACH 6-32 .75-IN-LG PAN-HD-POZI SCREW-MACH 6-32 .75-IN-LG PAN-HD-POZI	28480 28480 00000 00000 28480	4040-0747 1480-0116 ORDER BY DESCRIPTION ORDER BY DESCRIPTION 05305-00008
4 1	HSG-AMP	28480	05305-20107
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	TRANSISTOR NPN SI PD=350MW FT=300MHZ	04713 04713 04713 04713 04713	2N3904 2N3904 2N3904 2N3904 2N3904
2 2 2 1 1	TRANSISTOR PNP SI PD=310MW FT=250MHZ TRANSISTOR PNP SI PD=310MW FT=250MHZ TRANSISTOR PNP SI PD=310MW FT=250MHZ TRANSISTOR NPN SI PD=350MW FT=300MHZ TRANSISTOR NPN SI PD=350MW FT=300MHZ	28480 28480 28480 04713 04713	1853-0036 1853-0036 1853-0036 2N3904 2N3904
1	TRANSISTOR NPN SI PD=350MW FT=300MHZ	04713	2N3904
111111111111111111111111111111111111111		SCREW-MACH 6-32 .75-IN-LG PAN-HD-POZI COV-HSG 1 HSG-AMP TRANSISTOR NPN SI PD=350MW FT=300MHZ TRANSISTOR NPN SI PD=310MW FT=250MHZ TRANSISTOR PNP SI PD=310MW FT=250MHZ TRANSISTOR PNP SI PD=310MW FT=250MHZ TRANSISTOR NPN SI PD=350MW FT=300MHZ TRANSISTOR NPN SI PD=350MW FT=300MHZ TRANSISTOR NPN SI PD=350MW FT=300MHZ	SCREW-MACH 6-32 .75-IN-LG PAN-HD-POZI 00000 28480 1 HSG-AMP 28480 TRANSISTOR NPN SI PD=350MW FT=300MHZ 04713 17ANSISTOR NPN SI PD=310MW FT=250MHZ 17ANSISTOR PNP SI PD=310MW FT=250MHZ 28480 17ANSISTOR NPN SI PD=310MW FT=250MHZ 28480 17ANSISTOR NPN SI PD=350MW FT=300MHZ 04713

Table 6-7. Replaceable Parts (continued)

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
Designation	Humber	-				
A8R1 A8R2 A8R3 A8R4 A8R5	0683-1025 0683-5115 0683-1025 2100-0645	9695	2	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR-VAR CONTROL CCP 50K 10% LIN NOT ASSIGNED	01121 01121 01121 01121	CB1025 CB5115 CB1025 73M4N056L503U
A8R6 A8R7 A8R8 A8R9 A8R10	0683-3005 0683-2725 0683-1025 0683-2725 0683-3005	9 8 9 8 9	2	RESISTOR 30 5% .25W FC TC=-400/+500 RESISTOR 2.7K 5% .25W FC TC=-400/+700 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 2.7K 5% .25W FC TC=-400/+700 RESISTOR 30 5% .25W FC TC=-400/+500	01121 01121 01121 01121 01121	CB3005 CB2725 CB1025 CB2725 CB3005
A8R11 A8R12 A8R13 A8R14 A8R15	0683-1025 2100-2522 0683-7525 0683-2715 0683-5125	9 1 6 6 8	1 1	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR-TRNR 10K 10% C SIDE-ADJ 1-TRN RESISTOR 7.5K 5% .25W FC TC=-400/+700 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 5.1K 5% .25W FC TC=-400/+700	01121 30983 01121 01121 01121	CB1025 ET50X103 CB7525 CB2715 CB5125
A9R16 A9R17 A9R18 A9R19 A9R20	0683-5115 0698-3381 0698-3113 0683-1035 0683-4725	6 5 1 1 2	2 2	RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 150 5% .125W CC TC=-330/+800 RESISTOR 100 5% .125W CC TC=-270/+540 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 4.7K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB5115 BB1515 BB1015 CB1035 CB4725
ABR21 ABR22 ABR23 ABR24 ABR25	0683-1225 0683-5105 0683-2015 0698-3381 0698-3113	1 4 9 5 1	1	RESISTOR 1.2K 5% .25W FC TC=-400/+700 RESISTOR 51 5% .25W FC TC=-400/+500 RESISTOR 200 5% .25W FC TC=-400/+600 RESISTOR 150 5% .125W CC TC=-330/+800 RESISTOR 100 5% .125W CC TC=-270/+540	01121 01121 01121 01121 01121	CB1225 CB5105 CB2015 BB1515 BB1015
A8R26 A8R27 A8R28 A8R29 A8R30	0698-3378 0683-1025 0683-1025 0757-0417 0698-3437	0 9 9 8 2	2 1 1	RESISTOR 51 5% .125W CC TC=-270/+540 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 562 1% .125W F TC=0+-100 RESISTOR 133 1% .125W F TC=0+-100	01121 01121 01121 24546 24546	BB5105 CB1025 CB1025 C4-1/8-T0-562R-F C4-1/8-T0-133R-F
A8R31 A8R32 A8R33 A8R34	0698-3378 0683-2015 0683-1025	9		RESISTOR 51 5% .125W CC TC=-270/+540 RESISTOR 200 5% .25W FC TC=-400/+600 NOT ASSIGNED RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121	B95105 CB2015 CB1025 CB1025
A8R36 A8R36 A8R37 A8R38* A8R39 A8R40	0683-1025 0683-2715 0683-1035 0683-5105 0683-2015 0683-5135	9 6 1 4 9 0	3	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 51 5% .25W FC TC=-400/+500 RESISTOR 200 5% .25W FC TC=-400/+600 RESISTOR 51K 5% .25W FC TC=-400/+800	01121 01121 01121 01121 01121 01121	CB2715 CB1035 CB5105 CB2015 CB2015
A8R41 A8R42 A8R43 A8R44 A8R45	0683-1025 0683-3035 0683-2015 2100-0645 0683-1035	9 5 9 5 1	2	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 30K 5% .25W FC TC=-400/+800 RESISTOR 200 5% .25W FC TC=-400/+600 RESISTOR-VAR CONTROL CCP 50K 10% LIN RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB1025 CB3035 CB2015 73m4N056L503U CB1035
A8TP1 A8TP2 A8TP3 A8TP4	0360-0124 0360-0124 0360-0124 0360-0124	3 3 3	ì	CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND	28480 28480 28480 28480	0360-0124 0360-0124 0360-0124 0360-0124
A8U1 A8U2 A8U3 A8U4 A8U5	1820-0328 1820-0269 1820-0077 1820-0068 1820-0616	6 4 2 1 5		IC GATE TTL NOR QUAD 2-INP IC GATE TTL NAND QUAD 2-INP IC FF TTL D-TYPE POS-EDGE-TRIG CLEAR IC GATE TTL NAND TPL 3-IMP IC MUXR/DATA-SEL TTL 2-TO-1-LINE QUAD	01295 01295 01295 01295 07263	SN7402N SN7403N SN7474N SN7410N 9322PC
A8U6 A8U7 A8U8 A8U9	1820-0301 1820-1052 1820-0493 1DC9-0001	5 5 6 9	;	IC LCH TTL D-TYPE 4-BIT IC XLTR ECL ECL-TO-TTL QUAD 2-INP IC OP AMP GP 8-DIP-P PKG IC-DIGITAL BI-QUIN ECL COUNTER	01295 04713 27014 28480	SN7475N MC10125L LM307N 1DC9-0001
A8เม่1 A8เม่2 A8เม่3 A8เม่4	05328-6011 8159-0005 05328-6012 05328-6012	0 3 5	,	CBL AY-1.3GHZ RESISTOR-ZERO OHMS 22 AWG LEAD DIA CBL AY-EXT LINE CBL AY-EXT LINE	28480 28480 28480 28480	05328-60112 8159-0005 05328-60123 05328-60129
A8XA1	1251-4277	5	'	CONNECTOR 10-PIN F POST TYPE	28480	1251-4277
		1			1	

Table 6-7. Replaceable Parts (continued)

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
- 10X8A 8XU8 8XU9	1200-0475	0	1	NOT ASSIGNED CONNECTOR-SGL CONT SKT .017-IN-BSC-SZ	28480	1200-0475
A8A1	05305-60006	6		BD AY-1.3GHZ SERIES 1608	28480	05305-60006
A8A1C1 A8A1C2 A8A1C3 A8A1C4 A8A1C5	0160-3878 0160-3878 0160-3878 0160-0570 0160-3878	66696	10 3	CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 220PF +-20% 100VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480 28480 28480 28480 20932 28480	0160-3878 0160-3878 0160-3878 5024EM100RD221M 0160-3878
A8A1C6 A8A1C7 A8A1C8 A8A1C9 A8A1C10	0160-0570 0160-3878 0160-0570 0160-3878 0160-3876	9 6 9 6 4	1	CAPACITOR-FXD 220PF +-20% 100VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 220PF +-20% 100VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 47PF +-20% 200VDC CER	20932 28480 20932 28480 28480	5024EM100RD221M 0160-3878 5024EM100RD221M 0160-3878 0160-3876
A8A1C11 A8A1C12 A8A1C13 A8A1C14 A8A1C15	0160-2599 0160-3878 0160-3878 0160-3873 0160-3878	6 6 1 6	1	CAPACITOR-FXD 680PF +-10% 200VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 4.7PF +5PF 200VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480 28480 28480 28480 28480	0160-2599 0160-3878 0160-3878 0160-3873 0160-3878
A8A1C16 A8A1C17 A8A1C18	0160-3878 0160-4445 0160-4248	6 5 6	1 1	CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 1.5PF +25PF 100VDC CER CAPACITOR-FXD 1000PF +-10% 50VDC CER	28480 28480 26654	0160-3878 0160-4445 38X050S102K(D)
A8A1CR1 A8A1CR2 A8A1CR3 A8A1CR4 A8A1CR5	1902-0032 1901-0050 1902-3171 1901-0050 1901-0639	3 7 3 4	2 2 1 4	DIODE-ZNR 5.49V 5% DO-35 PD=.4W DIODE-SWITCHING 80V 200MA 2NS DO-35 DIODE-ZNR 11V 5% DO-35 PD=.4W TC=+.062% DIODE-SWITCHING 80V 200MA 2NS DO-35 DIODE-PIN	28480 28480 28480 28480 28480	1902-0032 1901-0050 1902-3171 1901-0050 5082-3080
A8A1CR6 A8A1CR7 A8A1CR8 A8A1CR9 A8A1CR10	1901-0639 1902-0032 1901-0639 1901-0639 1901-0179	4 3 4 4 7	3	DIODE-PIN DIODE-ZNR 5.49V 5% DO-35 PD=.4W DIODE-PIN DIODE-PIN DIODE-PIN DIODE-SWITCHING 15V 50MA 750PS DO-7	28480 28480 28480 28480 28480 28480	5082-3080 1902-0032 5082-3080 5082-3080 1901-0179
A8A1CR11 A8A1CR12 A8A1CR13	1901-0179 1901-0179 1901-0040	7 7 1		DIODE-SWITCHING 15V SOMA 750PS DO-7 DIODE-SWITCHING 15V SOMA 750PS DO-7 DIODE-SWITCHING 30V 50MA 2NS DO-35	28480 28480 28480	1901-0179 1901-0179 1901-0040
A8A1E1 A8A1E2 A8A1E3	05305-00010 05305-00010 05305-00010	6 6 6	3	CLAMP-GRNDING CLAMP-GRNDING CLAMP-GRNDING	28480 28480 28480	05305-00010 05305-00010 05305-00010
A8A1L1 A8A1L2 A8A1L3	9140-0144 9100-2265 9140-0144	0 6 0	2 1	INDUCTOR RF-CH-MLD 4.7UH 10% .105DX.26LG INDUCTOR RF-CH-MLD 10UH 10% .105DX.26LG INDUCTOR RF-CH-MLD 4.7UH 10% .105DX.26LG	28480 28480 28480	9140-0144 9100-2265 9140-0144
A8A1P1	1251-4249	1	10	CONTACT-CONN U/W-POST-TYPE DPSLDR	28480	1251-4249
A8A1R1 A8A1R2 A8A1R3 A8A1R4 A8A1R5	0698-6648 0698-5426 0698-6648 0698-7102 0698-5426	3 3 6 3	3	RESISTOR 620 5% .125W CC TC=-330/+800 RESISTOR 10K 10% .125W CC TC=-350/+867 RESISTOR 620 5% .125W CC TC=-330/+800 RESISTOR 5.1K 5% .125W CC TC=-350/+857 RESISTOR 10K 10% .125W CC TC=-350/+857	01121 01121 01121 01121 01121	886215 881031 886215 885125 881031
A8A1R6 A8A1R7 A8A1R8 A8A1R9 A8A1R10	0698-6648 0698-6648 0683-2265 0698-3378 0698-6984	3 3 1 0 0	1	RESISTOR 620 5% .125W CC TC=-330/+800 RESISTOR 620 5% .125W CC TC=-330/+800 RESISTOR 22M 5% .25W FC TC=-900/+1200 RESISTOR 51 5% .125W CC TC=-270/+540 RESISTOR 470 5% .125W CC TC=-330/+800	01121 01121 01121 01121 01121	BB6215 B86215 CB2265 BB5105 BB4715
A8A1R11 A8A1R12 A8A1R13 A8A1R14 A8A1R15	0698-7243 0698-5426 2100-1738 0698-7288 0698-7288	63999	1 1 4	RESISTOR 1.96K 1% .05W F TC=0+-100 RESISTOR 10K 10% .125W CC TC=-350/+857 RESISTOR-TRMR 10K 10% C TOP-ADJ 1-TRN RESISTOR 147K 1% .05W F TC=0+-100 RESISTOR 147K 1% .05W F TC=0+-100	24546 01121 73138 24546 24546	C3-1/8-T0-1961-F BB1031 82PR10K C3-1/8-T0-1473-F C3-1/8-T0-1473-F
A8A1R16 A8A1R17 A8A1R18 A8A1R19 A8A1R20	0698-7288 0698-7288 0698-5174 0698-5177 0683-9105	9 9 8 1 2	1 1 1	RESISTOR 147K 1% .05W F TC=0+-100 RESISTOR 147K 1% .05W F TC=0+-100 RESISTOR 200 5% .125W CC TC=-330/+800 RESISTOR 820 5% .125W CC TC=-330/+800 RESISTOR 91 5% .25W FC TC=-400/+500	24546 24546 01121 01121 01121	C3-1/8-T0-1473-F C3-1/8-T0-1473-F BB2015 BB8215 CB9105

Table 6-7. Replaceable Parts (continued)

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A8A1R21 A8A1R22 A8A1R23 A8A1R24 A8A1R25 A8A1U1 A8A1U2 A8A1U3 A8A1U4	0698-4132 2100-1986 0698-3442 0698-3378 0811-3468 1820-0223 5088-7017 1920-1811 1820-1694	6 9 9 0 1 0 4 4 1 1	1 1 1 1 1 1	RESISTOR 62 5% .125W CC TC=-270/+540 RESISTOR-TRMR 1K 10% C TOP-ADJ 1-TRN RESISTOR 237 1% .125W F TC=0+-100 RESISTOR 51 5% .125W CC TC=-270/+540 RESISTOR 240 1% .05W PWW TC=+5900+-300 IC OP AMP GP TO-99 PKG 1.3GHZ AMP IC DRVR ECL IC CNTR ECL BIN	01121 73138 24546 01121 28480 3L585 28480 28480 28480	BB6205 82PR1K C4-1/8-T0-237R-F BB5105 0811-3468 CA301AT 5088-7017 1820-1811 1820-1694

Table 6-8. Replaceable Parts

						
Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A10	05328-60020	1	1	ED AY-SYNCHRON	28480	05328-60020
A10C1 A10C2 A10C3 A10C4 A10C5	0180-0428 0180-0428 0160-2055 0160-3879 0160-3879	8 8 9 7	2 1 4	CAPACITOR-FXD 68UF+-20% 6VDC TA CAPACITOR-FXD 68UF+-20% 6VDC TA CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER	28480 28480 28480 28480 28480	0180-0428 0180-0428 0160-2055 0160-3879 0160-3879
A10C6 A10C7 A10C8 A10C9	0121-0059 0160-2244 0160-3879 0160-3879	7 8 7 7	1	CAPACITOR-V TRMR-CER 2-8PF 350V PC-MTG CAPACITOR-FXD 3PF +25PF 500VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER	52763 28480 28480 28480 28480	304324 2/8PF NPO 0160-2244 0160-3879 0160-3879
A10CR1	1902-3036	3	1	DIODE-ZNR 3.16V 5% DO-7 PD=.4W TC=064%	28480	1902-3036
A10J1 A10J2 A10J3	1200-0548 1200-0548 1200-0548	8 8	3	SOCKET-IC 14-CONT DIP DIP-SLDR SOCKET-IC 14-CONT DIP DIP-SLDR SOCKET-IC 14-CONT DIP DIP-SLDR	28480 28480 28480	1200-0548 1200-0548 1200-0548
A10MP1 A10MP2	4040-0748 1480-0116	3	1	EXTR-PC BD BLK POLYC .062-BD-THKNS PIN-GRV .062-IN-DIA .25-IN-LG STL	28480 28480	4040-0748 1480-0116
A10Q1	1854-0071	7	1	TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480	1854-0071
A10R1 A10R2 A10R3 A10R4 A10R5	0683-3315 0683-3315 1810-0080 0683-5115 1810-0080	4 4 6 6 6	2 5 7	RESISTOR 330 5% .25W FC TC=-400/+600 RESISTOR 330 5% .25W FC TC=-400/+600 NETWORK-RES 8-SIP500.0 OHM X 7 RESISTOR 510 5% .25W FC TC=-400/+600 NETWORK-RES 8-SIP500.0 OHM X 7	01121 01121 28480 01121 28480	CB3315 CB3315 1810-0080 CB5115 1810-0080
A10R6 A10R7 A10R8 A10R9 A10R10	0683-2025 0683-2225 0683-4725 0683-5115 0683-3025	1 3 2 6 3	3 3 1	RESISTOR 2K 5% .25W FC TC=-400/+700 RESISTOR 2.2K 5% .25W FC TC=-400/+700 RESISTOR 4.7K 5% .25W FC TC=-400/+700 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 3K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB2025 CB2225 CB4725 CB5115 CB3025
A10R11 A10R12 A10R13 A10R14 A10R15	0683-1815 0683-5115 0683-9115 1810-0080 0683-9115	5 6 4 6 4	1 2	RESISTOR 180 5% .25W FC TC=-400/+600 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 910 5% .25W FC TC=-400/+600 NETWORK-RES 8-5IP500.0 OHM X 7 RESISTOR 910 5% .25W FC TC=-400/+600	01121 01121 01121 28480 01121	C81815 CB5115 CB9115 1810-0080 CB9115
A10R16 A10R17 A10R18 A10R19	1810-0080 0683-5115 0683-2025	6 6 1		NETWORK-RES 8-SIP500.0 OHM X 7 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 2K 5% .25W FC TC=-400/+700 NOT ASSIGNED	28480 01121 01121	1810-0080 CB5115 CB2025
A10R20 A10R20	0683-2025 0683-5115	1 6		RESISTOR 2K 5% .25W FC TC=-400/+700 RESISTOR 510 5% .25W FC TC=-400/+600	01121 01121	CB2025 CB5115
A10R21 A10R22 A10R23 A10R24 A10R25	0683-2225 1810-0080 0683-2225 0683-1035 0683-1035	3 6 3 1	2	RESISTOR 2.2K 5% .25W FC TC=-400/+700 NETWORK-RES 8-SIP500.0 OHM X 7 RESISTOR 2.2K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 28480 01121 01121 01121	C82225 1810-0080 C82225 C81035 C81035
A10R26 A10R27 A10R28 A10R29 A10R30	1810-0020 0683-1025 0683-5115 0683-5115 0698-7212	4 9 6 6 9	1	NETWORK-RES 8-SIP1.5K OHM X 7 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 100 1% .05W F TC=0+-100	28480 01121 01121 01121 24546	1810-0020 CB1025 CB5115 CB5115 C3-1/8-T0-100R-F
A10S1	3101-1596	0	1	SWITCH-SL DPDT MINTR 1A 125VAC PC	28480	3101-1596
A10TP1 A10TP2 A10TP3 A10TP4	0360-0124 0360-0124 0360-0124 0360-0124	3 3 3 3		CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND	28480 28480 28480 28480	0360-0124 0360-0124 0360-0124 0360-0124
A10U1 A10U2 A10U3 A10U4 A10U5	1820-1320 1820-1049 1820-0802 1820-0802 1820-0802	0 0 1 1 1	1	IC RCVR ECL LINE RCVR TPL 2-INP IC BFR TTL NON-INV HEX IC GATE ECL NOR QUAD 2-INP IC GATE ECL NOR QUAD 2-INP IC GATE ECL NOR QUAD 2-INP	04713 01295 04713 04713 04713	MC10216L SN74367N MC10102P MC10102P MC10102P

Table 6-8. Replaceable Parts (continued)

Reference Designation	HP Part Number	C	Qty	Description	Mfr Code	Mfr Part Number
A10U6 A10U7	1820-0817 1820-0833	8	1 2	IC FF ECL D-M/S DUAL NOT ASSIGNED IC LCH TTL COM CLEAR 8-BIT	04713 07263	MC10131P 9334PC
A10U8 A10U9 A10U10	1820-0833 1820-1049 1820-0802	0	2	IC BFR TTL NON-INV HEX IC GATE ECL NOR QUAD 2-INP	01295 04713	SN74367N MC10102P
A10U11 A10U12 A10U13 A10U14 A10U15	1820-1359 1820-0803 1820-0803 1820-0802 1820-0833	5 2 2 1 8	1 2	IC MUXR/DATA-SEL ECL 4-TO-1-LINE DUAL IC GATE ECL OR-MOR TPL IC GATE ECL OR-MOR TPL IC GATE ECL NOR QUAD 2-INP IC LCH TTL COM CLEAR 8-BIT	04713 04713 04713 04713 04713 07263	MC10174P MC10105P MC10105P MC10102P 9334PC
A10U16 A10U17	1820-1245 1820-1208	8	1	IC DCDR TTL LS 2-TO-4-LINE DUAL 2-INP IC GATE TTL LS OR QUAD 2-INP	01295 01295	SN74LS155N SN74LS32N
A10W1	05328-60114	4	1	CBL AY-EXT LINE	28480	05328-60114
						,
				,		

Table 6-8. Replaceable Parts (continued)

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number			
A11	05328-60023	4	1	BD AY-DAC	28480	05328-60023			
A11C1 A11C2 A11C3 A11C4 A11C5	0180-0374 0160-3879 0180-2617 0180-2617 0160-2743	3 7 1 1 2	2 11 4 2	CAPACITOR-FXD 10UF+-10% 20VDC TA CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 6.8UF+-10% 35VDC TA CAPACITOR-FXD 6.8UF+-10% 35VDC TA CAPACITOR-FXD 33PF +-10% 200VDC CER	56289 28480 25088 25088 28480	150D106X9020B2 0160-3879 D6R8GS1B35K D6R8G51B35K 0160-2743			
A11C6 A11C7 A11C8 A11C9 A11C10	0180-0374 0160-3879 0160-3879 0160-3879 0160-3879	3 7 7 7		CAPACITOR-FXD 10UF+-10% 20VDC TA CAPACITOR-FXD .01UF +-20% 100VDC CER	56289 28480 28480 28480 28480	150D106X9020B2 0160-3879 0160-3879 0160-3879 0160-3879			
A11C11 A11C12 A11C13 A11C14 A11C15	0160-2743 0160-3879 0180-2617 0180-2617 0160-0572	2 7 1 1	1	CAPACITOR-FXD 33PF +-10% 200VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 6.8UF+-10% 35VDC TA CAPACITOR-FXD 6.8UF+-10% 35VDC TA CAPACITOR-FXD 2200PF +-20% 100VDC CER	28480 28480 25088 25088 28480	0160-2743 0160-3879 06R8GS1835K D6R8GS1835K 0160-0572			
A11C16 A11C17 A11C18 A11C19 A11C20	0160-3879 0160-3879 0160-3879 0160-3879 0160-3879	77777		CAPACITOR-FXD .01UF +-20% 100VDC CER	28480 28480 28480 28480 28480 28480	0160-3879 0160-3879 0160-3879 0160-3879 0160-3879			
A11CR1 A11CR2 A11CR3 A11CR4 A11CR5	1901-0179 1901-0179 1901-0179 1901-0179 1901-0179	7777	12	DIODE-SWITCHING 15V SOMA 750PS DO-7 DIODE-SWITCHING 15V SOMA 750PS DO-7 DIODE-SWITCHING 15V SOMA 750PS DO-7 DIODE-SWITCHING 15V SOMA 750PS DO-7 DIODE-SWITCHING 15V SOMA 750PS DO-7	28480 28480 28480 28480 28480	1901-0179 1901-0179 1901-0179 1901-0179 1901-0179			
A11CR6 A11CR7 A11CR8 A11CR9 A11CR10	1901-0179 1901-0179 1901-0179 1901-0179 1901-0179	77777		DIODE-SWITCHING 15V 50MA 750PS D0-7 DIODE-SWITCHING 15V 50MA 750PS D0-7 DIODE-SWITCHING 15V 50MA 750PS D0-7 DIODE-SWITCHING 15V 50MA 750PS D0-7 DIODE-SWITCHING 15V 50MA 750PS D0-7	28480 28480 28480 28480 28480 28480	1901-0179 1901-0179 1901-0179 1901-0179 1901-0179			
A11CR11 A11CR12 A11CR13 A11CR14 A11CR15	1901-0179 1901-0179 1902-0680 1902-0680 1901-0040	7 7 7 7 1	2	DIODE-SWITCHING 15V SOMA 750PS DO-7 DIODE-SWITCHING 15V SOMA 750PS DO-7 DIODE-ZNR 1N827 6.2V 5% DO-7 PD=.4W DIODE-ZNR 1N827 6.2V 5% DO-7 PD=.4W DIODE-SWITCHING 30V SOMA 2NS DO-3S	28480 28480 24046 24046 28480	1901-0179 1901-0179 1N827 1N827 1901-0040			
A11CR16	1901-0040	1		DIODE-SWITCHING 30V 50MA 2NS DO-35	28480	1901-0040			
A11J1	1200-0548	8	1	SOCKET-IC 14-CONT DIP DIP-SLDR	28480	1200-0548			
A11MP1 A11MP2	4040-0748 1480-0116	8	1	EXTR-PC BD BLK POLYC .062-BD-THKNS PIN-GRV .062-IN-DIA .25-IN-LG STL	28480 28480	4040-0748 1480-0116			
A11Q1 A11Q2 A11Q3 A11Q4 A11Q5	1855-0081 1855-0416 1855-0416 1855-0081 1853-0020	1 6 6 1 4	2 2 1	TRANSISTOR J-FET N-CHAN D-MODE SI TRANSISTOR J-FET P-CHAN D-MODE TO-18 SI TRANSISTOR J-FET P-CHAN D-MODE TO-18 SI TRANSISTOR J-FET N-CHAN D-MODE SI TRANSISTOR PNP SI PD=300MW FT=150MHZ	28480 28480 28480 28480 28480	1855-0081 1855-0416 1855-0416 1855-0081 1853-0020			
A11Q6 A11Q7 A11Q8	1854-0071 1854-0071	7 7		NOT ASSIGNED TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480	1854-0071 1854-0071			
A11R1 A11R2 A11R3 A11R4 A11R5	0683-6215 0757-0438 0683-2015 0683-4315 0698-3153	9 3 9 6 9	4 2 4 4 4	RESISTOR 620 5% .25W FC TC=-400/+600 RESISTOR 5.11K 1% .125W F TC=0+-100 RESISTOR 200 5% .25W FC TC=-400/+600 RESISTOR 430 5% .25W FC TC=-400/+600 RESISTOR 3.83K 1% .125W F TC=0+-100	01121 24546 01121 01121 24546	CB6215 C4-1/8-T0-5111-F CB2015 CB4315 C4-1/8-T0-3831-F			
A11R6 A11R7* A11R7* A11R7* A11R7* A11R7*	0683-6215 0698-3136 0698-3136 0698-3157 0757-0199 0757-0447	9 8 3 3 4	6 4 4 4	RESISTOR 620 5% .25W FC TC=-400/+600 RESISTOR 17.8K 1% .125W F TC=0+-100 RESISTOR 17.8K 1% .125W F TC=0+-100 RESISTOR 19.6K 1% .125W F TC=0+-100 RESISTOR 21.5K 1% .125W F TC=0+-100 RESISTOR 16.2K 1% .125W F TC=0+-100	01121 24546 24546 24546 24546 24546	C86215 C4-1/8-T0-1782-F C4-1/8-T0-1782-F C4-1/8-T0-1962-F C4-1/8-T0-2152-F C4-1/8-T0-1622-F			

Table 6-8. Replaceable Parts (continued)

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A11R8 A11R9 A11R10 A11R11* A11R11* A11R11*	0683-2015 0698-3152 0683-4315 0698-3136 0698-3157 0757-0199 0757-0447	9868734	4	RESISTOR 200 5% .25W FC TC=-400/+600 RESISTOR 3.48K 1% .125W F TC=0+-100 RESISTOR 430 5% .25W FC TC=-400/+600 RESISTOR 17.8K 1% .125W F TC=0+-100 RESISTOR 19.6K 1% .125W F TC=0+-100 RESISTOR 21.5K 1% .125W F TC=0+-100 RESISTOR 16.2K 1% .125W F TC=0+-100	01121 24546 01121 24546 24546 24546 24546	C82015 C4-1/8-T0-3481-F C84315 C4-1/8-T0-1782-F C4-1/8-T0-1962-F C4-1/8-T0-2152-F C4-1/8-T0-1622-F
A11R12 A11R13 A11R14 A11R15 A11R16	0698-3152 0683-1015 0698-3153 0683-1025 0683-1055	8 7 9 5	2 5 2	RESISTOR 3.48K 1% .125W F TC=0+-100 RESISTOR 100 5% .25W FC TC=-400/+500 RESISTOR 3.83K 1% .125W F TC=0+-100 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1M 5% .25W FC TC=-800/+900	24546 01121 24546 01121 01121	C4-1/8-T0-3481-F CB1015 C4-1/8-T0-3831-F CB1025 CB1055
A11R17 A11R18 A11R19 A11R20 A11R21	2100-2705 2100-2705 0683-1035 2100-2503 2100-2503	2 2 1 8 8	4 1 2	RESISTOR-TRMR 1K 10% C SIDE-ADJ 17-TRN RESISTOR-TRMR 1K 10% C SIDE-ADJ 17-TRN RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR-TRMR 20K 10% C SIDE-ADJ 17-TRN RESISTOR-TRMR 20K 10% C SIDE-ADJ 17-TRN	32997 32997 01121 32997 32997	3009P-1-102 3009P-1-102 CB1035 3009P-1-203 3009P-1-203
A11R22 A11R23 A11R24 A11R25 A11R26	0683-1025 0683-1055 2100-2705 0683-1015 2100-2705	9 5 2 7 2		RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1M 5% .25W FC TC=-800/+900 RESISTOR-TRNR 1K 10% C SIDE-ADJ 17-TRN RESISTOR 100 5% .25W FC TC=-400/+500 RESISTOR-TRNR 1K 10% C SIDE-ADJ 17-TRN	01121 01121 32997 01121 32997	CB1025 CB1055 3009P-1-102 CB1015 3009P-1-102
A11R27 A11R28* A11R28* A11R28* A11R28*	0698-3152 0698-3136 0698-3157 0757-0199 0757-0447	8 8 3 3 4		RESISTOR 3.48K 1%125W F TC=0+-100 RESISTOR 17.8K 1%125W F TC=0+-100 RESISTOR 19.6K 1%125W F TC=0+-100 RESISTOR 21.5K 1%125W F TC=0+-100 RESISTOR 16.2K 1%125W F TC=0+-100	24546 24546 24546 24546 24546	C4-1/8-T0-3481-F C4-1/8-T0-1782-F C4-1/8-T0-1962-F C4-1/8-T0-2152-F C4-1/8-T0-1622-F
A11R29 A11R30 A11R31 A11R32 A11R33 A11R33* A11R33* A11R33* A11R33*	0698-3153 0698-3152 0683-4315 0757-0438 0698-3136 0698-3136 0698-3157 0757-0199 0757-0447	9 8 6 3 8 8 3 4		RESISTOR 3.83K 1% .125W F TC=0+-100 RESISTOR 3.48K 1% .125W F TC=0+-100 RESISTOR 430 5% .25W FC TC=-400/+600 RESISTOR 5.11K 1% .125W F TC=0+-100 RESISTOR 17.8K 1% .125W F TC=0+-100 RESISTOR 17.8K 1% .125W F TC=0+-100 RESISTOR 19.6K 1% .125W F TC=0+-100 RESISTOR 21.5K 1% .125W F TC=0+-100 RESISTOR 21.5K 1% .125W F TC=0+-100 RESISTOR 16.2K 1% .125W F TC=0+-100	24546 24546 01121 24546 24546 24546 24546 24546 24546	C4-1/8-T0-3831-F C4-1/8-T0-3481-F CB4315 C4-1/8-T0-5111-F C4-1/8-T0-1782-F C4-1/8-T0-1782-F C4-1/8-T0-1962-F C4-1/8-T0-2152-F C4-1/8-T0-1622-F
A11R34 A11R35 A11R36 A11R37 A11R38	0683-2015 0698-3153 0683-6215 0683-4315 0683-2015	9 9 9 6 9		RESISTOR 200 5% .25W FC TC=-400/+600 RESISTOR 3.83K 1% .125W F TC=0+-100 RESISTOR 620 5% .25W FC TC=-400/+600 RESISTOR 430 5% .25W FC TC=-400/+600 RESISTOR 200 5% .25W FC TC=-400/+600	01121 24546 01121 01121 01121	CB2015 C4-1/8-T0-3831-F CB6215 CB4315 CB2015
A11R39 A11R40 A11R41 A11R42 A11R43	0683-6215 0757-0924 0683-1525 0683-1525	9 2 4 4	1	RESISTOR 620 5% .25W FC TC=-400/+600 NOT ASSIGNED RESISTOR 1K 2% .125W F TC=0+-100 RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 1.5K 5% .25W FC TC=-400/+700	01121 24546 01121 01121	CB6215 C4-1/8-T0-1001-G CB1525 CB1525
A11R44 A11R45 A11R46 A11R47 A11R48	0757-0421 0683-1045 0683-1025 0757-0421 0683-1025	4 3 9 4 9	2	RESISTOR 825 1% .125W F TC=0+-100 RESISTOR 100K 5% .25W FC TC=-400/+800 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 825 1% .125W F TC=0+-100 RESISTOR 1K 5% .25W FC TC=-400/+600	24546 01121 01121 24546 01121	C4-1/8-T0-825R-F CB1045 CB1025 C4-1/8-T0-825R-F CB1025
A11R49 A11R50 A11R51 A11R52 A11R53* A11R53* A11R53*	1810-0055 0683-1025 0683-2055 0683-2055 0683-1225 0683-1525 0683-1625 0683-1825	5 9 7 7 1 4 5 7	1 2 1 3 1 1	NETWORK-RES 9-SIP10.0K OHM X 8 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 2M 5% .25W FC TC=-900/+1100 RESISTOR 2M 5% .25W FC TC=-900/+1100 RESISTOR 1.2K 5% .25W FC TC=-400/+700 RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 1.8K 5% .25W FC TC=-400/+700	28480 01121 01121 01121 01121 01121 01121 01121	1810-0055 CB1025 CB2055 CB2055 CB1225 CB1525 CB1525 CB1625 CB1625
A11TP1 A11TP2 A11TP3 A11TP4 A11TP5	0360-0124 0360-0124 0360-0124 0360-0124 0360-0124	3 3 3		CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND	28480 28480 28480 28480 28480 28480	0360-0124 0360-0124 0360-0124 0360-0124 0360-0124
	0360-0124	3		CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND	28480	0360-0124

Table 6-8. Replaceable Parts (continued)

			iaui	e 0-0. Replaceable Falls (Collinus	-u)	
Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A11U1 A11U2 A11U3 A11U4 A11U5	1826-0059 1820-0693 1826-0161 1826-0059 1820-3475	2 8 7 2 0	2 1 2	IC OP AMP GP TO-99 PKG IC FF TTL S D-TYPE POS-EDGE-TRIG IC OP AMP GP QUAD 14-DIP-P PKG IC OP AMP GP TO-99 PKG IC SCHMITT-TRIG TTL LS NAND QUAD 2-INP	01295 01295 04713 01295 28480	LM201AL SN74S74N MLM324P LM201AL 1820-3475
A11U6 A11U7 A11U8 A11U9 A11U10	1826-0161 1820-0976 1820-1265 1820-1265 1820-1265	7 0 2 2 2	4 6	IC OP AMP GP QUAD 14-DIP-P PKG IC SHF-RGTR CMOS D-TYPE SERIAL-IN IC MULTR CMOS IC MULTR CMOS IC MULTR CMOS IC MULTR CMOS	04713 3L585 04713 04713 04713	MLM324P CD4015BE MC14527BCP MC14527BCP MC14527BCP
A11U11 A11U12 A11U13 A11U14 A11U15	1820-1265 1820-1265 1820-1265 1820-0976	2 2 2 0		NOT ASSIGNED IC MULTR CMOS IC MULTR CMOS IC MULTR CMOS IC MULTR CMOS IC SHF-RGTR CMOS D-TYPE SERIAL-IN	04713 04713 04713 3L585	MC14527BCP MC14527BCP MC14527BCP CD4015BE
A11U16 A11U17- A11U76 A11U77	1820-0976	0		IC SHF-RGTR CMOS D-TYPE SERIAL-IN NOT ASSIGNED IC SHF-RGTR CMOS D-TYPE SERIAL-IN	3L585 3L585	CD4015BE

Table 6-8. Replaceable Parts (continued)

Reference Designation		CD	Qty	Description	Mfr Code	Mfr Part Number
A12	05328-60041	6	1	BO AY-AMPLIFIER	28480	05328-60041
A12C1 A12C2 A12C3 A12C4 A12C5	0160-3879 0160-3879 0160-3879 0160-3879 0160-3879	7 7 7 7 7	17	CAPACITOR-FXD .01UF +-20% 100VDC CER	28480 28480 28480 28480 28480 28480	0160-3879 0160-3879 0160-3879 0160-3879 0160-3879
A12C6 A12C7 A12C8 A12C9 A12C10	0160-3879 0160-3879 0160-3879 0160-3879 0160-0128	7 7 7 7 3	2	CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 50VDC CER	28480 28480 28480 28480 28480	0160-3879 0160-3879 0160-3879 0160-3879 0160-0128
A12C11 A12C12 A12C13 A12C14 A12C15	0160-0128 0180-0428 0180-0428 0160-3879 0160-0576	3 8 8 7 5	5	CAPACITOR-FXD 2.2UF +-20% 50VDC CER CAPACITOR-FXD 68UF+-20% 6VDC TA CAPACITOR-FXD 68UF+-20% 6VDC TA CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .1UF +-20% 50VDC CER	28480 28480 28480 28480 28480 28480	0160-0128 0180-0428 0180-0428 0160-3879 0160-0576
A12C16 A12C17 A12C18 A12C19 A12C20	0160-0576 0180-0415 0160-3879 0180-0415 0180-0428	5 3 7 3 8	2	CAPACITOR-FXD .1UF +-20% 50VDC CER CAPACITOR-FXD 10UF+-20% 25VDC TA CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 10UF+-20% 25VDC TA CAPACITOR-FXD 68UF+-20% 6VDC TA	28480 28480 28480 28480 28480	0160-0576 0180-0415 0160-3879 0180-0415 0180-0428
A12C21 A12C22 A12C23 A12C24 A12C25	0180-0428 0160-4423 0160-4423 0160-4423 0160-4423	8 9 9 9		CAPACITOR-FXD 68UF+-20% 6VDC TA CAPACITOR-FXD 470PF +-20% 500VDC CER	28480 51642 51642 51642 51642	0180-0428 200-500-X7R-471M 200-500-X7R-471M 200-500-X7R-471M 200-500-X7R-471M
A12C26* A12C26* A12C26* A12C26* A12C26*	0150-0059 0160-2242 0160-2244 0160-2249 0160-2255	8 6 8 3	1 1	CAPACITOR-FXD 3.3PF +25PF 500VDC CER CAPACITOR-FXD 2.4PF +25PF 500VDC CER CAPACITOR-FXD 3PF +25PF 500VDC CER CAPACITOR-FXD 4.7PF +25PF 500VDC CER CAPACITOR-FXD 8.2PF +25PF 500VDC CER	28480 28480 28480 28480 28480	0150-0059 0160-2242 0160-2244 0160-2249 0160-2255
A12C27 A12C28 A12C29* A12C29* A12C29* A12C29* A12C29*	0150-0059 0160-2246 0160-2247 0160-2252 0160-2255	8 0 1 8 1	1 1	NOT ASSIGNED NOT ASSIGNED CAPACITOR-FXD 3.3PF +25PF 500VDC CER CAPACITOR-FXD 3.6PF +25PF 500VDC CER CAPACITOR-FXD 3.9PF +25PF 500VDC CER CAPACITOR-FXD 6.2PF +25PF 500VDC CER CAPACITOR-FXD 8.2PF +25PF 500VDC CER	28480 28480 28480 28480 28480	0150-0059 0160-2246 0160-2247 0160-2252 0160-2255
A12C30 A12C31 A12C32 A12C33 A12C34	0160-4424 0160-4424 0160-3879 0180-0428	0 0 7		CAPACITOR-FXD .047UF +-20% 500VDC CER CAPACITOR-FXD .047UF +-20% 500VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER NOT ASSIGNED CAPACITOR-FXD 68UF+-20% 6VDC TA	51642 51642 28480 28480	400-500-X7R-473M 400-500-X7R-473M 0160-3879
A12C35 A12C36 A12C37 A12C38 A12C39	0160-3879 0160-3879 0160-3879	7777		NOT ASSIGNED NOT ASSIGNED CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER	28480 28480 28480	0160-3879 0160-3879 0160-3879
A12C40 A12C41	0160-3879 0160-3879	77		CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER	28480 28480	0160-3879 0160-3879
A12CR1 A12CR2 A12CR3 A12CR4 A12CR5	1902-3082 1901-0040 1901-0040 1901-0040 1901-0376	1 1 6	10	DIODE-ZNR 4.64V 5% DO-35 PD=.4W DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-GEN PRP 35V 50MA DO-35	28480 28480 28480 28480 28480	1902-3082 1901-0040 1901-0040 1901-0040 1901-0376
A12CR6 A12CR7 A12CR8 A12CR9 A12CR10	1901-0376 1902-3048 1901-0376 1902-3048 1901-0376	6	2	DIODE-GEN PRP 3SV 50MA DO-35 DIODE-ZNR 3.48V 5% DO-35 PD=.4W DIODE-GEN PRP 3SV 50MA DO-35 DIODE-ZNR 3.48V 5% DO-35 PD=.4W DIODE-GEN PRP 3SV 50MA DO-35	28480 28480 28480 28480 28480	1901-0376 1902-3048 1901-0376 1902-3048 1901-0376

Table 6-8. Replaceable Parts (continued)

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A12CR11 A12CR12 A12CR13 A12CR14 A12CR15	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040	1 1 1 1		DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35	28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040
A12CR16 A12CR17 A12CR18 A12CR19	1901-0040 1901-0040 1902-3082 1902-3082	1 1 9 9		DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-ZNR 4.64V 5% DO-35 PD=.4W DIODE-ZNR 4.64V 5% DO-35 PD=.4W	28480 28480 28480 28480	1901-0040 1901-0040 1902-3082 1902-3082
A12J1	1200-0548	8	١	SOCKET-IC 14-CONT DIP DIP-SLDR	28480	1200-0548
A12K1 A12K2 A12K3 A12K4 A12K4	0490-0642 0490-1175 0490-1183 0490-1175 05328-80041	5 1 1 1 8	1 7 3	RELAY-REED 2C 500MA 28VDC 5VDC-COIL 3VA RELAY-REED 1A 500MA 100VDC 5VDC-COIL RELAY-REED 1B 500MA 100VDC 5VDC-COIL RELAY-REED 1A 500MA 100VDC 5VDC-COIL RELAY-SHIELDED	28480 28480 28480 28480 28480	0490-0642 0490-1175 0490-1183 0490-1175 05328-80041
A12K5 A12K6 A12K7 A12K8 A12K9	0490-1183 0490-1175 0490-1175 0490-1175 0490-1175	1 1 1 1		RELAY-REED 1B 500MA 100VDC 5VDC-COIL RELAY-REED 1A 500MA 100VDC 5VDC-COIL RELAY-REED 1A 500MA 100VDC 5VDC-COIL RELAY-REED 1A 500MA 100VDC 5VDC-COIL RELAY-REED 1A 500MA 100VDC 5VDC-COIL	28480 28480 28480 28480 28480 28480	0490-1183 0490-1175 0490-1175 0490-1175 0490-1175
A12K10 A12K11	0490-1175 0490-1183	1 1		RELAY-REED 1A 500MA 100VDC SVDC-COIL RELAY-REED 1B 500MA 100VDC SVDC-COIL	28480 28480	0490-1175 0490-1183
A12L1 A12L2 A12L3 A12L4	9100-2288 9100-2288 9140-0178 9140-0178	3 3 0 0	2 2	INDUCTOR RF-CH-MLD 1MH 10% .105DX.26LG INDUCTOR RF-CH-MLD 1MH 10% .105DX.26LG INDUCTOR RF-CH-MLD 12UH 10% .166DX.385LG INDUCTOR RF-CH-MLD 12UH 10% .166DX.385LG	28480 28480 28480 28480	9100-2288 9100-2288 9140-0178 9140-0178
A12MP1 A12MP2	4040-0748 1480-0116	3	1	EXTR-PC BD BLK POLYC .062-BD-THKNS PIN-GRV .062-IN-DIA .25-IN-LG STL	28480 28480	4040-0748 1480-0116
A12Q1 A12Q2 A12Q3 A12Q4	1854-0071 1854-0071 1855-0213 1855-0213	7711	2	TRANSISTOR NPN SI PD=300MU FT=200MHZ TRANSISTOR NPN SI PD=300MU FT=200MHZ TRANSISTOR-JFET DUAL N-CHAN D-MODE TO-78 TRANSISTOR-JFET DUAL N-CHAN D-MODE TO-78	28480 28480 28480 28480	1854-0071 1854-0071 1855-0213 1855-0213
A12R1 A12R2 A12R3 A12R4 A12R5	0698-7260 0698-7260 1810-0080 0683-3925 0698-7252	7 7 6 2 7	2 1 2 2	RESISTOR 10K 1% .05W F TC=0+-100 RESISTOR 10K 1% .05W F TC=0+-100 NETWORK-RES 8-SIP500.0 OHM X 7 RESISTOR 3.9K 5% .25W FC TC=-400/+700 RESISTOR 4.64K 1% .05W F TC=0+-100	24546 24546 28480 01121 24546	C3-1/8-T0-1002-F C3-1/8-T0-1002-F 1810-0080 CB3925 C3-1/8-T0-4641-F
A1 2R6 A1 2R7 A1 2R8 A1 2R9 A1 2R10	0683-3925 0757-0199 0698-7252 0683-8215 0683-8215	2 3 7 3 3		RESISTOR 3.9K 5% .25W FC TC=-400/+700 RESISTOR 21.5K 1% .125W F TC=0+-100 RESISTOR 4.64K 1% .05W F TC=0+-100 RESISTOR 820 5% .25W FC TC=-400/+600 RESISTOR 820 5% .25W FC TC=-400/+600	01121 24546 24546 01121 01121	CB3925 C4-1/8-T0-2152-F C3-1/8-T0-4641-F CB8215 CB8215
A12R11 A12R12 A12R13 A12R14 A12R15	0698-7236 0683-1025 0698-7236 0683-1035 0683-1035	7 9 7 1	4	RESISTOR 1K 1% .05W F TC=0+-100 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1K 1% .05W F TC=0+-100 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	24546 01121 24546 01121 01121	C3-1/8-T0-1001-F CB1025 C3-1/8-T0-1001-F CB1035 CB1035
A12R16 A12R17 A12R18 A12R19 A12R20	0683-5115 0683-5115 0683-2215 0683-2215 0683-1005	6 6 1 1 5	2	RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 220 5% .25W FC TC=-400/+600 RESISTOR 220 5% .25W FC TC=-400/+600 RESISTOR 10 5% .25W FC TC=-400/+500	01121 01121 01121 01121 01121	CB5115 CB5115 CB2215 CB2215 CB1005
A1 2R21 A1 2R22 A1 2R23 A1 2R24 A1 2R25	0683-1005 0683-2025 0683-1035 0683-2225 0683-1035	5 1 1 3 1	1	RESISTOR 10 5% .25W FC TC=-400/+500 RESISTOR 2K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 2.2K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB1005 CB2025 CB1035 CB2225 CB1035
A12R26 A12R27 A12R28 A12R29 A12R30	2100-2632 0698-7229 2100-2632 0698-7230 0698-7230	4 8 4 1 1	2 4	RESISTOR-TRMR 100 10% C SIDE-ADJ 1-TRN RESISTOR 511 1% .05W F TC=0+-100 RESISTOR-TRMR 100 10% C SIDE-ADJ 1-TRN RESISTOR 562 1% .05W F TC=0+-100 RESISTOR 562 1% .05W F TC=0+-100	30983 24546 30983 24546 24546	ET50X101 C3-1/8-T0-511R-F ET50X101 C3-1/8-T0-562R-F C3-1/8-T0-562R-F

Table 6-8. Replaceable Parts (continued)

Reference		c	Qty	Description	Mfr	Mfr Part Number
Designation	Number	D	<u> </u>		Code	
A12R31 A12R32 A12R33 A12R34 A12R35	0698-7229 0698-7230 0683-5115 0698-7188 0698-7230	8 1 6 8 1	2	RESISTOR 511 1% .05W F TC=0+-100 RESISTOR 562 1% .05W F TC=0+-100 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 10 1% .05W F TC=0+-100 RESISTOR 562 1% .05W F TC=0+-100	24546 24546 01121 24546 24546	C3-1/8-T0-S11R-F C3-1/8-T0-562R-F CBS115 C3-1/8-T0-10R-F C3-1/8-T0-562R-F
A12R36 A12R37 A12R38 A12R39 A12R40	0698-7188 0683-1055 0683-1055 0683-1055 0683-5115	8 5 5 5 6	. 4	RESISTOR 10 1% .05W F TC=0+-100 RESISTOR 1M 5% .25W FC TC=-800/+900 RESISTOR 1M 5% .25W FC TC=-800/+900 RESISTOR 1M 5% .25W FC TC=-800/+900 RESISTOR 510 5% .25W FC TC=-400/+600	24546 01121 01121 01121 01121	C3-1/8-TO-10R-F CB1055 CB1055 CB1055 CB5115
A12R41 A12R42 A12R43 A12R44 A12R45	0683-1055 0698-6400 0757-0465 0757-0465 0698-6400	5 5 6 6 5	2 2	RESISTOR 1M 5% .25W FC TC=-800/+900 RESISTOR 900K 1% .25W F TC=0+-100 RESISTOR 100K 1% .125W F TC=0+-100 RESISTOR 100K 1% .125W F TC=0+-100 RESISTOR 900K 1% .25W F TC=0+-100	01121 19701 24546 24546 19701	CB1055 MF52C1/4-T0-9003-F C4-1/8-T0-1003-F C4-1/8-T0-1003-F MF52C1/4-T0-9003-F
A12R46 A12R47 A12R48 A12R49 A12R50	0757-1000 0757-1000 0757-0931 0757-0900 0757-0931	7 7 1 4 1	2 2 2	RESISTOR 51.1 1% .5W F TC=0+-100 RESISTOR 51.1 1% .5W F TC=0+-100 RESISTOR 2K 2% .125W F TC=0+-100 RESISTOR 100 2% .125W F TC=0+-100 RESISTOR 2K 2% .125W F TC=0+-100	28480 28480 24546 24546 24546	0757-1000 0757-1000 C4-1/8-T0-2001-G C4-1/8-T0-101-G C4-1/8-T0-2001-G
A12R51 A12R52 A12R53* A12R54 A12R55	0757-0900 0698-5218 0683-1015	4 1 7	2 2	RESISTOR 100 2% .125W F TC=0+-100 RESISTOR 30K .5% .125W F TC=0+-100 RESISTOR 100 5% .25W FC TC=-400/+500 NOT ASSIGNED NOT ASSIGNED	24546 24546 01121	C4-1/8-T0-101-6 C4-1/8-T0-3002-D CB1015
A12R56 A12R57* A12R58	0698-5218 0683-1015 0757-0279	1 7 0	1	RESISTOR 30K .5% .125W F TC=0+-100 RESISTOR 100 5% .25W FC TC=-400/+500 RESISTOR 3.16K 1% .125W F TC=0+-100	24546 01121 24546	C4-1/8-T0-3002-D CB1015 C4-1/8-T0-3161-F
A12U1 A12U2 A12U3 A12U4	1820-0577 1820-0805 1820-0577 1826-0426	7 4 7 7	1	IC INV TTL HEX 1-INP IC GATE ECL EXCL-OR/NOR TPL 2-INP IC INV TTL HEX 1-INP IC COMPARATOR HS DUAL 16-DIP-C PKG	01295 04713 01295 34335	SN7416N MC10107P SN7416N AM687ADL
A12W1 A12W2	05328-6011 05328-6011			CBL AY-RF A & B IN CBL AY-RF A & B IN	28480 28480	05328-60111 05328-60111

Table 6-8. Replaceable Parts (continued)

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A14	05328-60010	9	1	BD AY-MULTIPLIER	28480	05328-60010
A14C1 A14C2 A14C3 A14C4 A14C5	0160-3878 0170-0066 0160-0161 0160-3878 0160-3878	6 9 4 6 6	6 1 1	CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD .027UF +-10% 200VDC POLYE CAPACITOR-FXD .01UF +-10% 200VDC POLYE CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER	28480 28480 28480 28480 28480	0160-3878 0170-0066 0160-0161 0160-3878 0160-3878
A14C6 A14C7 A14C8 A14C9 A14C10	0180-1714 0160-4084 0160-3878 0160-3878 0180-0210	7 8 6 6 6	1 4 7	CAPACITOR-FXD 330UF+-10% 6VDC TA CAPACITOR-FXD .1UF +-20% 50VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 3.3UF+-20% 15VDC TA	56289 28480 28480 28480 56289	150D337X9006S2 0160-4084 0160-3878 0160-3878 150D335X0015A2
A14C11 A14C12 A14C13 A14C14 A14C15	0180-0210 0160-2055 0160-2055 0180-0210 0180-0210	6 9 9 6 6	4	CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD 3.3UF+-20% 15VDC TA	56289 28480 28480 56289 56289	150D335X0015A2 0160-2055 0160-2055 150D335X0015A2 150D335X0015A2
A14C16 A14C17 A14C18 A14C19* A14C20	0160-2055 0160-2055 0121-0437 0140-0191 0160-4084	9 9 5 8	2	CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-V TRIR-CER 10-60PF 250V PC-MTG CAPACITOR-FXD 56PF +-5% 300VDC MICA CAPACITOR-FXD .1UF +-20% 50VDC CER	28480 28480 28480 72136 28480	0160-2055 0160-2055 0121-0437 DM15E560J0300W1CR 0160-4084
A14C21 A14C22 A14C23 A14C24 A14C25	0160-3878 0140-0149 0121-0437 0180-0210	6656	1	CAPACITOR-FXD 1000PF +-20% 100VDC CER CAPACITOR-FXD 470PF +-5% 300VDC MICA CAPACITOR-V TRMR-CER 10-60PF 250V PC-MTG CAPACITOR-FXD 3.3UF+-20% 15VDC TA NOT ASSIGNED	28480 72136 28480 56289	0160-3878 DM15F471J0300W1CR 0121-0437 150D335X0015A2
A14C26 A14C27 A14C28 A14C29 A14C30	0180-0210 0140-0192 0180-0210 0160-4084	6 9 6 8	1	CAPACITOR-FXD 3.3UF+-20% 1SVDC TA CAPACITOR-FXD 68PF +-5% 300VDC MICA NOT ASSIGNED CAPACITOR-FXD 3.3UF+-20% 1SVDC TA CAPACITOR-FXD .1UF +-20% 50VDC CER	56289 72136 56289 28480	150D335X0015A2 DM15E680J0300WV1CR 150D335X0015A2 0160 - 4084
A14C31 A14C32	0160-4084	8		NOT ASSIGNED CAPACITOR-FXD .1UF +-20% SOVDC CER	28480	0160-4084
A14CR1 A14CR2 A14CR3	0122-0299 0122-0015 1902-1293	9 7 0	1 1 1	DIODE-VVC 82PF 5% C2/C20-MIN=2 BVR=20V DIODE-VVC 150PF 10% D0-14 Q=1750-MIN DIODE-ZNR 10V 10% D0-7 PD=.4W	28480 28480 28480	0122-0299 0122-0015 1902-1293
A14L1 A14L2*	9100-2204 9100-2259	3	1 1	INDUCTOR RF-CH-MLD 60NH 10% .095DX.25LG INDUCTOR RF-CH-MLD 1.5UH 10% .105DX.26LG	28480 28480	9100-2204 9100-2259
A14MP1 A14MP2	4040-0752 1480-0116	9	1	EXTR-PC BD YEL POLYC .062-BD-THKNS PIN-GRV .062-IN-DIA .25-IN-LG STL	28480 28480	4040-0752 1480-0116
A14Q1 A14Q2 A14Q3 A14Q4	1854-0092 1854-0092 1853-0016 1854-0071	2 2 8 7	2 1 1	TRANSISTOR NPN SI PD=200MW FT=600MHZ TRANSISTOR NPN SI PD=200MW FT=600MHZ TRANSISTOR PNP SI TO-92 PD=300MW TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480 28480	1854-0092 1854-0092 1853-0016 1854-0071
A14R1 A14R2 A14R3 A14R4 A14R5	0683-1025 0683-1025 0683-1025	9 9 9	4	NOT ASSIGNED NOT ASSIGNED RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121	CB1025 CB1025 CB1025
A14R6 A14R7 A14R8 A14R9 A14R10	0683-1025 0683-5135 0683-0475 0683-6815 0683-3915	9 0 1 5 0	2 1 1	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 51K 5% .25W FC TC=-400/+800 RESISTOR 4.7 5% .25W FC TC=-400/+500 RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR 390 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB1025 CB5135 CB47G5 CB6815 CB3915
A14R11 A14R12 A14R13 A14R14 A14R15	0683-8225 0683-5115 0683-2715 0683-3335 0683-5125	5 6 8 8	2 3 1 1	RESISTOR 8.2K 5% .25W FC TC=-400/+700 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 33K 5% .25W FC TC=-400/+800 RESISTOR 5.1K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB8225 CB5115 CB2715 CB3335 CB5125

Table 6-8. Replaceable Parts (continued)

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A14R16 A14R17 A14R18 A14R19 A14R20	0683-5115 0683-3935 0683-5115 0683-5135 0683-2745	6 4 6 0 2	1	RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 39K 5% .25W FC TC=-400/+800 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 51K 5% .25W FC TC=-400/+800 RESISTOR 270K 5% .25W FC TC=-800/+900	01121 01121 01121 01121 01121	CB5115 CB3935 CB5115 CB5135 CB2745
A14R21 A14R22 A14R23 A14R24-	0683-1045 0683-1045 0683-1135	3 3 2	2 2	RESISTOR 100K 5% .25W FC TC=-400/+800 RESISTOR 100K 5% .25W FC TC=-400/+800 RESISTOR 11K 5% .25W FC TC=-400/+800	01121 01121 01121	CB1045 CB1045 CB1135
A1 4R27 A1 4R28 A1 4R29 A1 4R30 A1 4R31 A1 4R32	2100-2522 0683-8225 0683-2725 0683-1135 0683-4745	1 5 8 2 6	1 1 1	RESISTOR-TRMR 10K 10% C SIDE-ADJ 1-TRN RESISTOR 8.2K 5% .25W FC TC=-400/+700 RESISTOR 2.7K 5% .25W FC TC=-400/+700 RESISTOR 11K 5% .25W FC TC=-400/+800 RESISTOR 470K 5% .25W FC TC=-800/+900	30983 01121 01121 01121 01121	ET50X103 CB8225 CB2725 CB1135 CB4745
A14R33 A14R34	0683-1535 1810-0080	6	1	RESISTOR 15K 5% .25W FC TC=-400/+800 NETWORK-RES 8-SIP500.0 OHM X 7	01121 28480	CB1535 1810-0080
A14TP1 A14TP2 A14TP3 A14TP4 A14TP5	0360-0124 0360-0124 0360-0124 0360-0124 0360-0124	3 3 3 3		CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND	28480 28480 28480 28480 28480	0360-0124 0360-0124 0360-0124 0360-0124 0360-0124
A14TP6 A14TP7 A14TP8	0360-0124 0360-0124 0360-0124	3 3		CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND	28480 28480 28480	0360-0124 0360-0124 0360-0124
A14U1 A14U2 A14U3 A14U4 A14U5	1820-1383 1820-0810 1820-1119 1820-0630 1820-0054	5 1 5 3 5	1 1	IC CNTR ECL BCD POS-EDGE-TRIG IC RCVR ECL LINE RCVR TPL 2-INP IC OSC ECL IC MISC ITL IC GATE ITL NAND QUAD 2-INP	04713 04713 04713 04713 01295	MC10138L MC10116P MC1648L MC4044P SN7400N
A1 4U6 A1 4U7	1820-0493 1820-0493	6		IC OP AMP GP 8-DIP-P PKG IC OP AMP GP 8-DIP-P PKG	27014 27014	LM307N LM307N

Table 6-8. Replaceable Parts (continued)

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A19	05328-60021	2	1	BD AY-SWITCH	28480	05328-60021
A19C1 A19C2 A19C3 A19C4 A19C5	0180-0106 0160-3879 0160-3879 0160-3879 0160-3879	97777	1 6	CAPACITOR-FXD 60UF+-20% 6VDC TA CAPACITOR-FXD .01UF +-20% 100VDC CER	56289 28480 28480 28480 28480	150D606X0006B2 0160-3879 0160-3879 0160-3879 0160-3879
A19C6 A19C7 A19C8 A19C9	0160-3879 0160-3879 0180-1746 0180-1746	7 7 5 5	2	CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 1SUF+-10% 20VDC TA CAPACITOR-FXD 1SUF+-10% 20VDC TA	28480 28480 56289 56289	0160-3879 0160-3879 150D156X9020B2 150D156X9020B2
A19CR1 A19CR2 A19CR3 A19CR4 A19CR5	1901-0040 1901-0040 1901-0040 1901-0040	1 1 1 1	14	NOT ASSIGNED DIODE-SWITCHING 30V SOMA 2NS DO-35	28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0040
A19CR6 A19CR7 A19CR8 A19CR9 A19CR10	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040	1 1 1 1 1		DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35	28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040
A19CR11 A19CR12 A19CR13 A19CR14 A19CR15	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040	1 1 1 1		DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35	28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0040 1901-0040
A19DS1 A19DS2 A19DS3	1990-0485 1990-0486 1990-0485	5 6 5	2 1	LED-LAMP LUM-INT=800UCD IF=30MA-MAX LED-LAMP LUM-INT=1MCD IF=20MA-MAX BVR=5V LED-LAMP LUM-INT=800UCD IF=30MA-MAX	28480 28480 28480	5082-4984 5082-4684 5082-4984
A19J1 A19J2 A19J3 A19J4	1251-2034 1250-1163 1251-1626 1250-1163	8 0 2 0	1 2 1	CONNECTOR-PC EDGE 10-CONT/ROW 2-ROWS CONNECTOR-RF BNC FEM SGL-HOLE-RR 50-OHM CONNECTOR-PC EDGE 12-CONT/ROW 2-ROWS CONNECTOR-RF BNC FEM SGL-HOLE-RR 50-OHM	28480 28480 28480 28480	1251 - 2034 1250 - 1163 1251 - 1626 1250 - 1163
A19MP1 A19MP2 A19MP3 A19MP4	2190-0060 2190-0060 2950-0072 2950-0072	7 7 3 3	2	WASHER-LK INTL T 1/4 IN .256-IN-ID WASHER-LK INTL T 1/4 IN .256-IN-ID NUT-HEX-DBL-CHAM 1/4-32-THD .062-IN-THK NUT-HEX-DBL-CHAM 1/4-32-THD .062-IN-THK	28480 28480 00000 00000	2190-0060 2190-0060 ORDER BY DESCRIPTION ORDER BY DESCRIPTION
A19R1 A19R2 A19R3 A19R4 A19R5	2100-3516 0683-1035 0683-2225 0683-2025 0683-1035	5 1 3 1	2 9 1 3	RESISTOR-VAR W/SW 10K 20% LIN SPST-NO RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 2.2K 5% .25W FC TC=-400/+700 RESISTOR 2K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	WRS4G056S103M CB1035 CB2225 CB2025 CB1035
A19R6 A19R7 A19R8 A19R9 A19R10	0683-1035 0683-1035 0683-5115 0683-1035	1 1 6 1	1	NOT ASSIGNED RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 510 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121 01121 01121	CB1035 CB1035 CB5115 CB1035
A19R11 A19R12 A19R13 A19R14 A19R15	0683-1035 0683-3315 0683-1035 0683-1035 0683-2025	1 1 1 1	3	RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 330 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 2K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB1035 CB3315 CB1035 CB1035 CB2025
A19R16 A19R17 A19R18 A19R19 A19R20	0683-1035 0683-1525 0683-2715 0683-2715 0683-1525	1 4 6 6 4	4 2	RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 1.5K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB1035 CB1525 CB2715 CB2715 CB1525
A19R21 A19R22 A19R23 A19R24 A19R25	0683-6815 0683-1525 0683-3315 0683-3315 0683-2025	5 4 4 4 1	2	RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 330 5% .25W FC TC=-400/+600 RESISTOR 330 5% .25W FC TC=-400/+600 RESISTOR 2K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB6815 CB1525 CB3315 CB3315 CB2025

Table 6-8. Replaceable Parts (continued)

Reference Designation	HP Part Number	O D	Qty	Description	Mfr Code	Mfr Part Number
A19R26 A19R27 A19R28 A19R29 A19R30	0683-1525 0683-6815 2100-3516 0698-8072 0698-8072	4 5 5 1 1	2	RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR-VAR W/SW 10K 20% LIN SPST-NO RESISTOR 39K 5% .125W CC TC=-466/+875 RESISTOR 39K 5% .125W CC TC=-466/+875	01121 01121 01121 01121 01121	CB1525 CB6815 WRS4G056S103M BB3935 BB3935
A19S1 A19S2 A19S3 A19S4 A19S5	3101 - 2334 3101 - 2334 3101 - 2334 3101 - 1313 3101 - 2334	66696	7	SWITCH-SL DPDT SUBMIN .5A 125VAC/DC PC SWITCH-SL DPDT SUBMIN .5A 125VAC/DC PC SWITCH-SL DPDT SUBMIN .5A 125VAC/DC PC SWITCH-SL DP3T MINTR .5A 125VAC/DC PC SWITCH-SL DPDT SUBMIN .5A 125VAC/DC PC	28480 28480 28480 28480 28480	3101 - 2334 3101 - 2334 3101 - 2334 3101 - 1313 3101 - 2334
A19S6 A19S7 A19S8	3101-2334 3101-2334 3101-2334	6 6		SWITCH-SL DPDT SUBMIN .5A 125VAC/DC PC SWITCH-SL DPDT SUBMIN .5A 125VAC/DC PC SWITCH-SL DPDT SUBMIN .5A 125VAC/DC PC	28480 28480 28480	3101-2334 3101-2334 3101-2334
A19TP1	0360-0124	3	1	CONNECTOR-SGL CONT PIN .04-IN-BSC-SZ RND	28480	0360-0124
A19U1	1820-1052	5	1	IC XLTR ECL ECL-TO-TTL QUAD 2-INP	04713	MC10125L
	05328-40004	9	3	SPACER-LED SHT	28480	05328-40004

Table 6-9. Replaceable Parts

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
A15	05328-60043	8	1	BD AY-HP IB	28480	05328-60043
A15C1 A15C2	0160-2453 0170-0040	1 9	1	CAPACITOR-FXD .22UF +-10% 80VDC POLYE CAPACITOR-FXD .047UF +-10% 200VDC POLYE	28480 56289	0160-2453 292P47392
A15C3 A15C4 A15C5	0160-0154 0160-0161	5 4	1	NOT ASSIGNED CAPACITOR-FXD 2200PF +-10% 200VDC POLYE CAPACITOR-FXD .01UF +-10% 200VDC POLYE	28480 28480	0160-0154 0160-0161
A1506 A1507 A1508	0170-0024 0180-0229 0180-0229	9 7 7	2	CAPACITOR-FXD .022UF +-20% 200VDC POLYE CAPACITOR-FXD 33UF+-10% 10VDC TA CAPACITOR-FXD 33UF+-10% 10VDC TA	28480 56289	0170-002 4 150D336X9010B2
A15C9 A15C10	0180-1746 0160-3878	5 6	1	CAPACITOR-FXD 15UF+-10% 20VDC TA CAPACITOR-FXD 1000PF +-20% 100VDC CER	56289 56289 28480	150D336X9010B2 150D156X9020B2 0160-3878
A15CR1 A15CR2 A15CR3 A15CR4 A15CR5	1901-0999 1901-0999 1901-0999 1901-0999	99999		DIODE-SCHOTTKY 20V 10NS DO-35	28480 28480 28480 28480 28480 28480	1901-0999 1901-0999 1901-0999 1901-0999 1901-0999
A15CR6	1901-0999	9		DIODE-SCHOTTKY 20V 10NS DO-35	28480	1901-0999
A15J1A A15J1B A15J2-	1200-0485 1200-0485	2	2	SOCKET-IC 14-CONT DIP DIP-SLDR SOCKET-IC 14-CONT DIP DIP-SLDR	28480 28480	1200-0485 1200-0485
A15J4 A15J5	1200-0548	8	1	NOT ASSIGNED SOCKET-IC 14-CONT DIP DIP-SLDR	28480	1200-0548
A15J6	1251-3283	1	1	CONNECTOR 24-PIN F MICRORIBBON	28480	1251-3283
A15MP1 A15MP2 A15MP3 A15MP4 A15MP5	1400-0531 1400-0531 0380-0643 0380-0643	3 3 3	3	CLAMP-CABLE .13-DIA .375-WD ABS CLAMP-CABLE .13-DIA .375-WD ABS STANDOFF-HEX .255-IN-LG 6-32THD STANDOFF-HEX .255-IN-LG 6-32THD NOT ASSIGNED	28480 28480 00000 00000	1400-0531 1400-0531 ORDER BY DESCRIPTION ORDER BY DESCRIPTION
A15MP6 A15MP7	1530-1098 1530-1098	4	2	CLEVIS 0.070-IN W SLT: 0.454-IN PIN CTR CLEVIS 0.070-IN W SLT: 0.454-IN PIN CTR	00000	ORDER BY DESCRIPTION ORDER BY DESCRIPTION
A15Q1 A15Q2 A15Q3 A15Q4 A15Q5	1854-0215 1854-0215 1854-0215 1854-0215 1853-0036	1 1 1 2		TRANSISTOR NPN SI PD=350MW FT=300MHZ TRANSISTOR PNP SI PD=310MW FT=250MHZ	04713 04713 04713 04713 28480	2N3904 2N3904 2N3904 2N3904 1853-0036
A15R1 A15R2 A15R3 A15R4 A15R5	0683-3035 0683-1035 0683-1035 0683-2715	5 1 1 6		RESISTOR 30K 5% .25W FC TC=-400/+800 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 270 5% .25W FC TC=-400/+600 NOT ASSIGNED	01121 01121 01121 01121	CB3035 CB1035 CB1035 CB2715
A15R6 A15R7 A15R8 A15R9 A15R10	0683-1035 0683-1035 0683-1035 0683-1035	1 1 1 1		NOT ASSIGNED RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121 01121 01121	CB1035 CB1035 CB1035 CB1035
A15R11 A15R12 A15R13 A15R14 A15R15	0757-0446 0683-1035 0683-2715 0683-2715 0683-1035	3 1 6 6 1	1	RESISTOR 15K 1% .125W F TC=0+-100 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700	24546 01121 01121 01121 01121	C4-1/8-T0-1502-F CB1035 CB2715 CB2715 CB1035
A15R16 A15R17 A15R18 A15R19 A15R20	0683-4725	1 6 2 2 1	1	RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 15K 5% .25W FC TC=-400/+800 RESISTOR 4.7K 5% .25W FC TC=-400/+700 RESISTOR 4.7K 5% .25W FC TC*-400/+700 RESISTOR 10K 5% .25W FC TC*-400/+700	01121 01121 01121 01121 01121	CB1035 CB1535 CB4725 CB4725 CB1035
A15R21 A15R22 - A15R23 A15R24 A15R25	0683-4725 0683-4725 0683-1035	1 2 2 1 8		RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 4.7K 5% .25W FC TC=-400/+700 RESISTOR 4.7K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 2.7K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB1035 CB4725 CB4725 CB1035 CB2725

Table 6-9. Replaceable Parts (continued)

Reference Designation		C D	Qty	Description	Mfr Code	Mfr Part Number
A15R26 A15R27 A15R28 A15R29 A15R30	0683-2025 0683-3025 0698-3154 1810-0136 1810-0136	1 3 0 3 3	1 1 1 2	RESISTOR 2K 5% .25W FC TC=-400/+700 RESISTOR 3K 5% .25W FC TC=-400/+700 RESISTOR 4.22K 1% .125W F TC=0+-100 NETWORK-RES 10-SIP MULTI-VALUE NETWORK-RES 10-SIP MULTI-VALUE	01121 01121 24546 28480 28480	CB2025 CB3025 C4-1/8-T0-4221-F 1810-0136 1810-0136
A15R31 A15R32 A15R33 A15R34 A15R35	1810-0055 1810-0055 1810-0055 1810-0055 0683-1035	5 5 5 1	4	NETWORK-RES 9-SIP10.0K OHM X 8 RESISTOR 10K 5% .25W FC TC=-400/+700	28480 28480 28480 28480 01121	1810-0055 1810-0055 1810-0055 1810-0055 CB1035
A15R36	0683-2715	6		RESISTOR 270 5% .25W FC TC=-400/+600	01121	CB2715
A15S1	3101-1973	7	1	SWITCH-SL 7-1A DIP-SLIDE-ASSY .1A 50VDC	28480	3101-1973
A15U1 A15U2 A15U3 A15U4 A15U5	1820-0261 1820-0706 1820-0615 1820-1199 1820-1209	6 4 4 1 4	1 1 5 3 3	IC MY TTL MONOSTBL IC COMPTR TTL MAGID S-BIT IC MUXR/DATA-SEL TTL 8-TO-1-LINE 8-INP IC INV TTL LS HEX 1-INP IC BFR TTL LS NAND QUAD 2-INP	01295 07263 04713 01295 01295	SN74121N 9324PC MC8312P SN74LS04N SN74LS38N
A15U6 A15U7 A15U8 A15U9 A15U10	1820-0615 1820-1478 1820-0615 1820-1199 1820-1209	4 9 4 1 4	1	IC MUXR/DATA-SEL ITL 8-TO-1-LINE 8-INP IC CNTR TTL LS BIN ASYNCHRO IC MUXR/DATA-SEL ITL 8-TO-1-LINE 8-INP IC INV TTL LS HEX 1-INP IC BFR TTL LS NAND QUAD 2-INP	04713 01295 04713 01295 01295	MC8312P SN74L593N MC8312P SN74L504N SN74LS38N
A15U11 A15U12 A15U13 A15U14 A15U15	1820-1112 1820-0615 1820-0111 1820-1430 1820-1444	8 4 5 3 9	1 2	IC FF TTL LS D-TYPE POS-EDGE-TRIG IC MUXR/DATA-SEL TTL 8-TO-1-LINE 8-INP IC DCDR TTL BCD-TO-DEC 4-TO-10-LINE IC CNTR TTL LS BIN SYNCHRO POS-EDGE-TRIG IC MUXR/DATA-SEL TTL LS 2-TO-1-LINE QUAD	01295 04713 04713 01295 01295	SN74LS74AN MC8312P MC8301P SN74LS161AN SN74LS298N
A15U16 A15U17 A15U18 A15U19 A15U20	1820-1209 1820-1197 1820-1425 1820-1729 1820-1198	4 9 6 3 0	3 1 3	IC BFR TTL LS NAND QUAD 2-INP IC GATE TTL LS NAND QUAD 2-INP IC SCHMITT-IRIG TTL LS NAND QUAD 2-INP IC LCH TTL LS COM CLEAR 8-BIT IC GATE ITL LS NAND QUAD 2-INP	01295 01295 01295 01295 01295	SN74LS38N SN74LS00N SN74LS132N SN74LS259N SN74LS03N
A15U21 A15U22 A15U23 A15U24 A15U25	1820-1199 1818-2253 1820-1430 1820-1411 1820-1197	1 5 3 0 9	1	IC INV TTL LS HEX 1-INP IC NMOS 4096 (4K) ROM IC CNTR TTL LS BIN SYNCHRO POS-EDGE-TRIG IC ECH TTL LS D-TYPE 4-BIT IC GATE TTL LS NAND QUAD 2-INP	01295 28480 01295 01295 01295	SN74LS04N 1818-2253 SN74LS161AN SN74LS75N SN74LS00N
A15U26 A15U27 A15U28 A15U29 A15U30	1820-1729 1820-1198 1820-1885 1820-1197 1820-1211	3 0 2 9	2	IC LCH TTL LS COM CLEAR 8-BIT IC GATE TTL LS NAND QUAD 2-INP IC RGTR TTL LS D-TYPE QUAD IC GATE TTL LS NAND QUAD 2-INP IC GATE TTL LS EXCL-OR QUAD 2-INP	01295 01295 04713 01295 01295	SN74LS259N SN74LS03N SN74LS173N SN74LS00N SN74LS06N
A15U31 A15U32 A15U33 A15U34	1820-1112 1820-0615 1820-1729 1820-1885	8 4 3 2	١Į	IC FF TIL LS D-TYPE POS-EDGE-TRIG IC MUXR/DATA-SEL TIL 8-TO-1-LINE 8-INP IC LCH TIL LS COM CLEAR 8-BIT IC RGTR TIL LS D-TYPE QUAD	01295 04713 01295 04713	SN74LS74AN MC8312P SN74LS259N SN74LS173N
A15W1 A15W2	05328-6011 05328-6011		2	CBL AY-HPIB SING BLUE CBL AY-HPIB SING BLUE	28480 28480	05328-60110 05328-60110
A15XJ1	1200-0567		1 2	SOCKET-IC 28-CONT DIP DIP-SLDR	28480	1200-0567
A15XU1 - A15XU21 A15XU22	1200-0567		1	NOT ASSIGNED SOCKET-IC 28-CONT DIP DIP-SLDR	28480	1200-0567

Table 6-10. Replaceable Parts

Reference Designation		CD	Qty	Description	Mfr Code	Mfr Part Number
A16	05328-60033	6	1	BD AY-DISPLAY	28480	05328-60033
A16C1	0180-0124	١	1	CAPACITOR-FXD 200UF+75-10% 6VDC AL	56289	30D207G006DC2
A16CR1 A16CR2 A16CR3 A16CR4 A16CR5	1901-0040 1901-0040 1901-0040 1901-0999 1901-0040	1 1 9 1		DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SCHOTTKY 20V 10NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35	28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0999 1901-0040
A16CR6	1901-0040	1		DIODE-SWITCHING 30V SOMA 2NS DO-35	28480	1901-0040
A16DS1 A16DS2 A16DS3 A16DS4 A16DS5	1990-0437 1990-0437 1990-0437 1990-0437 1990-0437	7777	9	DISPLAY-NUM-SEG 1-CHAR .43-H	28480 28480 28480 28480 28480 28480	5082-7751 5082-7751 5082-7751 5082-7751 5082-7751
A16DS6 A16DS7 A16DS8 A16DS9 A16DS10	1990-0437 1990-0437 1990-0437 1990-0437 1990-0404	7 7 7 7 7 8	11	DISPLAY-NUM-SEG 1-CHAR .43-H DISPLAY-NUM-SEG 1-CHAR .43-H DISPLAY-NUM-SEG 1-CHAR .43-H DISPLAY-NUM-SEG 1-CHAR .43-H LED-LAMP LUM-INT=300UCD IF=50MA-MAX	28480 28480 28480 28480 28480 28480	5082-7751 5082-7751 5082-7751 5082-7751 5082-4480
A16DS11 A16DS12 A16DS13 A16DS14 A16DS15	1990-0404 1990-0404 1990-0404 1990-0404 1990-0404	8 8 8		LED-LAMP LUM-INT:300UCD IF:SOMA-MAX LED-LAMP LUM-INT:300UCD IF:SOMA-MAX LED-LAMP LUM-INT:300UCD IF:SOMA-MAX LED-LAMP LUM-INT:300UCD IF:SOMA-MAX LED-LAMP LUM-INT:300UCD IF:SOMA-MAX	28480 28480 28480 28480 28480	5082-4480 5082-4480 5082-4480 5082-4480 5082-4480
A16DS16 A16DS17 A16DS18 A16DS19 A16DS20	1990-0404 1990-0404 1990-0404 1990-0404 1990-0404	8 8 8 8		LED-LAMP LUM-INT=300UCD IF=50MA-MAX LED-LAMP LUM-INT=300UCD IF=50MA-MAX LED-LAMP LUM-INT=300UCD IF=50MA-MAX LED-LAMP LUM-INT=300UCD IF=50MA-MAX LED-LAMP LUM-INT=300UCD IF=50MA-MAX	28480 28480 28480 28480 28480	5082-4480 5082-4480 5082-4480 5082-4480 5082-4480
A16MP1 A16MP2 A16MP3 A16MP4 A16MP5	3130-0384 3130-0384 3130-0384 3130-0384 3130-0384	3 3 3 3	8	CONTACT-SPRING .15-WD PAL-7 CONTACT-SPRING .15-WD PAL-7 CONTACT-SPRING .15-WD PAL-7 CONTACT-SPRING .15-WD PAL-7 CONTACT-SPRING .15-WD PAL-7 PART OF S3 & S4	28480 28480 28480 28480 28480	3130-0384 3130-0384 3130-0384 3130-0384 3130-0384
A16MP6 A16MP7 A16MP8 A16MP9	3130-0384 3130-0384 3130-0384 5001-0157	3 3 3	2	CONTACT-SPRING .15-WD PAL-7 CONTACT-SPRING .15-WD PAL-7 CONTACT-SPRING .15-WD PAL-7 PART OF S3 & S4 SPRING-PC	28480 28480 28480 28480	3130-0384 3130-0384 3130-0384 5001-0157
A16MP10	5001-0157	,	-	SPRING-PC	28480	5001-0157
A16MP11 A16MP12	5040-6948 5040-6948	8	2	PART OF S3 & S4 INSUL-MALE INSUL-MALE PART OF S3 & S4	28480 28480 28480	5040-6948 5040-6948
A16MP13 A16MP14	5040-6949 5040-6949	9	2	INSUL-FEM INSUL-FEM	28480 28480	5040-6949 5040-6949
A16MP1S	05328-40003			PART OF S3 & S4 SPACER-LED LONG	28480	05328-40003
A16P1	1251-2582	1	1	CONNECTOR-PC EDGE 24-CONT/ROW 2-ROWS	28480	1251-2582
A16Q1 A16Q2 A16Q3 A16Q4 A16Q5	1853-0326 1853-0326 1853-0326 1853-0326 1853-0326	33333	9	TRANSISTOR PNP SI PD=1W FT=50MHZ	04713 04713 04713 04713 04713	MPS-U51 MPS-U51 MPS-U51 MPS-U51 MPS-U51
A16Q6 A16Q7 A16Q8 A16Q9 A16Q10	1853-0326 1853-0326 1853-0326 1853-0326 1854-0492	33336		TRANSISTOR PNP SI PD=1W FT=50MHZ TRANSISTOR NPN SI PD=350MW FT=250MHZ	04713 04713 04713 04713 04713	MPS-U51 MPS-U51 MPS-U51 MPS-U51 MPS3643
	·					

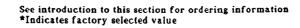


Table 6-10. Replaceable Parts (continued)

Reference Designation		CD	Qty	Description	Mfr Code	Mfr Part Number
A16Q11 A16Q12 A16Q13 A16Q14 A16Q15	1854-0492 1854-0492 1854-0492 1854-0492 1854-0492	66666		TRANSISTOR NPN SI PD=350MW FT=250MHZ	04713 04713 04713 04713 04713	MPS3643 MPS3643 MPS3643 MPS3643 MPS3643
A16Q16 A16Q17 A16Q18 A16Q19 A16Q20	1854-0492 1854-0492 1854-0492 1854-0492 1854-0492	66666		TRANSISTOR NPN SI PD=350MW FT=250MHZ	04713 04713 04713 04713 04713	MPS3643 MPS3643 MPS3643 MPS3643 MPS3643
A16Q21 A16Q22A	1854-0492 1854-0492	6 6	13	TRANSISTOR NPN SI PD=350MW FT=250MHZ TRANSISTOR NPN SI PD=350MW FT=250MHZ	04713 04713	MPS3643 MPS3643
A16R1 A16R2 A16R3 A16R4 A16R5	0683-3905 0683-3905 0683-3905 1810-0213 0683-1005	8 8 7 5	3 1 2	RESISTOR 39 5% .25W FC TC=-400/+500 RESISTOR 39 5% .25W FC TC=-400/+500 RESISTOR 39 5% .25W FC TC=-400/+500 NETWORK-RES 9-SIP10.0 OHM X 8 RESISTOR 10 5% .25W FC TC=-400/+500	01121 01121 01121 01121 91637 01121	CB3905 CB3905 CB3905 CSP09C01-100J CB1005
A16R6 A16R7 A16R8 A16R9 A16R10	2100-3455 0683-2005 0683-3325 0683-3325 0683-1005	1 7 6 6 5	1 1 2	RESISTOR-VAR CONTROL CCP 2.5M 20% 10CW RESISTOR 20 5% .25W FC TC=-400/+500 RESISTOR 3.3K 5% .25W FC TC=-400/+700 RESISTOR 3.3K 5% .25W FC TC=-400/+700 RESISTOR 10 5% .25W FC TC=-400/+500	01121 01121 01121 01121 01121	WP46048P255RZ CB2005 CB3325 CB3325 CB1005
A16R11	0683-1025	9		RESISTOR 1K 5% .25W FC TC=-400/+600	01121	CB1025
A1651 A1652 A1653 A1654	3101-2245 3101-1940 3130-0498 3130-0499	8 8 0 1	1 1	SWITCH-TGL SUBMIN DPDT 2A 250VAC PC SWITCH-PB DPDT MOM .02A 20VAC SHAFT & INDEX ASSEMBLY 1.562 STRUT CTR SHAFT & INDEX ASSEMBLY 1.562 STRUT CTR	28480 28480 28480 28480	3101-2245 3101-1940 3130-0498 3130-0499
A16XDS1 A16XDS2 A16XDS3 A16XDS4 A16XDS5	1200-0638 1200-0638 1200-0638 1200-0638 1200-0638	77777		SOCKET-IC 14-CONT DIP DIP-SLDR	28480 28480 28480 28480 28480	1200-0638 1200-0638 1200-0638 1200-0638 1200-0638
A16XDS6 A16XDS7 A16XDS8 A16XDS9	1200-0638 1200-0638 1200-0638 1200-0638	7 7 7 7	l	SOCKET-IC 14-CONT DIP DIP-SLDR SOCKET-IC 14-CONT DIP DIP-SLDR SOCKET-IC 14-CONT DIP DIP-SLDR SOCKET-IC 14-CONT DIP DIP-SLDR	28480 28480 28480 28480	1200-0638 1200-0638 1200-0638 1200-0638
	05328-20252	, ,		MISCELLANEOUS PARTS SPCR-STANDOFF	28480	05328-20252
	05328-40003	8	12	SPACER-LED LONG	28480	05328-40003

Table 6-11. Replaceable Parts (continued)

Reference Designation	HP Part Number	C D	Qty	Description	Mfr Code	Mfr Part Number
				CABINET PARTS AND HARDWARE		
81	3160-0209	4	1	FAN-TBAX 32-CFM 105-125V 50/60-HZ	23936	8500D
C1 C2 C3	0160-4355 0160-4355	6 6	2	NOT ASSIGNED CAPACITOR-FXD .01UF +-10% 250VAC(RMS) CAPACITOR-FXD .01UF +-10% 250VAC(RMS)	28480 28480	0160-4355 0160-4355
F1	2110-0001	8	1	FUSE 1A 250V NTD 1.25X.25 UL	75915	312001
H1 H2 H3 H4 H5	2190-0060 3050-0017 2190-0060 2950-0052 3101-0851	7 9 7 9 8	7 3 3 1	WASHER-LK INTL T 1/4 IN .256-IN-ID WASHER-FL MTLC 1/4 IN .26-IN-ID WASHER-LK INTL T 1/4 IN .256-IN-ID NUT-HEX-DBL-CHAM 1/4-40-THD .062-IN-THK CAP-PUSHBUTTON BLACK; .2-IN DIA; .155-IN	28480 28480 28480 00000 28480	2190-0060 3050-0017 2190-0060 ORDER BY DESCRIPTION 3101-0851
H6 H7 H8 H9 H10	2190-0060 3050-0052 2190-0060 2950-0052 2950-0001	7 2 7 9 8	1	WASHER-LK INTL T 1/4 IN .256-IN-ID WASHER-FL MTLC NO. 8 .166-IN-ID WASHER-LK INTL T 1/4 IN .256-IN-ID NUT-HEX-DBL-CHAM 1/4-40-THD .062-IN-THK NUT-HEX-DBL-CHAM 3/8-32-THD .094-IN-THK	28480 28480 28480 00000 00000	2190-0060 3050-0052 2190-0060 ORDER BY DESCRIPTION ORDER BY DESCRIPTION
H11 H12 H13 ~H14 H15	2190-0858 2190-0016 2950-0001 0370-2994 2190-0858	1 3 8 0 1	3 4 2	WASHER-FL MTLC 5/16 IN .375-IN-ID WASHER-LK INTL T 3/8 IN .377-IN-ID NUT-HEX-DBL-CHAM 3/8-32-THD .094-IN-THK KNOB WASHER-FL MTLC 5/16 IN .375-IN-ID	28480 28480 00000 28480 28480	2190-0858 2190-0016 ORDER BY DESCRIPTION 0370-2994 2190-0858
H16 H17 	2190-0016 2950-0001 0370-2994 2190-0060 3050-0017	3 8 0 7 9		WASHER-LK INTL T 3/8 IN .377-IN-ID NUT-HEX-DBL-CHAM 3/8-32-THD .094-IN-THK KNOB WASHER-LK INTL T 1/4 IN .256-IN-ID WASHER-FL MTLC 1/4 IN .26-IN-ID	28480 00000 28480 28480 28480	2190-0016 ORDER BY DESCRIPTION 0370-2994 2190-0060 3050-0017
H21 H22 H23 H24 H25	2190-0060 2950-0072 0370-1005 0370-1097 2950-0035	7 3 2 2 8	2 2 2 4	WASHER-LK INTL T 1/4 IN .256-IN-ID NUT-HEX-DBL-CHAM 1/4-32-THD .062-IN-THK KNOB-BASE-PTR 3/8 JGK .125-IN-ID KNOB-BASE-PTR 1/2 JGK .125-IN-ID NUT-HEX-DBL-CHAM 15/32-32-THD	28480 00000 28480 28480 00000	2190-0060 ORDER BY DESCRIPTION 0370-1005 0370-1097 ORDER BY DESCRIPTION
H26 H27 H28 H29 H30	2950-0035 2950-0035 2950-0035 0370-1097 6960-0002	8 8 8 2 4	1	NUT-HEX-DBL-CHAM 15/32-32-THD NUT-HEX-DBL-CHAM 15/32-32-THD NUT-HEX-DBL-CHAM 15/32-32-THD KNOB-BASE-PTR 1/2 JGK .125-IN-ID PLUG-HOLE DOME-HD FOR .5-D-HOLE STL	00000 00000 00000 28480 28480	ORDER BY DESCRIPTION ORDER BY DESCRIPTION ORDER BY DESCRIPTION 0370-1097 6960-0002
H31 H32 H33 H34 H35	6960-0001 2420-0014 0340-0765 0570-0111 2190-0017	3 0 6 3 4	1 1 1 1	PLUG-HOLE DOME-HD FOR .375-D-HOLE STL NUT-HEX-DBL-CHAM 6-32-THD .125-IN-THK INSULATOR-XSTR KAPTON SCREW-MACH 6-32 .375-IN-LG RD-HD-SLT WASHER-LK HLCL NO. 8 .168-IN-ID	28480 00000 28480 00000 28480	6960-0001 ORDER BY DESCRIPTION 0340-0765 ORDER BY DESCRIPTION 2190-0017
H36 H37 H38 H39 H40	0380-0643 2950-0001 2190-0016 2510-0043 2360-0113	3 8 3 6 2	1 1	STANDOFF-HEX .255-IN-LG 6-32THD NUT-HEX-DBL-CHAM 3/8-32-THD .094-IN-THK WASHER-LK INTL T 3/8 IN .377-IN-ID SCREW-MACH 8-32 .312-IN-LG PAN-HD-POZI SCREW-MACH 6-32 .25-IN-LG PAN-HD-POZI	00000 00000 28480 00000 00000	ORDER BY DESCRIPTION ORDER BY DESCRIPTION 2190-0016 ORDER BY DESCRIPTION ORDER BY DESCRIPTION
H41 H42 H43 H44 H45	2360-0117 2360-0119 2680-0128 2360-0359 2360-0125	6 8 7 8 6	1 1 1 1	SCREW-MACH 6-32 .375-IN-LG PAN-HD-POZI SCREW-MACH 6-32 .438-IN-LG PAN-HD-POZI SCREW-MACH 10-32 .25-IN-LG PAN-HD-POZI SCREW-MACH 6-32 .375-IN-LG PAN-HD-POZI SCREW-MACH 6-32 .75-IN-LG PAN-HD-POZI	00000 00000 00000 00000	ORDER BY DESCRIPTION
H46 H47 H48 H49 H50	2360-0115 2190-0858 3050-0017 3050-0003 3050-0001	4 1 9 3 1	1 1	SCREW-MACH 6-32 .312-IN-LG PAN-HD-POZI WASHER-FL MTLC 5/16 IN .375-IN-ID WASHER-FL MTLC 1/4 IN .26-IN-ID WASHER-FL MM NO. 6 .141-IN-ID .375-IN-OD WASHER-FL MTLC NO. 8 .172-IN-ID	00000 28480 28480 28480 28480	ORDER BY DESCRIPTION 2190-0858 3050-0017 3050-0003 3050-0001
H51 H52 H53 H54 H55	3050-0010 0515-1331 2950-0052 2420-0001 2190-0016	2 5 9 5 3	1	WASHER-FL MTLC NO. 6 .147-IN-ID SCREW-METRIC SPECIALTY M4 X 0.7 THD; 6 NUT-HEX-DBL-CHAM 1/4-40-THD .062-IN-THK NUT-HEX-W/LKWR 6-32-THD .109-IN-THK WASHER-LK INTL T 3/8 IN .377-IN-ID	28480 28480 00000 00000 28480	3050-0010 0515-1331 ORDER BY DESCRIPTION ORDER BY DESCRIPTION 2190-0016



Table 6-11. Replaceable Parts (continued)

Lug B. Jol J. Mrs. J.								
Reference Designation	HP Part Number	C	Qty	Description	Mfr Code	Mfr Part Number		
H56 H57 H58 H59 H60 H61	2190-0060 2190-0011 0515-0896 2360-0476 2950-0001	78508 3	1 1	WASHER-LK INTL T 1/4 IN .256-IN-ID WASHER-LK INTL T NO. 10 .195-IN-ID SCREW-MACH M4 X 0.7 10MM-LG SCREW-MACH 6-32 .188-IN-LG 100 DEG NUT-HEX-DBL-CHAM 3/8-32-THD .094-IN-THK NUT-HEX-DBL-CHAM 1/4-32-THD .062-IN-THK	28480 28480 28480 00000 00000	2190-0060 2190-0011 0515-0896 ORDER BY DESCRIPTION ORDER BY DESCRIPTION ORDER BY DESCRIPTION		
H62 H63 H64 H65	0380-0529 1400-0249 1400-0249 1200-0547	4007	1 2 1	STANDOFF-HEX 1.25-IN-LG 6-32THD CABLE TIE .062625-DIA .091-WD NYL CABLE TIE .062625-DIA .091-WD NYL LOCK-DUAL INLINE PKG IC FOR 14 PIN	00000 06383 06383 28480	ORDER BY DESCRIPTION PLT1M-8 1200-0547		
H66	2360-0115	4		SCREW-MACH 6-32 .312-IN-LG PAN-HD-POZI	00000	ORDER BY DESCRIPTION		
J1 J2 J3 J4 J5	1250-0082 1250-0082 1250-0082 1250-0082 1250-0082 1251-2357	00000	1	ADAPTER-COAX STR M-BNC M-N ADAPTER-COAX STR M-BNC M-N ADAPTER-COAX STR M-BNC M-N ADAPTER-COAX STR M-BNC M-N CONNECTOR-AC PUR HP-9 MALE FLG-MTG	28480 28480 28480 28480 28480 28480	1250-0082 1250-0082 1250-0082 1250-0082 1251-2357		
J6- J209 J210 J210A J210B	1510-0091 2950-0144 2420-0001	3 0 5	2 2 3	NOT ASSIGNED BINDING POST SGL SGL-TUR JGK RED NUT-HEX-DBL-CHAM 3/8-32-THD .188-IN-THK NUT-HEX-W/LKWR 6-32-THD .109-IN-THK	28480 00000 00000	1510-0091 ORDER BY DESCRIPTION ORDER BY DESCRIPTION		
J211 J211A J211B J212-	1510-0091 2950-0144 2420-0001	3 0 5		BINDING POST SGL SGL-TUR JGK RED NUT-HEX-DBL-CHAM 3/8-32-THD .188-IN-THK NUT-HEX-W/LKWR 6-32-THD .109-IN-THK NOT ASSIGNED	28480 00000 00000	1510-0091 ORDER BY DESCRIPTION ORDER BY DESCRIPTION		
J309 J310 J310F1 J311 J311A	1250-1899 2110-0301 05328-60113 2950-0001	9138	1 3 1 6	ADAPTER-COAX STR F-BNC M-SMC FUSE .125A 125V .281X.093 CBL AY-PROBE PUR NUT-HEX-DBL-CHAM 3/8-32-THD .094-IN-THK	28480 28480 28480 00000	1250-1899 2110-0301 05328-60113 ORDER BY DESCRIPTION		
MP1 MP2 MP3 MP4 MP5	5021-5801 5021-5802 5021-5831 5020-8895-	01668	1 3 3 1	FRAME, FRONT, FULL (METRIC) FRAME, REAR SIDE STRUTS HANDLE FRONT FOOT(STANDARD)	28480 28480 28480 28480 28480	5021-5801 5021-5802 5021-5831 5020-8895: 5021-8495 5040-7201 5041-6801 5040-7202-5041-9502		
MP6 MP7 MP8 MP9 MP10	6040~7202 05328-0001 05328-0002 05328-0003 05328-0006	3 4 7	1 1 1 1	TRIM, TOP BRKT-MAIN BRKT-CORNER BRKT-FRONT BRKT PWR SUP-FAN	28480 28480 28480 28480 28480	05328-00002 05328-00003 05328-00006		
MP11 00014 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	05328-00012- 05328-00013 1460-1345 5061-9451 05328-60130	5 6 5 8	1 1 1 1	PNL REAR PLT-COV HPIB TILT STAND SST COV-SIDE COV-TOP	28480 28480 28480 28480 28480	05328=000+2 C5 3 ZE - 0003 4 -05328=000+3 - C5 3 ZE - 00014 1460-1345 - 506 Z - 375 I 5061-9451 - 6013 3		
MP16 MP16A MP17 MP18 MP19	5061-9446. 05328-60015 05328-20226 05328-20227 05328-20228	5 6	1 1 1 1	COV-BOTTOM INSULATOR-POWER SUPPLY PNL-FRT STD PNL-FRT DVM PNL-FRT C CHAN	28480 28480 28480 28480 28480	-5061-9446- 5062 - 3746 05328-60015 05328-20226 05328-20227 05328-20228		
MP20 MP21 MP22 2625 MP23 MP24	05328-20229 4040-1062 95328-2 021 9 0370-1005 5061 <u>-9498</u>	6	1 1	PNL-FRT DVM/°C° PANEL 1.18-IN-WD PANEL-DISPLAY KNOB-BASE-PTR 3/8 JGK .125-IN-ID HANDLES, FRONT	28480 28480 28480 28480 28480	05328-20229 4040-1062 05328-20219 0370-1005 5061-9498 5062-3778		
MP25	05328-20223	2	1	HPIB SHIELD	28480	05328-20223		
№ S1	3101-1609	6	1	SWITCH-SL 2-DPDT STD 1.5A 250VAC	28480	3101-1609		
T1 W1 W2 W3 W4 W5	9100-3046 8120-1378 8120-2176 8120-0520 8120-0520 8120-0520	3 1 9 3 3	1 1 3	TRANSFORMER-POWER 100/120/220/240V CABLE ASSY 18AWG 3-CNDCT JGK-JKT CABLE ASSY 26AWG 28-CNDCT CABLE ASSY 26AWG 14-CNDCT CABLE ASSY 26AWG 14-CNDCT CABLE ASSY 26AWG 14-CNDCT	28480 28480 28480 28480 28480 28480	9100-3046 8120-1378 8120-2176 8120-0520 8120-0520 8120-0520		
	_							

Table 6-11. Replaceable Parts (continued)

Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
XF1A XF1B XF1C XF1D	2110-0564 2110-0565 2110-0569 1400-0090	8 9 3 9	1 1 1	FUSEHOLDER BODY 12A MAX FOR UL FUSEHOLDER CAP 12A MAX FOR UL FUSEHOLDER COMPONENT NUT; THREAD M12.7 FUSEHOLDER COMPONENT FOR USE ON	H9027 28480 28480 28480	031.1657 2110-0565 2110-0569 1400-0090
	0340-1102, 7120-0644 8159-0005 05305-20105	7 1 0 2	1 1 6 1	MISCELLANEOUS INSULATOR-XFORMR LABEL-WARNING 1.25-IN-WD 2-IN-LG AL RESISTOR-ZERO OHMS 22 AWG LEAD DIA INSUL	28480 28480 28480 28480	0340-1102 7120-0644 8159-0005 05305-20105

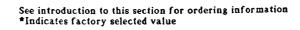


Table 6-12. Code List of Manufacturers

Mfr Code	Manufacturer Name	Address	Zip Code
H9027 00000 01121 01295 04713 06383 07263 23936 224546 27014 26480 30983 52763 56289 72136 75915 84411 91637	SCHURTER A G H ANY SATISFACTORY SUPPLIER ALLEN-BRADLEY CO TEXAS INSTR INC SEMICOND CMPNT DIV MOTOROLA SEMICONDUCTOR PRODUCTS PANDUIT CORP FAIRCHILD SEMICONDUCTOR DIV PAMOTOR DIV WILLIAM J PURDY CORNING GLASS WORKS (BRADFORD) MATIONAL SEMICONDUCTOR CORP HEWLETT-PACKARD CO CORPORATE HQ MEPCO/ELECTRA CORP STETINER ELECTRONICS INC SPRAGUE ELECTRIC CO ELECTRO MOTIVE CORP LITTELFUSE INC TRW CAPACITOR DIV DALE ELECTRONICS INC	LUZERN SW MILWAUKEE WI TX DALLAS TX PHOENIX AZ TINLEY PARK IL MOUNTAIN VIEW CA BURLINGAME CA BRADFORD PA SANTA CLARA CA PALO ALTO CA SAN DIEGO CA CHATTANOOGA TN NORTH ADAMS MA FLORENCE SC DES PLAINES IL OGALLALA NE COLUMBUS NE	53204 75222 85008 60477 94042 94010 16701 95051 94304 92121 13035 01247 06226 60016 69153 68601

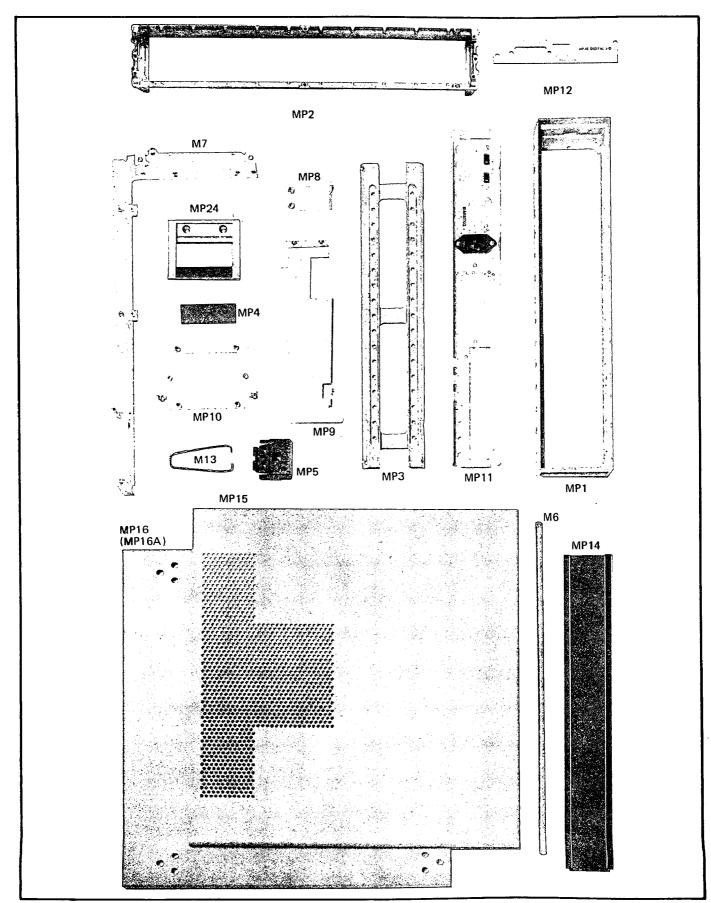


Figure 6-1. Cabinet Parts

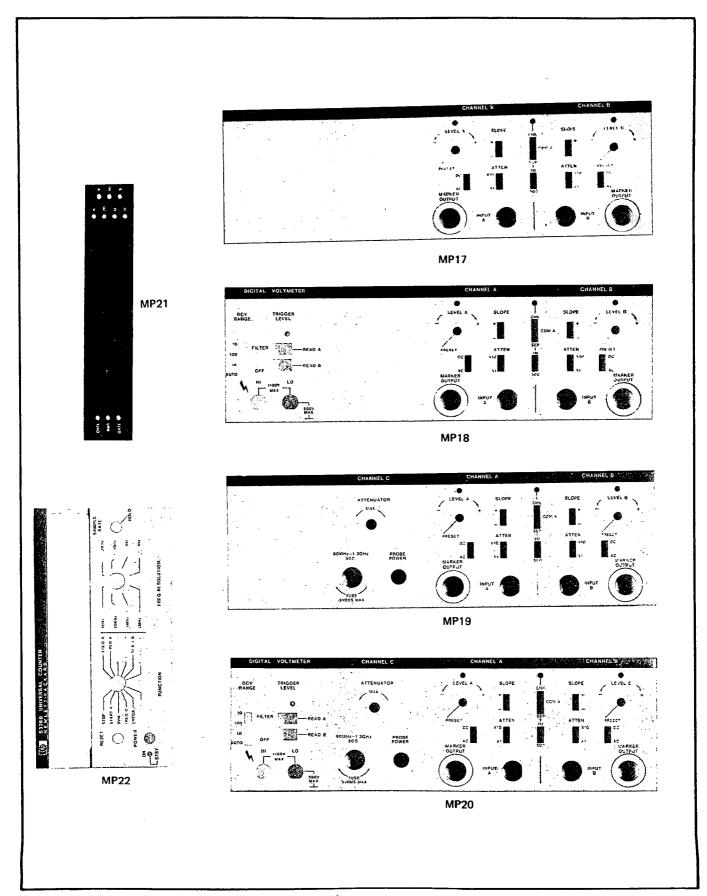


Figure 6-1. Cabinet Parts (continued)

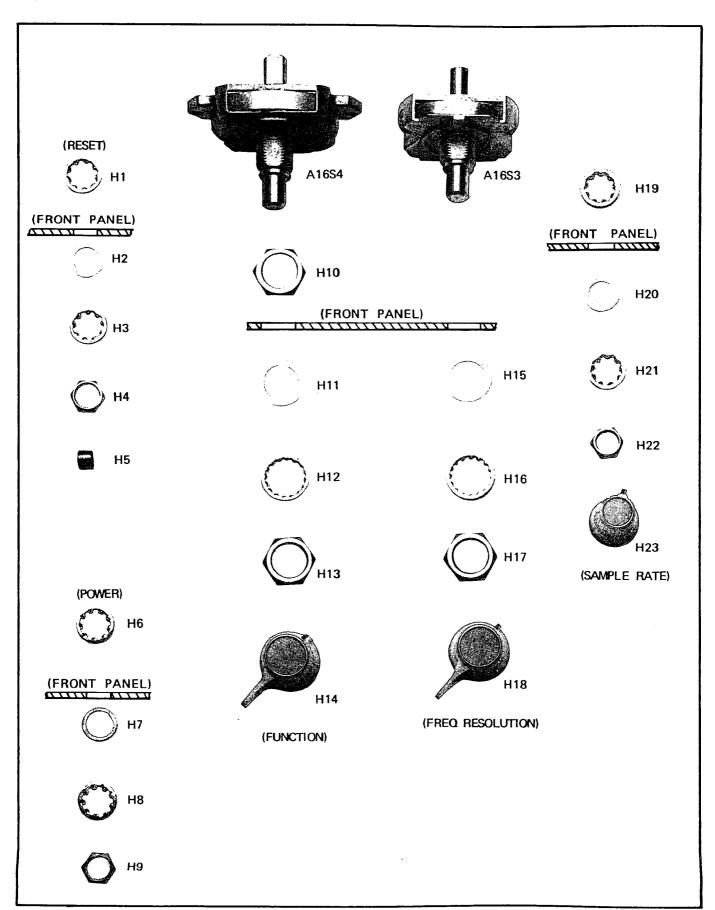


Figure 6-2. Front Panel Display Hardware

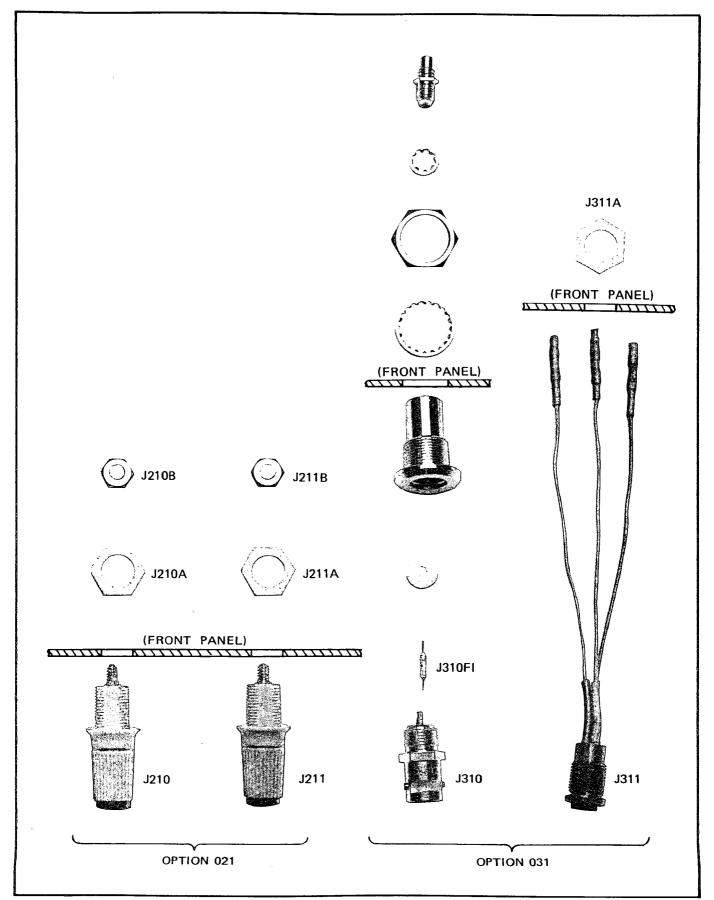


Figure 6-3. Option 050 (Options 021 & 031) Front Panel Hardware

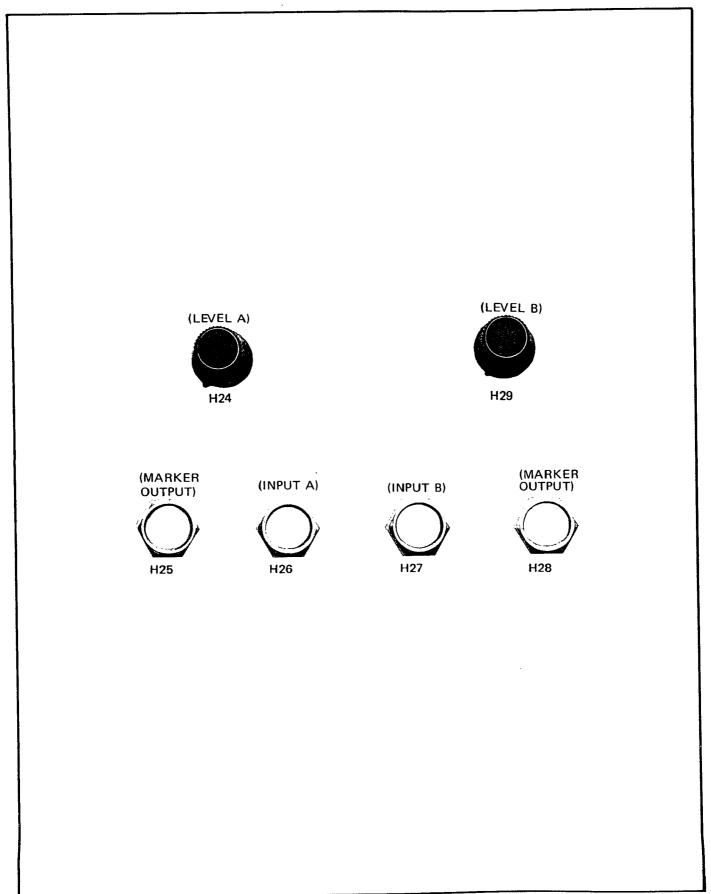


Figure 6-4. Programmable Input Front Panel Hardware

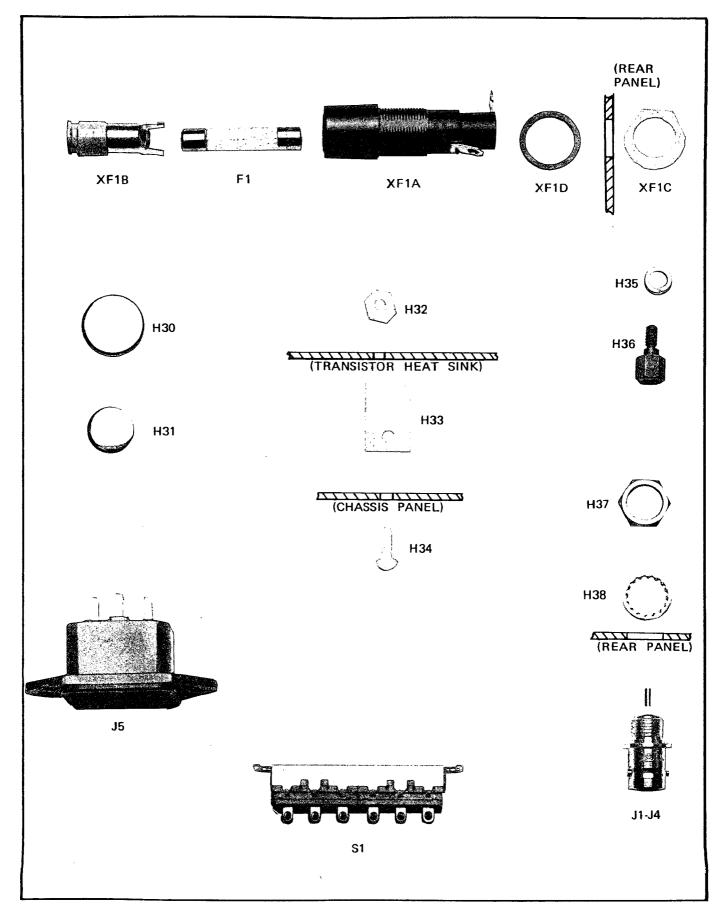


Figure 6-5. Rear Panel Chassis Parts

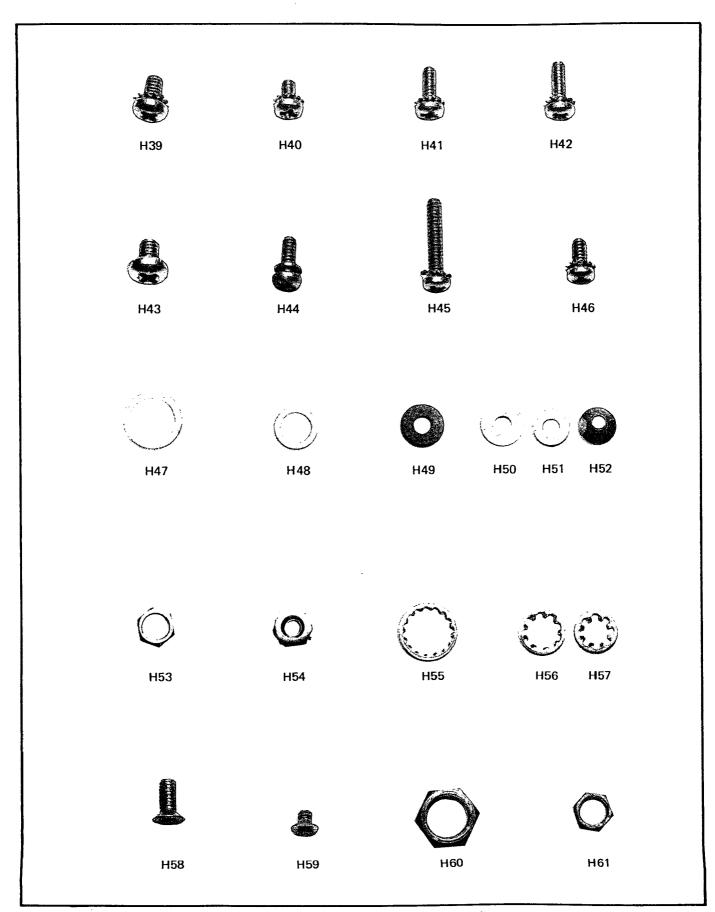


Figure 6-6. Miscellaneous Cabinet Hardware

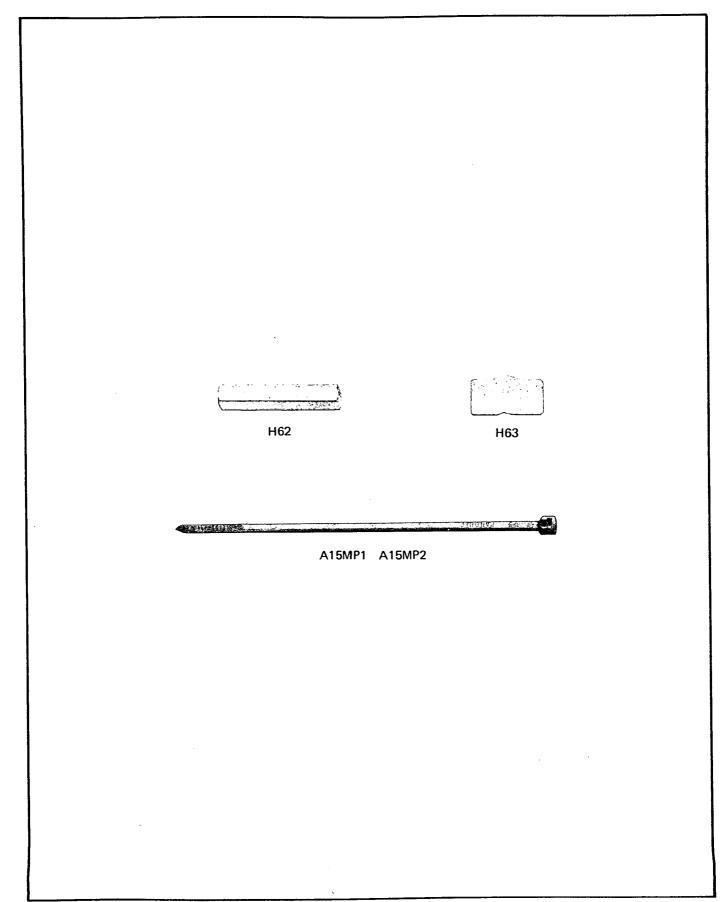


Figure 6-7. Internal Cabinet Parts

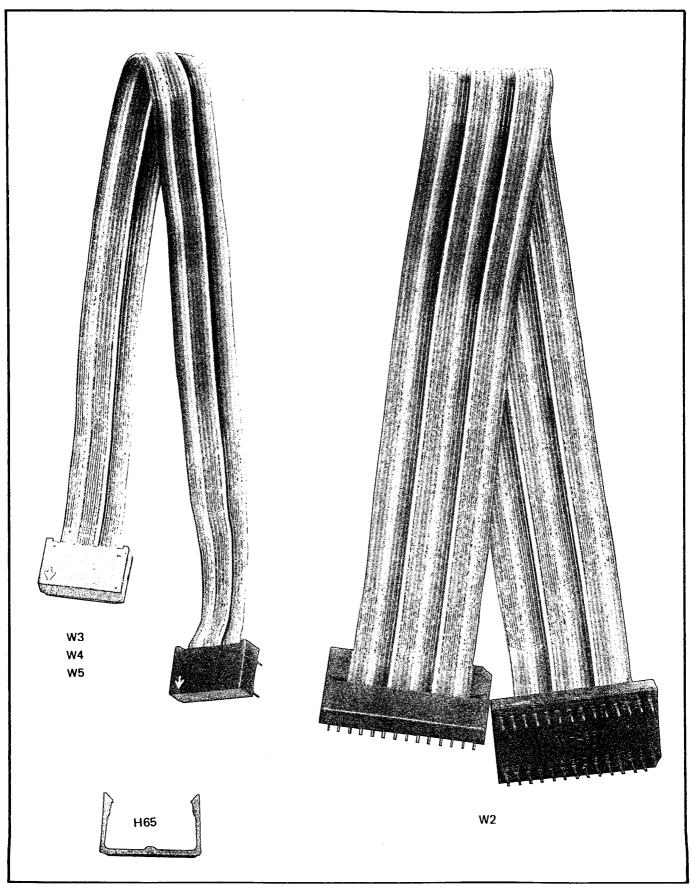


Figure 6-8. Ribbon Cables

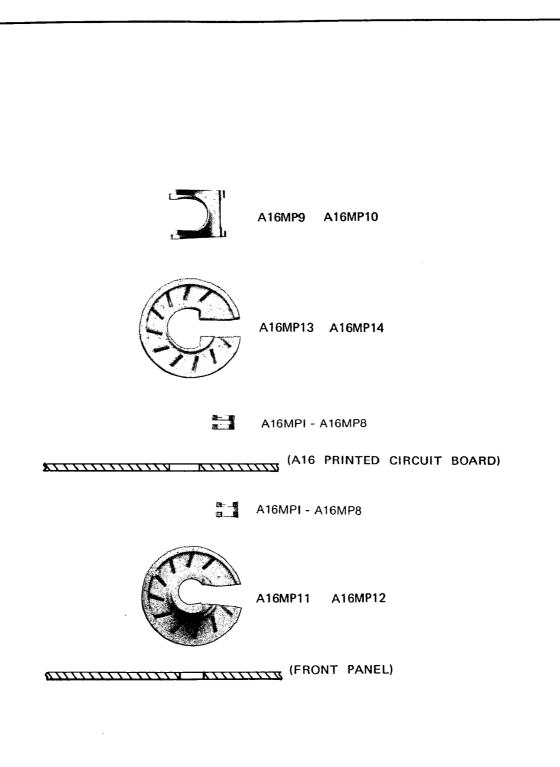


Figure 6-9. Display Panel Switch Assembly A16S3 and A16S4

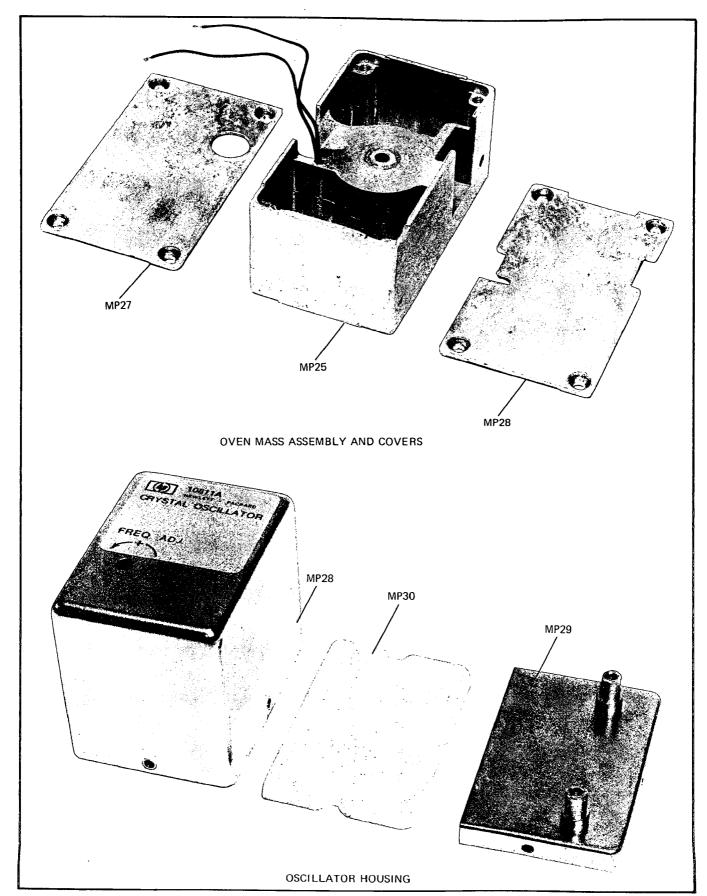


Figure 6-10. Option 010 Oven Oscillator Mechanical Parts

SECTION VII MANUAL CHANGES

7-1. INTRODUCTION

7-2. This section contains information for adapting this manual to instruments with serial prefixes other than that listed on the title page. Refer to Section I for additional important information about serial number coverage.

7-3. MANUAL CHANGES

- 7-4. Instruments having serial number prefixes higher than those listed on the title page of this manual are covered with a "Manual Changes" sheet included with this manual. If this change sheet is missing, the information can be supplied by any Hewlett-Packard Sales and Service Office listed at the back of this manual.
- 7-5. If your instrument's serial number prefix is lower than that listed on the title page of this manual, this manual must be modified to correctly apply to your instrument. To determine which changes must be made to this manual, locate your instrument's serial number prefix in *Table 7-1* and make the indicated changes.

Table 7-1. Manual Backdating

IF YOUR 5328B HAS SERIAL PREFIX	THEN MAKE THE FOLLOWING CHANGES TO THIS MANUAL
2450	. 1
2408	1, 2

Change 1 (2450A)

Table 6-11. Cabinet Parts and Hardware:

Change H52 from 0515-1331 to 2510-0192 machine screw 8-32 .25 inches long.

Change H58 from 0515-0896 to 2510-0195 machine screw 8-32 .375 inches long.

Change MP1 from 5021-5801 to 5020-8801 FRAME, FRONT.

Change MP2 from 5021-5802 to 5020-8802 FRAME, REAR.

Change MP3 from 5021-5831 to 5020-8831 SIDE STRUT.

Change MP14 from 5061-9451 to 5060-9851 COVER, SIDE.

Change MP15 from 05328-60130 to 05328-0016 COVER, TOP.

Change MP16 from 5061-9446 to 5060-9846 COVER, BOTTOM.

Change MP24 from 5061-9498 to 5060-9898 HANDLE, FRONT.

Change Option 908 RACK FLANGE KIT (FOR INSTRUMENTS WITHOUT HANDLES) from 5061-9674 to 5061-0074.

Change 2 (2408A)

Table 6-2, A1 Motherboard (05328-60049) Replaceable Parts: Replace the parts list with the parts list shown in Table 7-2.

Figure 8-A-10, A1 Motherboard (05328-60049) Schematic Diagram: Replace the schematic with the schematic shown in Figure 7-1.

Table 7-2. Replaceable Parts

				rable 7-2. Replaceable Faits		
Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A1	05328-60001	8	1	BOARD ASSY-MOTHERBOARD (SERIES 2408)	28480	05328-60001
A1C1 A1C2 A1C3 A1C4 A1C5	0180-0100 0160-0161 0180-0106	3 4 9	1 2 2	NOT ASSIGNED NOT ASSIGNED CAPACITOR-FXD 4.7UF+-10% 35VDC TA CAPACITOR-FXD .01UF +-10% 200VDC POLYE CAPACITOR-FXD 60UF+-20% 6VDC TA	56289 28480 56289	1500475X9035B2 0160-0161 1500606X0006B2
A1C6 A1C7 A1C8 A1C9	0140-0177 0170-0024 0180-0195	0 9 6	1 1 1	CAPACITOR-FXD 400PF +-1% 300YDC MICA CAPACITOR-FXD .022UF +-20% 200VDC POLYE CAPACITOR-FXD .33UF+-20% 35VDC TA NOT ASSIGNED	72136 28480 56289	DM15F401F0300WV1CR 0170-0024 150D334X0035A2
A1C10	0160-0314	9	1	CAPACITOR-FXD .01UF +-5% 400VDC POLYE	84411	663UW10354W2
A1C11- A1C16 A1C17* A1C18 A1C19	0160-2264 0121-0060	2 0	1	NOT ASSIGNED CAPACITOR-FXD 20PF +-5% 500VDC CER 0+-30 CAPACITOR-V TRMR-CER 2-8PF 350V PC-MTG NOT ASSIGNED	28480 52763	0160-2264 304322 2/8PF NPO
A1C20 A1C21 A1C22 A1C23- A1C28	0160-0161 0180-0210 0160-2055	4 6 9	5 2	CAPACITOR-FXD .01UF +-10% 200VDC POLYE CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD .01UF +80-20% 100VDC CER NOT ASSIGNED	28480 56289 28480	0160-0161 1500335X0015A2 0160-2055
A1C29 A1C30 A1C31 A1C32	0180-0230 0160-0153 0180-0230	0 4 0	2	CAPACITOR-FXD 1UF+-20% SOVDC TA CAPACITOR-FXD 1000PF +-10% 200VDC POLYE CAPACITOR-FXD 1UF+-20% SOVDC TA NOT ASSIGNED	56289 28480 56289	150D105X0050A2 0160-0153 150D105X0050A2
A1C33	0160-4554	7	7	CAPACITOR-FXD .01UF +-20% 50VDC CER	28480	0160-4554
A1C34 A1C35 A1C36 A1C37 A1C37	0180-0210 0160-4554 0180-0210 0160-4554 0180-0106	6 7 6 7 9		CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD .01UF +-20% 50VDC CER CAPACITOR-FXD 3.3UF+-20% 15VDC TA CAPACITOR-FXD .01UF +-20% 50VDC CER CAPACITOR-FXD 60UF+-20% 6VDC TA	56289 28480 56289 28480 56289	150D335X0015A2 0160-4554 150D335X0015A2 0160-4554 150D606X0006B2
A1C38 A1C39 A1C40 A1C41 A1C42	0160-2055 0160-4554 0180-0155 0160-4554 0180-0155	9 7 8 7 8	6	CAPACITOR-FXD .01UF +80-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 50VDC CER CAPACITOR-FXD 2.2UF+-20% 20VDC TA CAPACITOR-FXD .01UF +-20% 50VDC CER CAPACITOR-FXD 2.2UF+-20% 20VDC TA	28480 28480 56289 28480 56289	0160 - 2055 0160 - 4554 1500225X0020A2 0160 - 4554 1500225X0020A2
A1C43 A1C44 A1C45 A1C46 A1C47	0160-4554 0160-4554 0180-0210 0180-0210 0160-4084	7 7 6 6 8	1	CAPACITOR-FXD .01UF +-20% SOVDC CER CAPACITOR-FXD .01UF +-20% SOVDC CER CAPACITOR-FXD 3.3UF+-20% 1SVDC TA CAPACITOR-FXD 3.3UF+-20% 1SVDC TA CAPACITOR-FXD .1UF +-20% SOVDC CER	28480 28480 56289 56289 28480	0160 - 4554 0160 - 4554 1500335X0015A2 1500335X0015A2 01-R0 - 4084
A1C48 A1C49 A1C50 A1C51 A1C52	0180-0155 0180-0155 0160-3879 0160-3879 0180-0155	8 8 7 7 8	2	CAPACITOR-FXD 2.2UF+-20% 20VDC TA CAPACITOR-FXD 2.2UF+-20% 20VDC TA CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD .01UF +-20% 100VDC CER CAPACITOR-FXD 2.2UF+-20% 20VDC TA	56289 56289 28480 28480 56289	1500225X0020A2 1500225X0020A2 0160-3879 0160-3879 1500225X0020A2
A1C53	0180-0155	8		CAPACITOR-FXD 2.2UF+-20% 20VDC TA	56289	150D225X0020A2
A1CR1 A1CR2	1901-0040	1	12	DIODE-SWITCHING 30V SOMA 2NS DO-35 NOT ASSIGNED	28480	1901-0040
A1CR3 A1CR4	1910-0016	٥	3	DIODE-GE 60V 60MA 1US DO-7 NOT ASSIGNED	28490	1910-0016
A1CR5	1901-0050	3		DIODE-SWITCHING 80V 200MA 2NS DO 35	28480	1901-0050
A1CR6 A1CR7 A1CR8 A1CR9 A1CR10	1901-0050 1901-0535 1902-3082 1901-0040 1901-0040	9 1 1	1	DIODE-SWITCHING 80V 200MA 2NS DO-35 DIODE-SM SIG SCHOTTKY DIODE-ZNR 4.64V 5% DO-35 PD=.4W DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35	28480 28480 28480 28480 28480	1901-0050 1901-0535 1902-3082 1901-0040 1901-0040
A1CR11 A1CR12 A1CR13 A1CR14 A1CR15	1901-0040 1901-0040 1901-0040 1901-0040 1910-0016	1 1 1 0		DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-GE 60V 60MA 1US DO-7	28480 28480 28480 28480 28480 28480	1901-0040 1901-0040 1901-0040 1901-0040 1910-0016

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Table 7-2. Replaceable Parts (continued)

	1	Τ_		` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` `	,	
Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A1CR16 A1CR17 A1CR18 A1CR19 A1CR20	1901-0040 1901-0040 1910-0016 1901-0040 1901-0040 1901-0040	1 1 0 1 1		DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SEG 60V 60MA 1US DO-7 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35	28480 28480 28480 28480 28480 28480	1901-0040 1901-0040 1910-0016 1910-0040 1901-0040
A1L1 A1Q1 A1Q2 A1Q3 A1Q4 A1Q5	9100-2276 1854-0071 1853-0015 1853-0015 1854-0071	9 7 7 7 7 7 7	1 6 2	INDUCTOR RF-CH-MLD 100UH 10% .105DX.26LG TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR PNP SI PD=200MW FT=500MHZ TRANSISTOR PNP SI PD=200MW FT=500MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ	28480 28480 28480 28480 28480 28480	9100 · 2276 1854-0071 1853-0015 1853-0015 1854-0071
A1Q6 A1Q7 A1Q8 A1Q9 A1Q10	1854-0092 1854-0071 1854-0071 1854-0071 1853-0036	2 7 7 7 2	1	TRANSISTOR NPN SI PD=200MW FT=600MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR NPN SI PD=300MW FT=200MHZ TRANSISTOR PNP SI PD=310MW FT=250MHZ	28480 28480 28480 28480 28480	1854-0092 1854-0071 1854-0071 1854-0071 1853-0036
A1R1 A1R2 A1R3 A1R4* A1R5	0698-3444 1810-0055 0683-2725 0683-3355 1810-0055	1 5 8 2 5	1 5 1	RESISTOR 316 1% .125W F TC=0+-100 NETWORK-RES 9-SIP10.0K OHM X 8 RESISTOR 2.7K 5% .25W FC TC=-400/+700 RESISTOR 3.3M 5% .25W FC TC=-900/+1100 NETWORK-RES 9-SIP10.0K OHM X 8	24546 28480 01121 01121 28480	C4-1/8-T0-316R-F 1810-0055 CB2725 CB3355 1810-0055
A1R7 A1R8 A1R9 A1R10	0683-3315 0683-1035 0683-4725 0683-1525 0683-1025	4 1 2 4 9	1 20 3 4 8	RESISTOR 330 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 4.7K 5% .25W FC TC=-400/+700 RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 1K 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB3315 CB1035 CB4725 CB1525 CB1025
A1R12 A1R13 A1R14 A1R15	0683-1025 0683-1035 0683-4725 1810-0041	9 1 2 9	1	RESISTOR 1K 5% .25W FC TC=-400/+600 NOT ASSIGNED RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 4.7K 5% .25W FC TC=-400/+700 NETWORK-RES 9-SIP2.7K OHM X 8	01121 01121 01121 28480	CB1025 CB1035 CB4725 1810-0041
A1R17 A1R18 A1R19 A1R20 A1R21	0683-1035 0683-1525 1810-0055 0683-1525	1 4 5 4 0	2	NOT ASSIGNED RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 1.5K 5% .25W FC TC=-400/+700 NETWORK-RES 9-SIP10.0K 0HM X 8 RESISTOR 1.5K 5% .25W FC TC=-400/+700 RESISTOR 46.4 1% .125W F TC=0+-100	01121 01121 28480 01121	CB1035 CB1525 1810-0055 CB1525
A1R22 A1R23 A1R24 A1R25	0683-2715 0683-2015 0683-1025 0683-1525	6 9 9	3	RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 200 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1.5K 5% .25W FC TC=-400/+700	24546 01121 01121 01121 01121	C4-1/8-T0-46R4-F CB2715 CB2015 CB1025 CB1525
A1R27 A1R28 A1R29 A1R30	0683-1025 0757-0928 1810-0055 0757-0952 0683-1035	96561	1	RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1.5K 2% .125W F TC=0+-100 NETWORK-RES 9-SIP10.0K 0HM X 8 RESISTOR 15K 2% .125W F TC=0+-100 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 24546 28480 24546 01121	CB1025 C4-1/8-T0-1501-G 1810-0055 C4-1/8-T0-1502-G CB1035
A1R31 A1R32 A1R33 A1R34 A1R35	0683-5105 0683-1035 0683-1035 0683-2715 0683-5115	4 1 6 6	1	RESISTOR 51 5% .25W FC TC=-400/+500 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+600 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 510 5% .25W FC TC=-400/+600	01121 01121 01121 01121 01121	CB5105 CB1035 CB1035 CB2715 CB5115
A1R36 A1R37 A1R38 A1R39 A1R40	0683-1035 0683-2715 0683-1035 0683-1035 0683-1035	1 6 1 1 1		RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 270 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB1035 CB2715 CB1035 CB1035 CB1035
A1R41 A1R42 A1R43 A1R44 A1R45	0683-1035 1810-0055 0683-1025 0683-1035 0683-1035	1 5 9 1 1		RESISTOR 10K 5% .25W FC TC=-400/+700 NETWORK-RES 9-SIP10.0K ONUT X 8 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 28480 01121 01121 01121	CB1035 1810-0055 CB1025 CB1035 CB1035

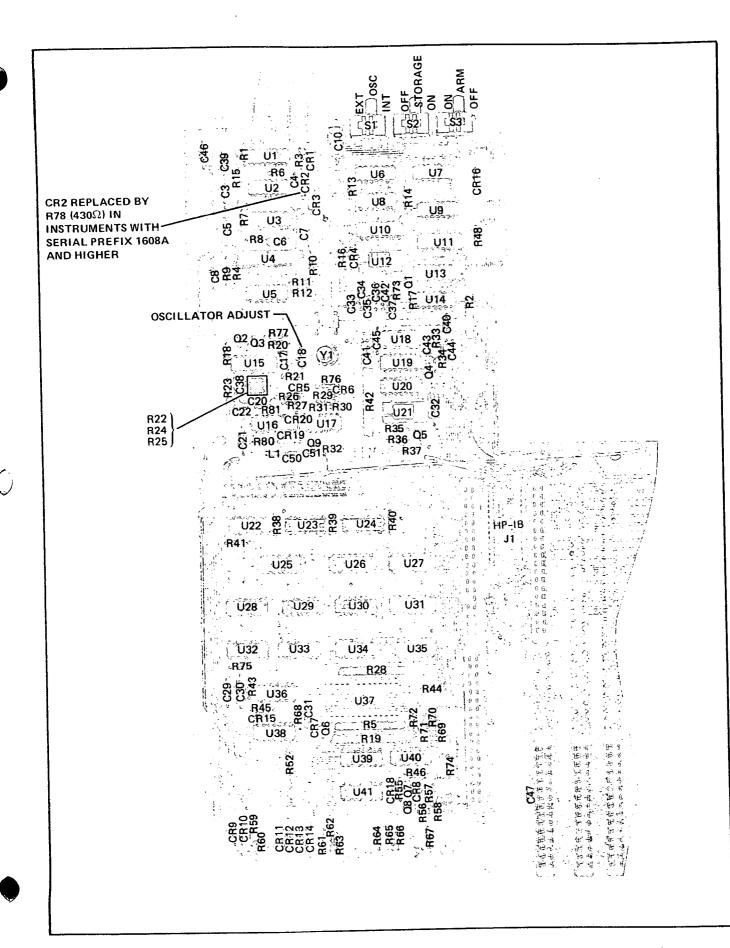
Table 7-2. Replaceable Parts (continued)

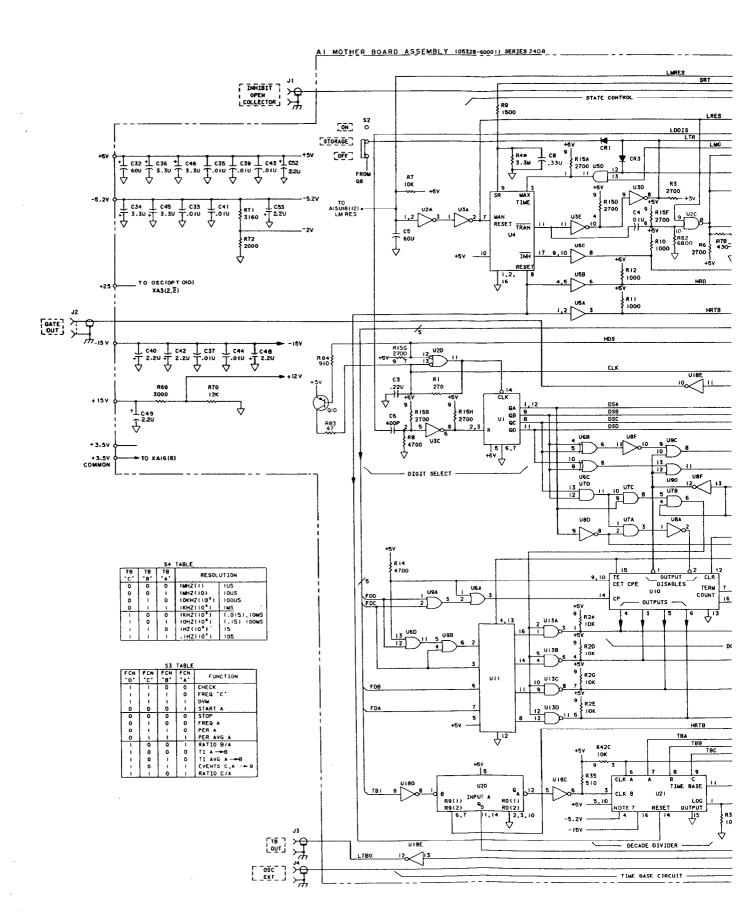
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A1R46 A1R47 A1R48 A1R49-	0683-1545 0683-1035	8	1	RESISTOR 150K 5% .25W FC TC=-800/+900 NOT ASSIGNED RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121	CB1545
A1R51 A1R52	0683-5635	5	1	NOT ASSIGNED RESISTOR S6K 5% .25W FC TC=-400/+800 NOT ASSIGNED	01121	CB5635
A1R53 A1R54 A1R55 A1R56	0683-1025 0683-4725	9 2		NOT ASSIGNED RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 4.7K 5% .25W FC TC=-400/+700	01121 01121	CB1025 CB4725
A1R57 A1R58 A1R59 A1R60 A1R61	0683-6815 0683-1025 0683-5605 0683-5605 0683-5605	5 9 9 9	10	RESISTOR 680 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 56 5% .25W FC TC=-400/+500 RESISTOR 56 5% .25W FC TC=-400/+500 RESISTOR 56 5% .25W FC TC=-400/+500	01121 01121 01121 01121 01121	CB6815 CB1025 CB5605 CB5605 CB5605
A1R62 A1R63 A1R64 A1R65 A1R66	0683-5605 0683-5605 0683-5605 0683-5605 0683-5605	9999		RESISTOR 56 5% .25W FC TC=-400/+500	01121 01121 01121 01121 01121	CB5605 CB5605 CB5605 CB5605 CB5605
A1R67 A1R68 A1R69 A1R70 A1R71	0683-5605 0683-1035 0757-0935 0757-0950 0757-0279	9 1 5 4 0	1 1	RESISTOR 56 5% .25W FC TC=-400/+500 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 3K 2% .125W F TC=0+-100 RESISTOR 12K 2% .125W F TC=0+-100 RESISTOR 3.16K 1% .125W F TC=0+-100	01121 01121 24546 24546 24546	CB5605 CB1035 C4-1/8-T0-3001-G C4-1/8-T0-1202-G C4-1/8-T0-3161-F
A1R72 A1R73 A1R74 A1R75 A1R76	0757-0931 0683-1035 0683-1035 0683-1035 0683-1015	1 1 1 7	1	RESISTOR 2K 2% .125W F TC=0+-100 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 100 5% .25W FC TC=-400/+500	24546 01121 01121 01121 01121	C4-1/8-T0-2001-G CB1035 CB1035 CB1035 CB1035 CB1015
A1R77 A1R78 A1R79 A1R80 A1R81	0683-5605 0683-4315 0683-1025 0683-1035 0683-1035	9 6 9 1 1	1	RESISTOR 56 5% .25W FC TC=-400/+500 RESISTOR 430 5% .25W FC TC=-400/+600 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 10K 5% .25W FC TC=-400/+700 RESISTOR 10K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB5605 CB4315 CB1025 CB1035 CB1035
A1R82 A1R83 A1R84	0683-6825 0698-4037 0757-0422	7 0 5	1	RESISTOR 6.8K 5% .25W FC TC=-400/+700 RESISTOR 46.4 1% .125W F TC=0+-100 RESISTOR 909 1% .125W F TC=0+-100	01121 24546 24546	CB6825 C4-1/8-T0-46R4-F C4-1/8-T0-909R-F
A1S1 A1S2 A1S3	3101-1977 3101-1977 3101-1977	1 1 1	3	SWITCH-SL DPDT SUBMIN .5A 125VAC PC SWITCH-SL DPDT SUBMIN .5A 125VAC PC SWITCH-SL DPDT SUBMIN .5A 125VAC PC	28480 28480 28480	3101-1977 3101-1977 3101-1977
A1U1 A1U2 A1U3 A1U4 A1U5	1820-0055 1820-1056 1820-0175 1820-1401 1820-0513	6 9 1 8 1	2 1 1 1 2	IC CNTR TIL DECD SYNCHRO POS-EDGE-TRIG IC SCHMIIT-TRIG TIL NAND QUAD 2-INP IC INV TIL HEX 1-INP IC MISC IC GATE TIL AND QUAD 2-INP	01295 01295 01295 28480 01295	SN7490AN SN74132N SN7405N 1820-1401 SN7409N
A1U6 A1U7 A1U8 A1U9 A1U10	1820-0282 1820-0511 1820-0174 1820-0661 1820-1143	1 9 0 0 5	6 2	IC GATE ITL EXCL-OR QUAD 2-INP IC GATE ITL AND QUAD 2-INP IC INV TTL HEX IC GATE ITL OR QUAD 2-INP IC CNTR TTL DECD SYNCHRO	01295 01295 01295 01295 01295 27014	SN7486N SN7408N SN7404N SN7432N DM8552N
A1U11 A1U12 A1U13 A1U14 A1U15	1820-0301 1820-2316 1820-0269 1820-0513 1820-0803	5 6 4 1 2	2	IC LCH TTL D-TYPE 4-BIT IC CNTR IIL DECD HEX IC GATE TTL NAND QUAD 2-INP IC GATE TTL AND QUAD 2-INP IC GATE ECL OR-NOR TPL	01295 28480 01295 01295 04713	SN7475N IDB2-4 SN7403N SN7409N MC10105P
A1U16 A1U17 A1U18 A1U19 A1U20	1820-0537 1820-0068 1820-0174 1820-0077 1820-0055	9 1 0 2 6	1	IC SCHMITT-TRIG TIL NAND DUAL 4-INP IC GATE TIL NAND TPL 3-INP IC INV TIL HEX IC FF TIL D-TYPE POS-EDGE-TRIG CLEAR IC CNIR TIL DECD SYNCHRO POS-EDGE-TRIG	01295 01295 01295 01295 01295	SN7413N SN7410N SN7404N SN7474N SN7490AN
A1U21 A1U22 A1U23 A1U24 A1U25	1820-2317 1820-0269 1820-0328 1820-0282 1820-0301	7 4 6 1 5	1	IC MISC IIL IC GATE ITL NAND QUAD 2-INP IC GATE ITL NOR QUAD 2-INP IC GATE ITL EXCL-OR QUAD 2-INP IC ECH ITL D-1YPE 4-BIT	28490 01295 01295 01295 01295	IDB9-TL SN7403N SN7402N SN748EN SN747SN

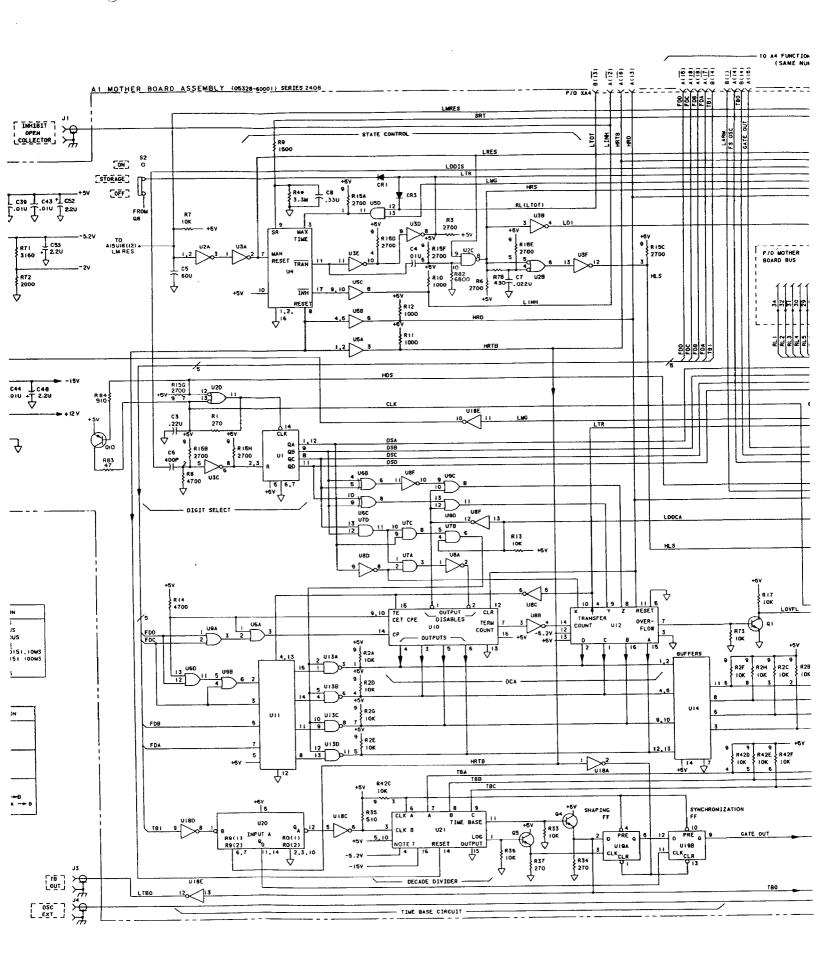
See introduction to this section for ordering information *Indicates factory selected value

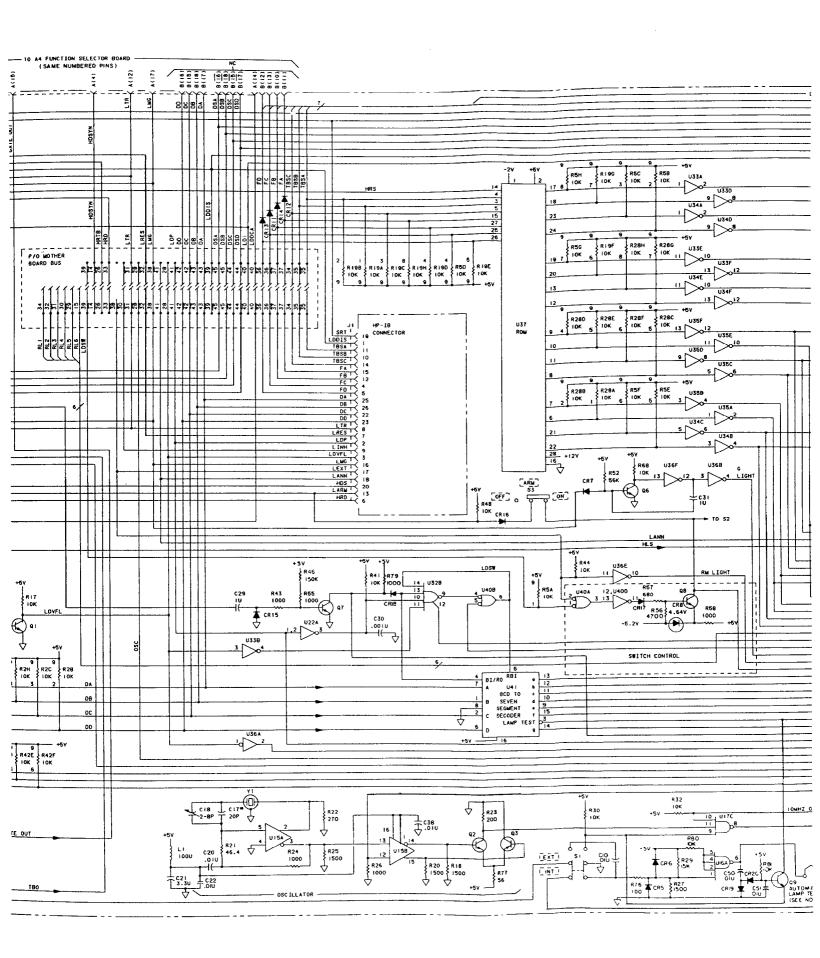
Table 7-2. Replaceable Parts (continued)

	Table 7-2. Replaceable Faits (continued)					
Reference Designation	HP Part Number	CD	Qty	Description	Mfr Code	Mfr Part Number
A1U26 A1U27 A1U28 A1U29 A1U30	1820-0301 1820-0301 1820-0538 1820-0282 1820-0282	5 5 0 1	2	IC LCH TTL D-TYPE 4-BIT IC LCH TTL D-TYPE 4-BIT IC GATE TTL NOR DUAL 4-INP IC GATE TTL EXCL-OR QUAD 2-INP IC GATE TTL EXCL-OR QUAD 2-INP	01295 01295 01295 01295 01295	SN7475N SN7475N SN7423N SN7486N SN7486N
A1U31 A1U32 A1U33 A1U34 A1U35	1820-0301 1820-0538 1820-0174 1820-0174 1820-0174	5 0 0 0 0		IC LCH TTL D-TYPE 4-BIT IC GATE TTL NOR DUAL 4-INP IC INV TTL HEX IC INV TTL HEX IC INV TTL HEX	01295 01295 01295 01295 01295	SN7475N SN7423N SN7404N SN7404N SN7404N
A1U36 A1U37 A1U38 A1U39 A1U40	1820-0174 1820-0661 1820-0214 1820-0054	0 0 9 5	1	IC INV TTL HEX NOT ASSIGNED IC GATE TTL OR QUAD 2-INP IC DCDR TTL BCD-TO-DEC 4-TO-10-LINE IC GATE TTL NAND QUAD 2-INP	01295 01295 01295 01295	SN7404N SN7432N SN7442AN SN7400N
A1U41	1820-1644	1	1	IC DCDR TTL LS BCD-TO-7-SEG 4-TO-7-LINE	01295	SN74LS248N
A1XA1 A1XA2 A1XA3 A1XA4A A1XA4B	1251-2035 1251-2026 1251-2026	9 8 8	4 9	NOT ASSIGNED NOT ASSIGNED CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS	28480 28480 28480	1251-2035 1251-2026 1251-2026
A1 XA5 A1 XA5B A1 XA6 A1 XA7 A1 XA8A A1 XA8B	1251-2035 1251-2026 1251-2035 1251-2035 1251-2026 1251-2026	989988		CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS	28480 28480 28480 28480 28480 28480	1251-2035 1251-2026 1251-2035 1251-2035 1251-2026 1251-2026
A1XA9 A1XA10 A1XA11 A1XA12 A1XA13	1251-2026 1251-2026 1251-2026	8 8		NOT ASSIGNED CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS NOT ASSIGNED	28480 28480 28480	1251-2026 1251-2026 1251-2026
A1XA14	1251-2026	8		CONNECTOR-PC EDGE 18-CONT/ROW 2-ROWS	28480	1251 - 2026
A1XJ1	1200-0567	1	2	SOCKET-IC 28-CONT DIP DIP-SLDR	28480	1200-0567
A1XU1 - A1XU3 A1XU4 A1XU5 - A1XU11	1200-0639	8	,	NOT ASSIGNED SOCKET-IC 20-CONT DIP DIP-SLDR NOT ASSIGNED	28480	1200-0639
A1XU12	1200-0473	8	2	SOCKET-IC 16-CONT DIP DIP-SLDR	28480	1200-0473
A1XU13- A1XU20 A1XU21 A1XU22-	1200-0473	8		NOT ASSIGNED SOCKET-IC 16-CONT DIP DIP-SLDR	28480	1200-0473
A1XU36				NOT ASSIGNED		1200 0563
A1XU37	1200-0567	'		SOCKET IC 28-CONT DIP DIP-SLOR	28480 28480	1200-0567
A1XY1 A1Y1	1200-0153 0410-0405	0		SOCKET-XSTR 3-CONT TO-5 DIP-SLDR CRYSTAL-QUARTZ 10.000 MHZ	28480	0410-0405









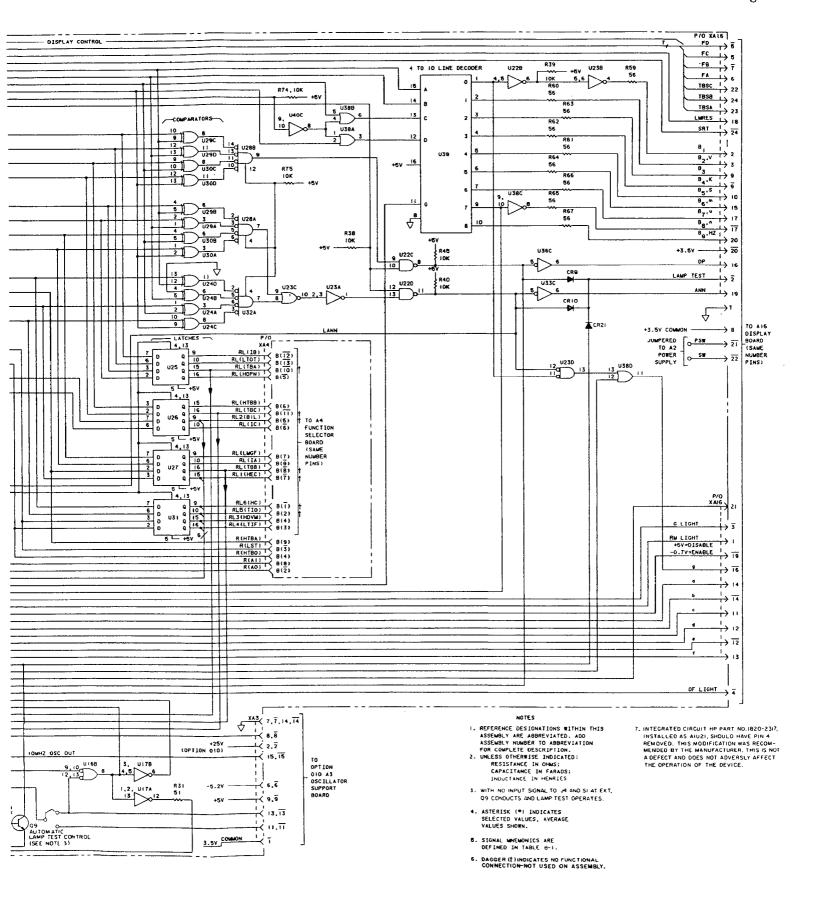


Figure 7-1. A1 Motherboard Schematic Diagram

SERVICE

8-1. INTRODUCTION

This manual section contains:

- service information
- circuit diagrams
- principles of operation

8-2. PAGE AND REFERENCE NUMBERING

This is "Section 8" of the "5328B Operating and Service Manual".

All page numbers, (sub)section numbers, "Figure" numbers, and "Table" numbers in this section begin "8-".

In the first "general" part of this section, the reference numbers have only two parts:

- The section identifier, "8-", and ...
- A suffix number, assigned serially for each category of reference (tables, figures, etc.).

In the portions of this section dealing with specific areas of the Model 5328B, the reference numbers have three parts:

- The section identifier, "8-", and ...
- The sub-section identifier "A-", "B-", "C-", or "D-" and ...
- A suffix number, assigned serially for each catagory of the sub-section reference.

Sub-section A covers the Main Counter and Function Selector. Sub-section "B" covers the Input Options. sub-section "B" is further divided to describe each option and is labeled with a second letter of "A", "B", or "C" following the sub-section designator "B".

```
Sub-section "8-BA-" describes the Programmable Input Module Sub-section "8-BB-" describes the Digital Voltmeter Module Sub-section "8-BC-" describes the Channel C Module
```

Sub-section "C" describes the Hewlett Packard Interface Bus (HP-IB) board.

Sub-section "D" describes the Power Supply board.

This approach parallels the organization of Model 5328B. For example, material related to the HP-IB area of the Model 5328B is referenced by numbers beginning "8-C-".

8-3, SAFETY CONSIDERATIONS

This section contains warnings and cautions that must be followed for your protection and to avoid damage to the equipment.

WARNING

MAINTENANCE DESCRIBED HEREIN IS PERFORMED WITH POWER SUPPLIED TO THE MODEL 5328B, AND PROTECTIVE COVERS REMOVED. SUCH MAINTENANCE SHOULD BE PERFORMED ONLY BY SERVICE-TRAINED PERSONNEL WHO ARE AWARE OF THE HAZARDS INVOLVED (FOR EXAMPLE, FIRE AND ELECTRICAL SHOCK). WHERE MAINTENANCE CAN BE PERFORMED WITHOUT POWER APPLIED, THE POWER SHOULD BE REMOVED.

Some of the procedures in this manual section require removal of one or more of the Model 5328B's covers. Removal of a cover may allow access to a potentially lethal voltage. Be sure to use adequate protective measures to prevent personal injury.

The Model 5328B has a chassis ground which is connected to earth ground through a ground wire in the power cable, when the unit's main power cable is connected to a suitably-wired outlet. You should not attempt to defeat this grounding arrangement. If you suspect that a satisfactory ground cannot be obtained because of a fault in the power cable, replace the power cable.

8-4. CIRCUIT DIAGRAMS

A listing of the circuit diagrams (and related Parts List tables) is provided in Table 8-1.

Each circuit diagram contains:

- a table that shows all reference designations used for the assembly
- a table of active elements (diodes, transistors, and integrated circuits), including part numbers.

The schematic diagrams contain "to/from" information for inputs and outputs. This information is supplemented by an overall interconnection diagram of the Motherboard (A1) and a block diagram of each assembly.

8-5. SCHEMATIC DIAGRAM SYMBOLS

A listing of some of the symbols used in the schematic diagrams in this manual is provided in of Figure 8-1.

8-6. REFERENCE DESIGNATIONS

Assemblies such as printed circuit boards are assigned reference designation numbers in sequence — "A1", "A2", etc.

As shown in Figure 8-2, subassemblies within an assembly are given a subordinate "A" reference designation. For example, rectifier subassembly "A1" has the complete designation "A25A1".

For an individual component, the complete designator is created by adding the reference designators of the assembly and subassembly (if any) of which it is a part; for example the complete reference designation for diode "CR1" on the rectifier subassembly (see Figure 8-D-2) ("A25A1") is "A25A1CR1".

Table 8-1. Circuit Diagrams

Assy Ref Des	Description	HP Part No.	Circuit Diagram	Parts List Table
A1	Motherboard Assembly	05328-60001	8-A-10	6-2
A2	Power Supply Assembly	05328-60034	8-D-2	6-3
A3	Support Board Assembly	05328-60018	8-A-15	6-4
A3A1	Quartz Oscillator — 10 MHz	10811-60111		6-4
A4	Function Selector Assembly	05328-60005	8-A-13	6-5
A5	PS Autorange Assembly (See NOTE 1)	05328-60012	8-BB-2	6-6
A6	Buffer Amplifier Assembly (See NOTE 1)	05328-60013	8-BB-3	6-6
A 7	VF Converter Assembly (See NOTE 1)	05328-60014	8-BB-4	6-6
A8	1300 MHz C Channel Assembly (See NOTE 2)	05328-60025	8-BC-3	6-7
A8A1	1.3 GHz Board Assembly (See NOTE 2)	05305-60006	8-BC-2	6-7
A9	Not Assigned			
A10	Synchronizer Assembly	05328-60020	8-BA-9	6-8
A11	DAC Assembly	05328-60023	8-BA-11	6-8
A12	Amplifier Assembly	05328-60041	8-BA-8	6-8
A13	Not Assigned			
A14	Multiplier Assembly	05328-60010	8-BA-10	6-8
A15	HP-IB Interface Assembly	05328-60043	8-C-2	6-9
A16	Display Assembly	05328-60033	8-A-14	6-10
A17	DVM Front Board Assembly (See NOTE 1)	05328-60015		6-6
A18	Not Assigned			
A19	Switch Control Assembly	05328-60021	8-BA-7	6-8

NOTES —

- 1. Part of (optional) Digital Voltmeter (Option 021 or part of Option 050)
- 2. Part of (optional) 90-1300 MHz "C" Channel (Option 031 or part of Option 050)

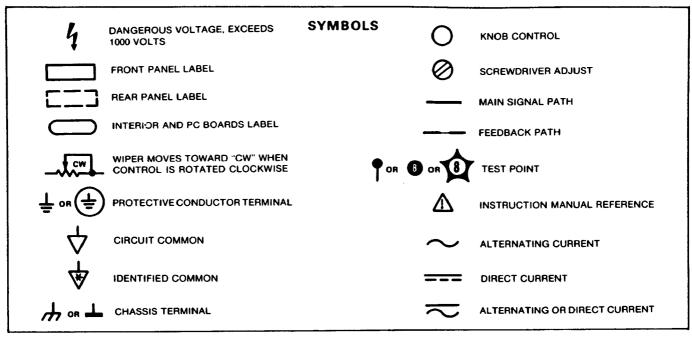


Figure 8-1. Schematic Diagram Symbols

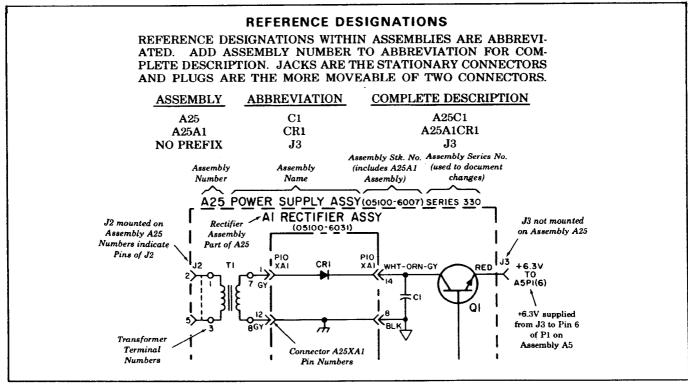


Figure 8-2. Reference Designations

8-7. SIGNAL NAMES

In general, the signal names used in the Model 5328B provide a very good indication of what that particular signal does. A listing of most of the Model 5328B's signal names is provided in Table 8-2.

Table 8-2. Signal Names and Descriptions

MNEMONIC	DESCRIPTION
	Output of Time Interval Unit, A Channel. ECL levels.
A A	
R(AØ) R(A1)	Nonlatched ROM bits that drive Arming Multiplexer select lines on Function Selector. TTL levels.
<u>B</u> B	Output of Time Interval Unit, B Channel ECL levels.
<u>C</u>	Output of C module, the carry input for the FS decade. ECL levels.
C ARM	Active high TTL line used for module C arming measurement.
CLK	Clock. Digital Address Clock to display. TTL levels.
Data A Data B Data C Data D	TTL 4-bit BCD code. Data going to display and HP-IB.
Digit A Digit B Digit C Digit D	TTL 4-bit digit address code. Controls interchange of data.
DVM	Frequency line counted by Function Selector to give display reading. ECL level.
F Code A (FA) F Code B (FB) F Code C (FC) F Code D (FD)	Function code from FUNCTION switch. TTL levels.
FS	Function Selector.
GOSC GOSC	Gated oscillator. ECL levels.
HDS	TTL level high disables sychronizers.
HDSA	Used by Option 011 HP-IB Interface to strobe bus data in remote listener.
HLS	TTL level line used to strobe latches.
RL (HOPN)	Latched ROM line which locks open Function Selector main gate.
HPL	Same as LDP.
HRD	High resets decades. TTL active high.
HRS	High strobes 4K ROM. TTL active high.
HRTB	High resets time base. TTL active high. Also resets Function Selector.
R (HTBA)	Nonlatched ROM bit which enables the TTL level Channel A signal from the Function Selector to be counted by the Time Base.
RL (HTBB)	Latch ROM bit which enables the TTL level Channel B signal from the Function Selector to be counted by the Time Base.
R (HTBO)	Nonlatched ROM bit which enables the time base to count the oscillator output.
RL (IA) RL (IB) RL (IC)	TTL level latched ROM bits that drive High Speed Multiplexer select lines on Function Selector.
LL ANN	Low annunciators. TTL active low turns RHS annunciators on. Must be timed with digit address code to display selected annunciators.
LDDCA	Low disable Decade Counting Assembly (DCA). TTL active low disables DCA so that all DCA outputs are high.
LDI	Low disable indicators. TTL active low blanks RHS annunciators and all decimal points.
LDDIS	Low display display. TTL active low blanks display except LHS annunciators.

Table 8-2. Signal Names and Descriptions (continued)

	Die 6-2. Signai Names and Descriptions (continued)
MNEMONIC	DESCRIPTION
LDP	Low decimal point. TTL active low turns decimal points on. Must be time with digit address code to display selected decimal point.
LDSW	Low disable switches. The active low disables the FUNCTION, RESOLUTION, and RESET switches. Allows module control.
LEXT	Low external. TTL active low disables FUNCTION and RESOLUTION switches for external control and lights RMT annunciator.
LINH	Low inhibit. TTL active low inhibits starting new measurement.
LMG	Low main gate. TTL active low indicates main gate open.
RL (LMGF)	Latched ROM bit to Function Selector which selects the main gate F/F on the Function Selector to establish the gate time.
LMRES	Low when RESET signal comes from display. Provides power-up type reset.
LRES	Low reset. TTL active low resets when FUNCTION, RESOLUTION, or RESET switch settings are changed. Also resets when DVM switches are changed. Provides power-up type of reset.
R (LST)	Nonlatched ROM line which is HIGH in stop totalize and LOW is start.
RL (LTOT)	Low totalize. Latched ROM bit low in TOTALIZE mode. TTL level.
LTR	Low transfer. TTL active low used in DCA.
MG MG	Main gate. Accurate signal to drive remote gate such as Channel C. ECL levels.
OSC	10 MHz oscillator. TTL level.
OSC OSC	10 MHz oscillator. ECL levels.
OVFL	Overflow. TTL active low indicates display overflow.
RG	ROM bit. Used to recognize period and institute hysteresis compensation. TTL level.
RL1 (HEC)	Latched ROM bit. TTL level enables Channel C to strobe its digit onto the bus.
RL2 (BIL)	Latched ROM bit. High for time interval average. TTL level.
RL3 (HDVM)	Latched ROM bit. Enables DVM to strobe a minus sign on the display on blank characters. TTL level.
RL4 (LTIF)	Latched ROM bit. TTL level low in Time Interval or Period measurement.
RL5 (TIO)	Latched ROM bit. TTL level used to recognize period average.
RL6 (HC)	Latched ROM bit which turns hysteresis compensation on and has a Time Interval as opposed to a Period measurement made by the Time Interval unit. TTL level.
SRT	The charge node line that controls the sample rate speed.
RL (TBA) RL (TBB) RL (TBC)	Latched ROM bits that drive Time Base Select code inputs.
TBI.	TTL signal that drives Time Base.
ТВО	Time Base scaled output. TTL levels.
TBS Code A (TBSA) TBS Code B (TBSB) TBS Code C (TBSC)	Time Base Code input to ROM controlled by the Time Base switch. TTL levels.
Ti Ti	Time interval. Output of Time Interval modulse used in time interval measurements. ECL levels.

Table 8-2. Signal Names and Descriptions (continued)

	QUALIFIERS				
SIGNAL	DESCRIPTION				
ADDR ANN ATN	H= My Listen Address L = Annunciator On L = Attention				
BØ B1 B2 B3 BLK	16 State Sequence Count for Output of ASCII Code 16 State Sequence Count for Output of ASCII Code 16 State Sequence Count for Output of ASCII Code 16 State Sequence Count for Output of ASCII Code 16 State Sequence Count for Output of ASCII Code				
D DAC DAV DIO1 DIO2 DIO3 DIO4 DIO5 DIO6 DIO7	H = Decimal Point has been Outputted H = Data Accepted L = Data is Valid HP-IB Data Bit 1 HP-IB Data Bit 2 HP-IB Data Bit 3 HP-IB Data Bit 4 HP-IB Data Bit 5 HP-IB Data Bit 5 HP-IB Data Bit 6 HP-IB Data Bit 7				
EOM	H = End of Measurement				
J	Alwasys HIGH, used for unconditional jump				
LDP LIS LLO	L = Decimal Point On H = Address to Listen H = Local Lockout On				
MA MB MLT MS	 L = Enable Strobe to Function Select Latch U34 and Select Bit on Module Strobe Code L = Enable Strobe to Time Base Select Latch U28 and Select Bit on Module Strobe Code H = Make Multiple Measurements H = Module Strobe L = FC & TB Strobe 				
OVFL ODV	L = Overflow L = Wait until Addressed				
RDF REN RFD	H = Read Data on the fly L = Remote Enabled H = Ready for Data				
S SP SRQ SWL	H = Measurement has dimension of time H = Serial Pole Active L = Service Request H = Switch to Local				
TALK A TLK	L = Talk Always H = Address to Talk				

	Table 8-2. Signal Names and Descriptions (continued)					
	OUTPUTS					
SIGNAL	DESCRIPTION					
LLIS	Unlisten					
HLIS	Listen					
LTLK	Untalk					
HTLK	Talk					
LSP	Serial Poll Disable					
HSP	Serial Poll Enable					
LMA	Enable Function Code Latch Input, Module Select Code. Also					
1	used in putting out Exponent					
НМА	Opposite of LMA					
LS	Measurement does not have dimension of time. Output POS EXP					
HS	Opposite of LS					
НІТСН	Latch Data into U28, U33, or U34					
1	Go to Local					
LRMT	Go to Remote					
HRMT						
LD	Decimal Point has not been outputted					
HD	Decimal Point has been outputted					
LMB	Enable Time Base Code Latch Input, Module Select Code. Also					
	used in putting out Exponent					
HMB	Opposite of LMB					
LMS	Enable Function and Time Base Code Latches, Disable Module					
	Strobe Line					
HMS	Opposite of LMS					
LDAV	5328A says Data Not Valid					
HDAV	5328A says Data Valid					
LRFD	5328A says Not Ready for Data					
HRFD	5328A says Ready of Data					
LDAC	5328A says Data not Accepted					
HDAC	5328A says Data Accepted					
ιιο	Local Lockout Off					
HLLO	Local Lockout On					
LEOM	Reset End of Measurement F/F (U11B)					
HIC	Initialize 16 State Counter					
HDSA	Strobe Mainframe Display and 16 State Counter					
LRPR	Turn OFF Master Remote Programming Reset					
HRPR	Turn ON Master Remote Programming Reset					
LDDIS	Low Disable Display. TTL active low turns blanks display except					
	LHS Annunciators					
HDDIS	Opposite of LDDIS					
LINH	Inhibit Counter from Arming					
LRST	Turn OFF Counter Mainframe Reset					
HRST	Turn ON Counter Mainframe Reset					
LSRQ	Output (on U15, U24) Binary Ø on ASCII Bus					
HSRQ	Output (on U15, U24) Binary 64 on ASCII Bus					
ASP	Output (on U15, U24) ASCII space					
LDAO	Output (on U15, U24) all HIGHS on Bus and Disarm DAC Line					
HDAO	Output (on U15, U24) all HIGHS on Bus and Arm DAC Line					
	All succeeding bits put out on U15, U24 to be put on					
	HP-IB as ASCII Characters					
ADIG	ASCII Digit from Display					
ALF	ASCII Line Feed					
AØ	ASCII Ø					
ACR	ASCII Carriage Return					
AE	ASCII E					
ADP	ASCII Decimal Point					
A3	ASCII 3					
A6	ASCII 6					
A9	ASCII 9					
A+	ASCII +					
A-	ASCII -					
AOVF	ASCII Letter O					

8-8. IDENTIFICATION MARKINGS ON PRINTED CIRCUIT BOARDS

HP printed circuit boards have four identification numbers (see Figure 8-3):

- the Hewlett-Packard "Part Number"
- the "SERIES" number
- · a revision letter
- a production code

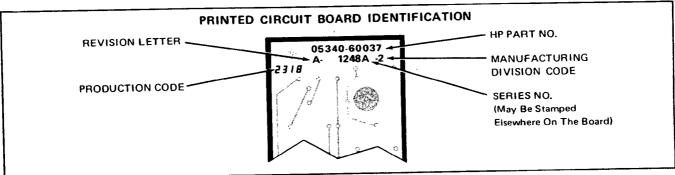


Figure 8-3. Identification Markings on Printed Circuit Boards

8-9. Hewlett-Packard Part Number

The Hewlett-Packard Part Number on a 5328B printed circuit board has ten digits (example: "05328-60018"), and is the primary identification. In general, all assemblies having the same part number are interchangeable. When a production change is made to an assembly that makes it incompatible with earlier versions, the HP Part Number is changed.

8-10. SERIES Number

The SERIES number (example: "SERIES 2408") is used to document minor changes to a part. As changes are made, the SERIES number is changed. When a replacement board is ordered, it may have a SERIES number that is different (typically higher) than that of the board it replaces. If there is a difference between the SERIES number marked on the board and the SERIES number indicated on the circuit diagram for that board, a minor difference exists. If the SERIES number on the board is lower than that on the schematic, refer to Section 7 of this manual for backdating information. If the SERIES number on the board is higher than that on the schematic in this manual, refer to the "Manual Changes" supplement that accompanies the manual for information. If the "Manual Changes" supplement is missing, contact your local Hewlett-Packard Sales and Service Office (see listing at the back of this manual).

8-11. Revision Letter

A "revision letter" ("A", "B", etc.) denotes a change in the layout of a printed circuit. For example, if a capacitor's type is changed (its electrical value can stay the same), and requires different spacing for its leads, the printed circuit board's layout is changed; the board's revision letter is changed to indicate this change. When a revision letter changes, the board's SERIES number is also usually changed.

8-12. Production Code

The production code is the four-digit seven-segment number used for production purposes.

8-13. Special Symbols

A triangle (" \triangle ") or square (" \square ") may be used on a printed circuit board to help in identifying such important things as —

- pin "1" of a DIP or flat-pack IC
- pin "1" of a resistor pack
- the tab of a "TOx" case
- the "+" side of an electrolytic capacitor
- · the cathode end of a diode
- section "1" of a DIP switch

8-14. ASSEMBLY LOCATIONS AND COMPONENT LOCATORS

Figure 8-4, Figure 8-5, and Figure 8-6 show the front, rear, and top views of the Model 5328B, respectively. The front (Figure 8-4) and rear (Figure 8-5) views show reference designators of the front and rear panel controls, connectors, and indicators.

The top view (Figure 8-6) shows assembly locations.

The component locator for each printed circuit assembly is shown next to the schematic diagram for that assembly.

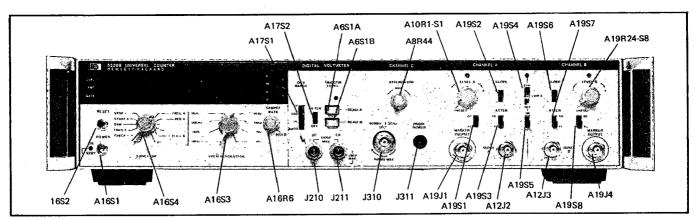


Figure 8-4. Model 5328B Front View

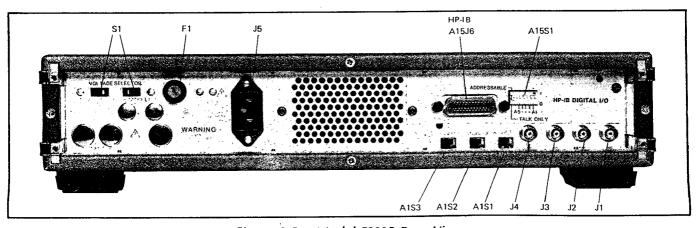


Figure 8-5. Model 5328B Rear View

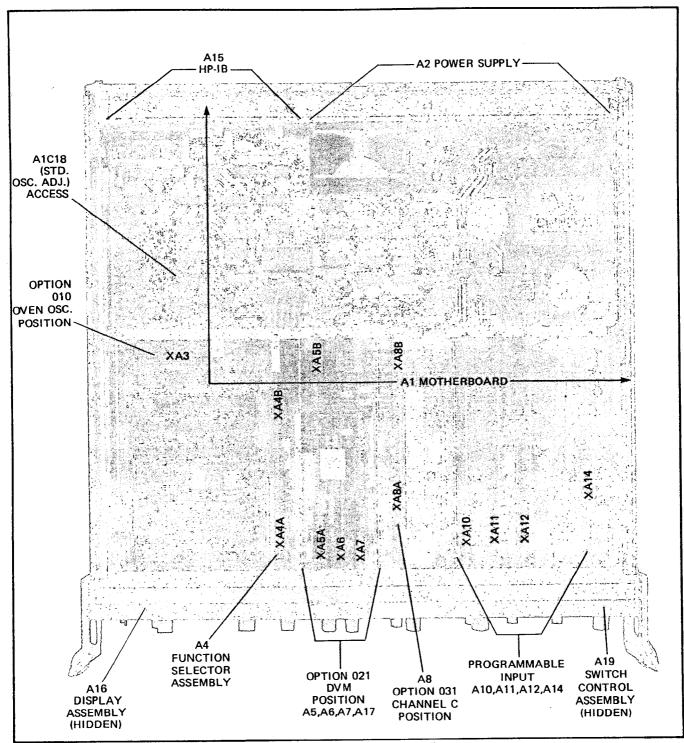


Figure 8-6. Model 5328B Top Internal View

8-15. FACTORY-SELECTED COMPONENTS

Factory selected parts are identified by an asterisk ("*") on the schematic and in the parts list. The nominal value is shown in the schematic and listed in the table of replaceable parts. A table on the schematic lists factory-selected parts by reference designator. The normal range of values are shown and the factors affecting part selection are described.

8-16. OVERALL DESCRIPTION

The Model 5328B is a basic universal counter mainframe that supports several options. As shown in the Overall Block Diagram (Figure 8-7), the counter is organized into several relatively independent sections that communicate via an internal 90-line bus system. Detailed descriptions of the counter's various sections are provided.

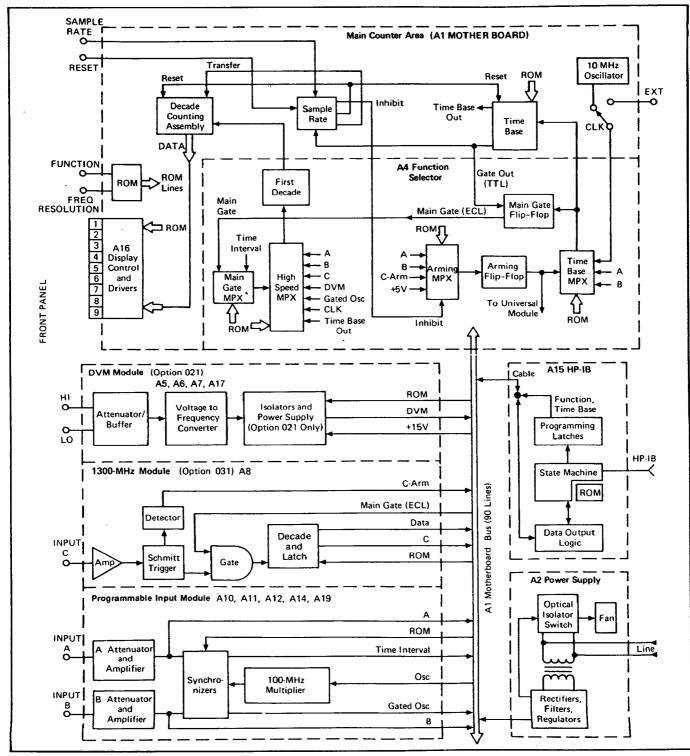


Figure 8-7. Overall Block Diagram

8-17. TROUBLESHOOTING OVERVIEW

The 5328B counter will be segmented into 6 different sections for the purpose of troubleshooting. See Figure 8-8.

Each of these sections performs an independent function and can be viewed as a potential problem center in a troubleshooting sense. These sections are listed below:

- Power Supply Section
- Main Counter Section
- Input Section
- HP-IB Section
- DVM Section (Option 021)
- Channel C Section (Option 031)

This sectioning closely parallels the division of the counter into operating sections as described in the overall description. The first four sections comprise the main counter and the other two sections are for extended capabilities. The HP-IB board allows remote operation of the 5328B through controllers. Option 021 allows the counter to make DC measurements of external voltages and internal counter trigger levels for more accurate time interval measurements. Option 031 adds a C Channel to the 5328B extending the frequency range of the counter to 1300 MHz.

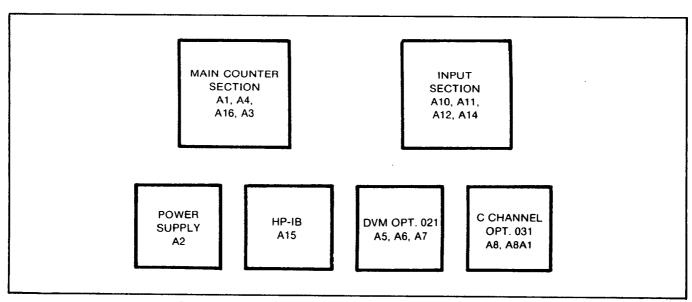


Figure 8-8. Independent Sections For Troubleshooting

Troubleshooting is approached in a hierarchical fashion. The troubleshooting procedure:

- 1. Identifies a faulty section.
- 2. Identifies a faulty board within that section.
- 3. Identifies the faulty component(s) on that board.

8-18. Troubleshooting Methodology

There are two methods to approach the troubleshooting of the counter:

- 1. The branching approach
- 2. The sequential approach

8-19. Branch Mode troubleshooting

In the Branching mode, identify the faulty section(s) by observing the failure symptoms and relating them to the function of each section as shown in Figure 8-9. Table 8-3, Symptoms-to-Section Troubleshooting lists some of the more common failure symptoms and the sections that they are probably related to. This is not an exhaustive list and could be greatly supplemented by experience in troubleshooting the 5328B counter. The Branching approach towards troubleshooting is not foolproof but can greatly reduce troubleshooting time by localizing the problem without sequentially going through all the sections of the counter. Upon identifying the faulty section, the next step is to identify the faulty board within that section and then troubleshooting the board to component level.

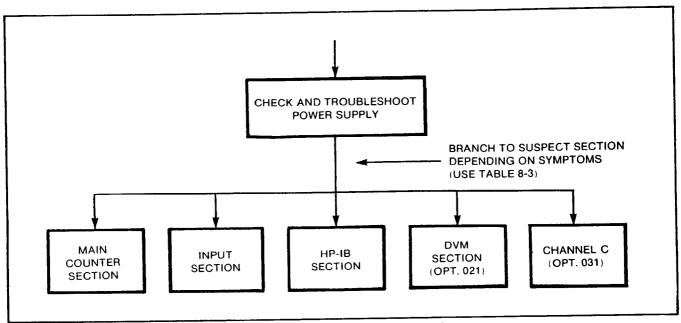


Figure 8-9. Branching Mode Troubleshooting

8-20. Sequential Mode Troubleshooting

In the Sequential troubleshooting mode, move through the sections of the counter shown in Figure 8-10. The sequence in Figure 8-10 is based upon a history of past failures and ease of troubleshooting each section. Depending upon the failure symptoms and prior troubleshooting experience, this sequence may be changed. The sequential method is more time consuming than branching but is more reliable in discovering a fault.

In both these troubleshooting approaches it is advisable to first check the power supply voltages before proceeding as these voltages are critical to the proper operation of every one of the sections. Also, power supply voltage variations or failures are the most common cause of failures in the 5328B.

Using either of the methods described, branching or sequential, the next step is to troubleshoot each faulty section. This will be discussed in the next few paragraphs.

8-21. Special Extender Board (HP Part Number 05328-62016)

A special extender board is available to make testing of certain parts of the Model 5328B easier.

Two of these boards are required to extend the A4 Function Selector.

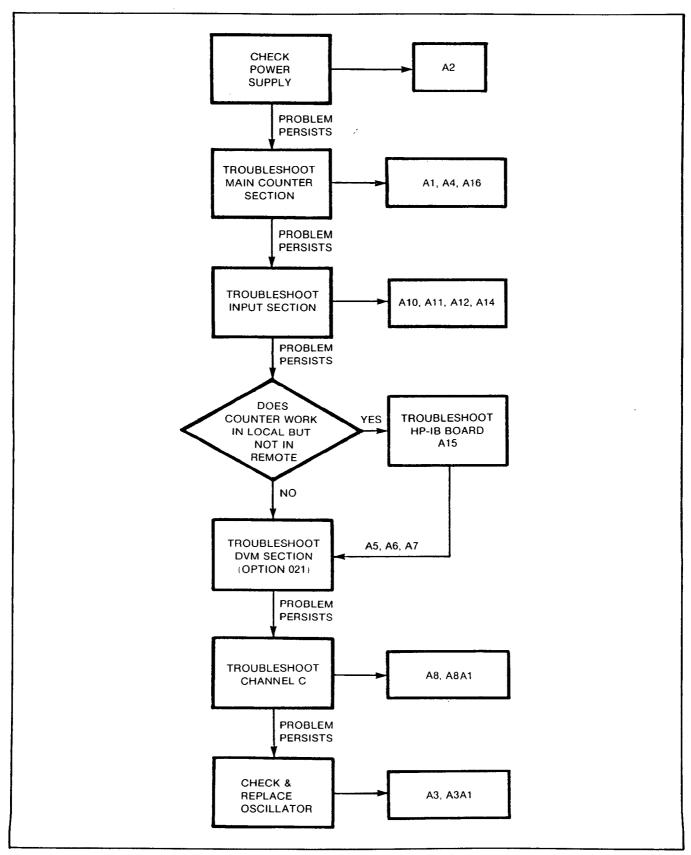


Figure 8-10. Sequential Mode Troubleshooting

8-22. Troubleshooting Kit 05328-60131

The troubleshooting kit consists of test cards for troubleshooting the Main Counter Section and an HP-IB test kit for troubleshooting the HP-IB board.

The kit is recommended to reduce troubleshooting time for the HP5328B.

The troubleshooting kit consists of the following parts:

Test Cards:

Card 1: 05328-21004 Card 2: 05328-22004

HP-IB (A15) Board Troubleshooting:

05328-13306: Cassette containing the HP55005A Logic Troubleshooting

System and characterization data

05328-90106: HP-IB Troubleshooting Procedure

05328-13306: Modified 8120-2176 Ribbon Cables (2)

10230-62101: Grabber Clips (6) 1400-0734: 16 pin IC Test Clip (2)

05005-60116: Probe Ground (wire) used as a jumper

05328-90106: Troubleshooting Procedure for the troubleshooting kit

The following accessories are used with the test kit but are not included:

HP 5005B Signature Multimeter
HP85F Interfacing System containing:
HP85 Personal Computer
HP82936A ROM Drawer
HP82937A HP-IB Interface Module
00085-15003 I/O Module
HP82903A 16K Memory Module
00085-15005 Advanced Program ROM

Table 8-3. Symptoms to-Section Troubleshooting

	Symptom	Probable Faulty Section	Probable Faulty PC Board
1.	Display Blank even when reset. Power Supply OK.	Main Counter	A1/A16
2.	Strange Digits	Main Counter	A1
3.	No Decimal Points or All Decimal Points	Main Counter	A1/A4
4.	Decimal Points/2 more Decimal Points	Main Counter	A1/A4
5.	No Annunciators or wrong annunciators	Main Counter	A1/A4
6.	No measurement/No gate light	Main Counter	A1/A4
7.	No Reset	Main Counter	A1/A4
8.	No Storage but display counts	Main Counter	A1/A4
9.	Gate Time wrong	Main Counter	A1/A4

Table 8-3. Symptoms to-Section Troubleshooting (continued)

	Symptom	Probable Faulty Section	Probable Faulty PC Board
10.	Wrong Digits	Main Counter	A1/A4
11.	No Internal oscillations on bus	Main Counter	A1/A4
12.	Oscillator shows wrong frequency	Main Counter	A3/A4
13.	No oscillations on bus when in external	Main Counter	A1/A4
14.	No output when input is present	Input	A19
15.	Offset adjust won't work	Input	A19
16.	Counter doesn't work in A or B function	Input	A-10
17.	Counter works in Freq. A but not in TI or Period Modes	Input	A10
18.	Counter measures TI in PER or PER in TI Modes	Input	A10
19.	No period measurements for F>10 MHz	Input	A10
20.	Slope switch doesn't work	Input	A10
21.	Display blank on RESET	Main Counter	A16
22.	One digit blank	Main Counter	A16
23.	Segment blank across display	Main Counter	A16
24.	BUS annunciators blank	Main Counter	A16
25.	Trigger LEDs inoperative or markers inoperative	Input	A12/A19
26.	A or P channel trigger level inoperative	Input	A12
27.	Front panel input control switches inoperative	Input	A12
28.	TI, Freq., Patio, TI or PER Average incorrect, yet trigger LEDs operative	Input	A12/A10
29.	Doesn't count in Freq. C but Ok in CHECK	Channel C	A8
30.	Sensitivity too high at F>500 MHz	Channel C	A8
31.	Sensitivity too low at F>400 MHz	Channel C	A8
32.	Double counts on C at high input levels (usually at low freq.)	Channel C	A8
33.	No reading on display on DbM input	DbM	A5/A6/A7
34.	Unstable voltage reading	DbM	A6/A7
35.	Will not blank when in 100 ' or 1000 ' range	DbM	A5
36.	No minus sign/Always minus sign	DbM	A5
37.	Will not range up/down when in auto	DbM	A5

8-A-1. MOTHERBOARD ASSEMBLY (A1)

8-A-2. General

The Motherboard's two main circuit sections are the Model 5328B's Main Counter Section (shown at the top of the Overall Block diagram, Figure 8-7) and the multi-conductor bus that connects the counter's various functional areas. The bus is shown as a broad double-headed arrow at the lower right-hand corner of the diagram.

8-A-3. Main Counter Section

The Model 5328B's Main Counter Section, shown at the top of the Overall Block Diagram (Figure 8-7), is contained entirely on the Motherboard Assembly (A1). This section contains all of the functional sub-units of a standard counter, except for input signal conditioning and special logic, which are contained in the Input Options portion of section VIII.

Referring to the blocks within this area of the diagram; The Display Control and Drivers:

- decode the BCD (binary-coded-decimal) data that operates the numeric portion of the display, and
- control the lighting of the display's decimal points and the measurement units indicators.

The 4K ROM (Read-Only Memory) is the Model 5328B's master controller; and it is this circuitry that gives the counter's operating subsections the capability of accepting diverse data from various combinations of Input Option modules. The ROM accepts either;

- the four-bit Function Code and the three-bit Time Base Code from the front-panel switches or.
- instructions from the HP-IB Remote Programming Board.

The ROM generates 32 bits of output data which are transmitted throughout the Model 5328B to set up each subsection for the particular measurement situation selected. Various combinations of Input Option modules can be handled without changing any part of the circuit other than the Master Controller ROM.

The Decade Counting Assembly contains:

- eight decades of BCD (binary-coded-decimal) counting logic
- latches
- · output multiplexing logic

The Sample Rate Circuit controls the counter's display cycle. This circuit generates the signals "INHIBIT", "RESET", "MAIN GATE", "TRANSFER", "SAMPLE RATE", and the BCD digital address code for the strobed display.

The Time Base Assembly contains —

- eight counting decades
- output multiplexing logic
- synchronizers, to generate precise timing signals for the main gate.

The Oscillator Section contains —

- the standard room-temperature 10 MHz oscillator
- input/output logic to accept an external signal via the rear-panel or an internal signal from the (optional) crystal oven oscillator

Data from the Decade Counting Assembly or the Input Option modules is decoded and displayed on the nine-digit LED numeric display.

The Function Selector (A4) is the Model 5328B's main signal switch. It routes input signals through multiplexers to the Decade Counting Assembly and/or the Time Base. At the same time, the Function Selector interacts with the Display Control circuits to determine the beginning and end of the display cycle. The precision ECL Main Gate signal is created on the Function Selector through its interaction with the Time Base Assembly. The Function Selector also has extensive interaction with the Input Option modules; it is the main receiver of the high-speed data from the modules and the originator and receiver of module-arming pulses. The Function Selector Board is described in more detail later in this section.

8-A-4. Display Control Section

The Motherboard's Display Control Section is the interface between the Display Board (A16) and other circuits in the counter section.

The outputs of A16's FUNCTION and RESOLUTION switches go to the Motherboard ROM (A1U37). The outputs of the ROM:

- control lighting of the decimal point and measurement units indicators in the display, and—
- provide control functions for other circuits in the counter.

Data from the Data Bus is translated from BCD (Binary-Coded-Decimal) to seven-segment form in the Decoder (A1U41) and sent to the display, which is strobed by A1U39. Integrated Circuit A1U39 decodes the digital address code from BCD to one of ten forms. Leading-zero blanking is provided by the latch comprised of A1U32B and A1U40B. Latches A1U25, A1U26, A1U27, and A1U31 provide outputs related to function and time base codes for use in other sections of the instrument.

8-A-5. State Control Section

The State Control Section consists of integrated circuits A1U1 through A1U5. Decade Counter A1U1 generates the Digit Select Strobe Code for the display. Circuit A1U4 receives the Sample Rate signal and generates the main "RESET", "TRANSFER", and "INHIBIT" signals.

8-A-6. Oscillator

The Model 5238B's (standard) 10 MHz Oscillator circuit consists of —

- ECL crystal-controlled oscillator A1Y1 and A1U15,
- · buffer amplifier
- level-shifting circuitry
- internal/external mode control circuitry
- control circuitry for the Option 010 Oscillator

As shown in the schematic diagram (Figure 8-A-10), an ECL "OR/NOR" gate (A1U15A) is used as an amplifier with positive feedback. The positive feedback path is from the non-inverting output, through a 10 MHz crystal and parallel configuration of fixed capacitor (A1C17) and trimmer capacitor (A1C18), to one of the gate's inputs. Negative feedback from the inverting output to the same input of the gate establishes the input bias for the gate amplifier. The non-inverting output is buffered, translated to a TTL level, and routed to a control gate (A1U17C). This gate is controlled by the rear-panel DPDT "EXT/INT" switch, and passes the internal oscillator signal when this switch is in the INT position.

When the Option 010 Oscillator is installed, its output also appears at the input of this gate through J1. However, this Option disables the mainframe oscillator, and its output is therefore passed through the control gate when in the internal oscillator operation. The output of the control gate goes to one input of gate A1U16B. The other input to this gate is the output of the external input signal Schmitt trigger gate A1U16A. When in internal mode, this signal is an "enable" signal to pass the internal oscillator signal. When in "external", with a signal input at the rear-panel OSC INT EXT connector, this signal is the external input. The output of this gate goes to the A4 Function Selector, Motherboard Bus, and EXT/INT switch (S1). The EXT/INT switch and connector are connected such that the connector inputs an EXT signal in external operation and outputs the internal oscillator in internal operation. This oscillator circuit may be modified by installing Option 010 Oven Oscillator.

8-A-7. Option 010 Installation and Removal Instructions

a. Disconnect the power cable from the 5328B.

WARNING

THE AC POWER CIRCUITS TO TRANSFORMER T1 AND THE UNREGULATED DC VOLTAGE ARE STILL ON EVEN WHEN THE POWER SWITCH IS OFF. CONTACT WITH THESE CIRCUITS CAN RESULT IN INJURY TO PERSONNEL OR DAMAGE TO THE EQUIPMENT.

- b. Remove the top and bottom covers by loosening the screw at the rear of each cover assembly.
- c. Remove the 10 MHz crystal (Y1), from its socket on the motherboard.
- d. Apply power to the 5328B and check pin 2 of connector XA3 for 25 Volts, dc. Check all other voltages at connector XA3 for the values shown on the schematic diagram for the motherboard.
- e. Disconnect the power cable from the 5328B.
- f. Install the plastic support post on the motherboard by securing with a plastic screw from the bottom of the motherboard.
- g. Ensure that the oven oscillator is secured to the connector on the support board. Plug the support into XA3 on the motherboard with the stud mount facing the rear panel.
- h. Install a 6-32 X 5/16 inch screw through the 5328B main bracket into the support stud mount and tighten.
- i. Apply power to the 5328B and verify counter operation by performing the In-Cabinet Performance Check in Section IV.

8-A-8. Option 010 Removal

To remove the Option 010, essentially reverse the steps used to install the Option 010.

8-A-9. Decade Counting Assembly (DCA)

The Model 5328B's Decade Counting Assembly (DCA) consists of Decade Counter/Latches A1U10 and A1U14, and integrated circuits A4U1A, A4U3, and A4U4B on the Function Selector Board (A4). The Motherboard contains output-enable circuitry (A1U6, A1U7, and A1U9) for controlling the counter's output data, signal overflow indication, and circuitry (A1U41) for strobing data into the display. The first Decade Counter in the sequence of operation corresponds to the least-significant digit; the last Decase Counter corresponds to the most-significant digit. Digits "0" through "5" are processed by A1U10, digit "6" by A1U14, and digit "7" by A1U12.

All measurements performed by the counter result in pulses being counted in the DCA. Pulses are admitted to the DCA via the Main Gate Flip-flop on A4; this flip-flop is controlled by a "Gate Out" signal from the Time Base (A1U19), or it is held open by the "HOPN" signal from A1U25.

Data Strobe signals, TRANSFER pulses, RESET pulses, and an output-disabling signal are routed to the DCA via the Model 5328B's State Control Circuitry. These signals are processed in the DCA and are used to control transfer of the counter's output data to the latch outputs, strobe this data onto the Data Bus, disable the outputs that feed into the Data Bus, and reset the counters after a measurement cycle is over.

8-A-10. Time Base

The Model 5328B's Time Base circuit consists of eight-decade divider A1U21, shaping flip-flop A1U19A, and synchronization flip-flop A1U19B.

The Time Base's input, depending on the particular measurement being made, is either:

- the 10 MHz system clock, or
- the Channel "A" or Channel "B" input signal.

The Time Base's input signals are routed to it via the ROM-controlled Time Base Multiplexer (A4U10) on the Function Selector Board.

The Time Base circuit has two modes of operation, corresponding to the Model 5328B's two measurement modes.

- For frequency-or time interval-type measurements, the Time Base circuit generates a gate during which either oscillator or input pulses are counted.
- For totalization-type measurements, the Time Base circuit divides its input by "N" (which is determined by the setting of the front-panel "RESOLUTION, N" switch), and outputs the divided signal to be counted in the DCA.

The Time Base circuits outputs are generated simultaneously for either operating mode. Regardless of the type of measurement being performed, these outputs are made available to the Function Selector (A4) which selects the signal that is correct for the function.

The length of time generated by the Time Base circuit and the scale factor of the Time Base Input is determined by the Time Base Code. The counter's Mainframe ROM reads the codes of both the Time Base ("RESOLUTION, N") and FUNCTION switches and outputs the proper code to the Time Base such that the measurement resolution and scale factor agree with the information in the various RESOLUTION, N switch positions.

8-A-11. Function Selector (A4)

8-A-12. The Function Selector is the counter's main high-speed switching module, as shown in Figure 8-A-13. It receives high-speed differential ECL data from the Main bus (from the modules that process the signal input) and routes that data to either the Time Base or the DCA. This board also contains the Model 5328B's Main Gate Flip-flop, Arming Multiplexer and Arming Flip-flop, and the first Decade of the DCA.

NOTE

Refer to Table 8-2 for a listing of signal names and descriptions.

8-A-13. HIGH-SPEED MULTIPLEXER, MAIN GATE, AND FIRST DECADE

The high-speed Multiplexer (A4U6), is controlled by commands from the ROM in the Main Counter Section. The multiplexer selects from the signals present at its inputs the signal that will be sent to the Main Counter Section. The signals are sent via the First Decade circuit on the multiplexer board to be used as data. The signals at the inputs of the Multiplexer are:

- · Channel 'A".
- Channel "B",
- Gated Oscillator ("GOSC"),
- Digital Voltmeter output ("DVM")
- Time Base Output ("TBO")
- Oscillator output ("OSC")

ROM lines "IA", "IB", and "IC" control the multiplexer's "active" address. Pin "2" ("ENABLE") of the multiplexer is the Main Gate. The Low Time Interval (LTIF), Low Main Gate FF (LMGF) or (LTOT*LST), signal operating through A4U8 and enabled by ROM lines LMGF, LTIF, (LTO*LST), respectively, control the Main Gate. In addition, ROM line "HOPN" can override LTIF or LMGF and lock open Main Gate A4U6(2) through A4U8C. The status of the Main Gate is detected and sent off the Function Selector Assembly by ECL-to-TTL translator A4U2D. Capacitor A4C11 and Resistor A4R35 stretch any ECL gate signal present at A4U2(10) so that the slower TTL control chip (A1U4) and gate light one-shot (A4Q6, A4U36B,E) can see the pulses and properly react. Integrated circuit A4U8D differentially drives bus lines MG and MG to operate the remote Main Gate of Channel "C".

The output of the main multiplexer (A4U6(15)) feeds into the first binary (A4U1) of the main DCA. Integrated Circuit A4U1A is an ECL high-speed binary, the output of which is coupled to pins 14 and 15 of ECL-to-TTL translator A4U2. The TTL output of A4U2(13) clocks Schottky quinary A4U4 and A4U3. The outputs of the first decade A4U3(9), A4U4(9), A4U3(5), and A4U2(13) travel off the Function Selector board to the next DCA on the Motherboard, where they are latched and the carry feeds into the next decade of the DCA.

8-A-14. ARMING FLIP-FLOP AND ARMING MULTIPLEXER

The Arming Flip-flop (A4U4A), can inhibit various measurements by enabling or disabling the Time Base Multiplexer (A4U10) and the synchronizers in the Model 5328B's Programmable Input

Module. This inhibiting action occurs via the "High Disables Synchronizers" ("HDS") signal from A4U4(6). The signal which sets or enables A4U4 comes from the Arming Multiplexer (A4U5(6)). ROM lines control A4U5(10, 11) while the remaining address line (A4U5(9)) is controlled by the "Low Arm" ("LARM") signal from the rear-panel ARM switch. Integrated circuit A4U5 thus selects either "C-ARM", "A", "B", or "free-run" (+5V) as the signal to send to A4U4 as the Arming signal. The "A" and "B" signals are derived from ECL-to-TTL translator A4U2A and A4U2B, respectively. Capacitors A4C4, C5, and resistors R17 and R18 serve as pulse-stretcher timing elements to enable the narrow ECL pulses on lines \overline{A} and \overline{B} to be seen by the TTL Arming Flip-flop (A4U4).

8-A-15. TIME BASE MULTIPLEXER AND MAIN GATE FLIP-FLOP

The Time Base Multiplexer, A4U10, selects either "A", "B", or "OSC" to send to the Time Base Input ("TB I") via pin "8" to the Time Base. This same signal is also sent to A4U1, the Main Gate Flip-op, as a re-synchronizing signal. ROM lines "R(HTBA)", "R(HTBO)", and "R(HTBB)" control the selection of the Time Base Input signal. the "HDS" signal to A4U1(13) or ROM line "LTOT" to A4U10(1) serve to enable or disable A4U10.

Integrated Circuit A4U1B is a high-speed ECL Flip-flop used to generate precise stable gate times for the Main Gate Multiplexer, A4U8, and the remote gate in the Frequency "C" module. A TTL replica of the Main Gate signal ("GATE OUT") is generated in the Time Base and sent to A4U1 via the line Main Gate Synchronizer on the Motherboard. Resistors R14 and R43B translate this TTL signal down to ECL levels at A4U1(10). The output of Time Base Multiplexer A4U10 via resistors A4R42 and A4R43C and capacitor A4C16 clocks A4U1(11), yielding a synchronized fast rise and fall time main gate signal on A4U1(14).

8-A-16. EXAMPLE OF OPERATION

To show how the above-mentioned function selector circuits operate together, an example of the measurement of Frequency A is given below.

Assume that the counter is in the middle of its display cycle. "Low Inhibit" ("LINH") is TTL "low", "High Reset Time Base" ("HRTB") has momentarily gone "high", resetting A4U1 and A4U4 and "High Reset Decade" ("HRD") has momentarily gone "high", resetting First Decade A4U1, A4U4, and A4U3. The control chip on the Motherboard releases LINH to go "high". Integrated circuit A4U9(13) goes "low", enabling Arming Multiplexer A4U5. Assuming that "SELF ARM" has been selected, "A" will have been selected by the ROM on pins "9", "10", and "11" of A4U5. When the first "A" pulse occurs, A4U4(4) goes "low", setting A4U4. Integrated circuit A4U4(5) goes "high", turning on transistor Q1, which, in turn, pulls LINH "low" again and inhibits another measurement from starting until RESET has occurred. In a frequency measurement, the ROM selects the Oscillator signal in pin "2" of A4U10 to be sent into the Time Base. Shortly after the Time Base returns, a "high" signal on Main Gate Synchronizer drives A4U1(10) "high". On the next Oscillator signal (through A4U10), A4U10(11) gets clocked, causing A4U1(14) to go "low". This "low" signal propagates through A4U8(B,C) to A4U6(2), opening the Main Gate and initiating the count. Signal "A" has been selected on A4U6 by ROM lines R22, R23 and R24, thus each "A" event is counted into first decade A4U1A, A4U4A, and A4U3.

After the appropriate gate time has elapsed ("N" clock counts into the Time Base) the Main Gate Synchronizer signal goes "low" and the next Oscillator signal clocks Main Gate Flip-flop, A4U1, closed. Intrgrated circuit A4U2(10) detects the closing of the Main Gate and sends a TTL signal (LMGF) to A1U4 in the State Control section of the Motherboard, which initiates a new display cycle.

8-A-17. Display Assembly (A16)

The Display Assembly (A16) contains the display (as shown in the Overall Block Diagram in Figure 8-7), in addition to switches S1 ("POWER"), S2 ("RESET"), S3 ("FUNCTION"), S4 ("FREQ RESOLUTION, N"), and variable resistor A16R6 ("SAMPLE RATE" control).

The display consists of a nine-digit seven-segment LED numeric display (DS1-DS9) and indicators for measurement units (DS10-DS16), in addition to "overflow" (OVFL) (DS17), "remote" (RMT) (DS18), and "gate" (GATE) (DS19). The display digits and the other indicators are automatically lighted, along with the correct decimal point.

The digit address code from A1U39 on the Motherboard is applied to transistors Q1 through Q9 to strobe each digit which receives the seven-segment code from A1U41 through transistors A16Q13 through A16Q20. The "GATE" (DS19), "RMT" (DS20), and "OVFL" (DS17) indicators receive signals from the Motherboard through transistors A16Q10, A16Q11, and A16Q12, respectively.

8-A-18. MAIN COUNTER SECTION TROUBLESHOOTING

The Main Counter Section includes the A1 Motherboard, the A4 Function Selector Board and the A16 Display Board. This is the core counter and a majority of the control signals in the counter pass through this section. The block diagram in Figure 8-7 gives an overview of the inter-relationship between the boards in this section. Familiarity with the operation of the basic counter is recommended when troubleshooting this section.

The first step in troubleshooting this section is to identify which board (or boards) is faulty. Table 8-3, Symptoms-To-Section Troubleshooting, is used to isolate the faulty board.

A major task in troubleshooting the Main Counter section is isolating a faulty or inoperative signal or data information to one of the two boards A1 and A4. The passing back and forth of control signals between these two boards in a measurement makes this a particularly tedious task. One of the easiest solutions is board swapping.

8-A-19. Board Swapping

If a known "good" A4 Function Selector Board is readily available then it can be inserted into the A1 Motherboard connector XA4 and the counter tested. If the problem disappears then the A4 Function Selector is faulty. If the problem persists then the problem resides on the A1 Motherboard.

8-A-20. Test Cards

If another A4 board is not available then the test cards mentioned in the kit can be used. Two test cards provide a series of eight tests, four of which will be used in this section. The test cards are numbered 1 and 2 and each has 4 tests, one at each edge of the four edge connectors on the card. The card part numbers are as follows:

Test Card 1: 05328-21004 Test Card 2: 05328-22004

When using the test cards, remove the A4 Function Selector assembly from connector XA4 and replace with the selected edge of the appropriate test card. The cards are keyed to prevent incorrect insertion into the connector XA4.

Each test places a static ROM code condition into the motherboard control signal circuitry, replacing the dynamic signal change of the A4 board. If that section is not functioning properly then the counter fails the test.

Tests 2, 3 and 8 provide the critical function of isolating the Motherboard from the A4 board while still allowing the motherboard to be checked for proper operation. Figure 8-A-1. shows a flowchart suggesting how to use the test cards. Test 3 places a static state into the DCA on the A1 Motherboard and will detect a fault in the DCA if the counter fails to totalize. Test 2 tests both the DCA and the Time Base circuitry. So, if the counter passes Test 3 but fails Test 2, the implication is that the DCA is functioning properly but the Time Base circuitry is faulty. Tests 2, 3 and 8 isolate the fault to either the Motherboard A1 or the Function Selector board A4. Problems on the A16 Display Board are determined by observing the front panel display. Blanked digits or single annunciators blanked generally implies a display problem. Test 8 independently detects a faulty display and may be used before the other tests if a faulty A16 board is suspected.

The next step is to troubleshoot the faulty boards within the faulty section.

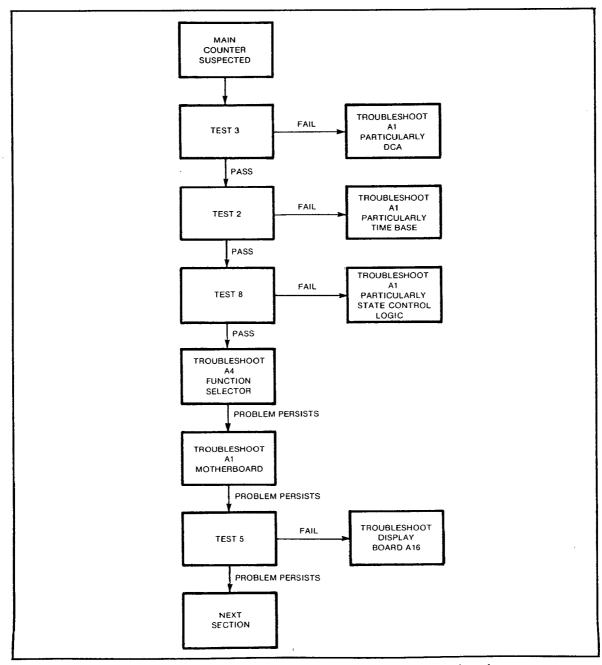


Figure 8-A-1. Main Counter Section Troubleshooting Flowchart

8-A-21. Test Card 1, Test 3 — Decade Counting Assembly (DCA)

DESCRIPTION: This test checks the Decade Counting Assembly (DCA) on the motherboard without the A4 Function Selector being used; this allows isolation of a problem source to either the Motherboard or the Function Selector.

SETUP: Set up equipment for this test as shown in Figure 8-A-2.

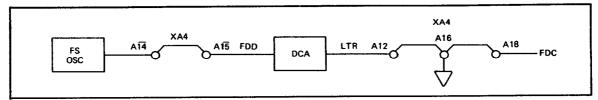


Figure 8-A-2. Test Card 1, Test 3 Setup

PROCEDURE: Set the FUNCTION switch to CHECK and switch ON the counter.

DISPLAY RESULTS:

- The display should totalize. Overflow should occur almost immediately. The decimal point moves with changing the resolution.
- as "N" is increased, the displayed digits will decrease in decade steps of "10".

FAILURE INDICATIONS:

- 1. Count Stops or Display Does Not Totalize: The DCA may be faulty. Specifically check U10, pins 2, 14. These should normally be toggling.
- 2. If there is a number to the right of the decimal point when the function switch is at N=1 (1 Mhz): U10 and/or U12 may be faulty Check U10, pins 3, 4, 5, 6 and U12, pins 2, 15, 16. These should normally be toggling.
- 3. No overflow annunciator: U12 may be faulty. Check pin 7.

8-A-22. Test Card 1, Test 2 — DCA and Time Base (DCA)

DESCRIPTION: The proper functioning of both the DCA and the Time Base circuits can be tested without the A4 Function Selector board. Problem isolation between the A1 Motherboard and the A4 Function Selector board is possible. If the counter passed Test 3 and fails Test 2 then the problem most likely resides in the Time Base circuitry on the Motherboard.

SETUP: Set up equipment for this test as shown in Figure 8-A-3.

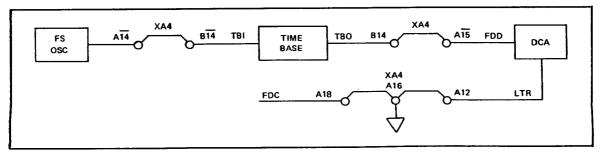


Figure 8-A-3. Test Card 1, Test 2 Setup

PROCEDURE: Set the FUNCTION switch to CHECK and switch ON the counter.

DISPLAY RESULTS: The display should totalize with no immediate overflow. Overflow will occur after a period of time if the counter is allowed to continue totalizing. The decimal point should shift on changing the resolution.

FAILURE INDICATIONS:

Counter stops totalizing: If the counter has passed Test 3 and fails Test 2 then the Time Base may be faulty. Check U20, pins 1, 12 and U21, pins 3, 11. When the counter is functioning normally these pins should be toggling.

8-A-23. Test Card 2, Test 8 — Auto Sample Rate

DESCRIPTION: This test allows the Sample Rate circuitry to be tested without an A4 Function Selector in the instrument. This allows for fault isolation between the Motherboard (A1) and the Function Selector (A4). The Time Base Output simulates the opening and closing of the main gate.

SETUP: Set up equipment for this test as shown in Figure 8-A-4.

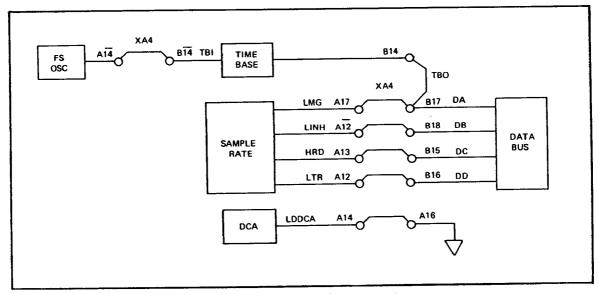


Figure 8-A-4. Test Card 2, Test 8 Setup

PROCEDURE: Set the FUNCTION switch to CHECK, and the RESOLUTION switch to 1 Hz (106). Set SAMPLE RATE to mid-range.

DISPLAY RESULTS: The display will alternately show 11.1111111 and 33.3333333.

FAILURE INDICATIONS:

- 1. Display stuck on either 11.1111111 or 33.3333333: Check U5, pin 13; U18, pins 8, 9; U19, pin 13. The waveforms on these pins should normally be shifting up and down as the display toggles between the two values.
- 2. Incorrect results, e.g., the display toggles between 0.0000000 and 22.2222222: Check U18, pins 10, 11, 12, 13 and U19, pin 2.
- 3. Blank Display: Check "CLK" line on Motherboard between U4 and U5.

8-A-24. Test Card 2, Test 5 — Function Code and Display

DESCRIPTION: This test can be used to verify the Function Code or as a rapid way to verify a faultless display.

SETUP: Set up equipment for this test as shown in Figure 8-A-5.

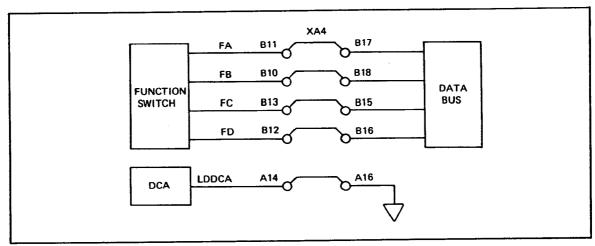


Figure 8-A-5. Test Card 2, Test 5 Setup

PROCEDURE: Set the RESOLUTION switch to 0.1 kHz (104), and set the FUNCTION switch to each position listed in Table 8-A-1.

Table 8-A-1. Test Card 2, Test 5 — RESOLUTION switch settings and Display Results

FUNCTION	DISPLAY	NOTE	ANUNCIATOR
Check Freq C	00000000 3 3 3 3 3 3 3 3 3	Ninth digit if Option 031 is installed.	MHz MHz
DVM START A STOP	11111111111111111111111111111111111111	·	V m m
*START CLOCK *DVM/A *DVM, A→B FREQ A PER A PER AVG A RATIO B/A TI A→B TI AVG A→B	2222222 3333333 5555555 4444444 5555555 4444444 5555555	The "phantom" (unmarked) positions of the FUNCTION switch that follow (*) are located 1, 2, and 3 switch positions clockwise from STOP, respectively.	m m kHz ms ns ns
EVENTS C, A→B RATIO, C→A	3.3.3.3.3.3.3. 6666666666	Ninth digit if Option 031 is installed.	

*"Phantom" Functions

DISPLAY RESULTS: See Table 8-A-1, above.

8-A-25. Troubleshooting The A1 Motherboard

The various tools used in troubleshooting the Motherboard are:

The troubleshooting tree in Figure 8-A-9
Troubleshooting Motherboard Integrated Circuits, Table 8-A-2
A1 Motherboard Waveforms, Figure 8-A-7
Oscilloscope to observe waveforms on IC pins

The results of the tests using the test cards indicate whether the problem is in the DCA, Time Base or Sample Rate circuits. The troubleshooting tree is primarily a short-cut technique relating symptoms to the possible faults in the devices. Use it as a guide in troubleshooting the Motherboard.

The motherboard should initially be viewed as a collection of separate, but interrelated modules. Each module accomplishes some function in order to produce the appropriate display, given the desired signal processes as called up through the front panel control switches.

The front panel output is comprised of varying TTL levels. These levels are decoded by the ROM, and used to:

- 1. Control the board functions through the latches.
- 2. Determine the appropriate decimal point, annunciator and time base resolution.

The display itself is "strobed" such that while all the digits hold the same data at any given time, only one is energized at a time to display such information. Any given number on any given digit will be seen only when the seven segment decoder activates the appropriate segments and the U39 digit strobe energizes the individual digit.

U41, the seven segment decoder, receives its data from latch U11, the decade U10 and the hex decade U12. Because the appropriate data must be decoded by U41 at the correct time in relation to the "strobe" output of U39; U10, 11 and 12 are enabled with a digital signal processed by the digit address decoder. This circuit processes digit data from the digit decoder U1, which also controls the strobe time. The digit address decoder is enabled by its own independent clock, and thus the proper segments are present at the proper time to yield the appropriate display.

The format for the rest of the A1 Motherboard troubleshooting will discuss a list of symptoms and trace back to the probable cause.

Circuit elimination is the first step in the troubleshooting process. If an instrument fails in any or all functions, verify operational circuits to limit the field of inquiry. For example: If there is something on the display (e.g., "0s" but no "1s" when in check mode) then the digit decoder and strobe are functional. If changing the resolution switch changes the decimal placement, the decimal comparators are probably functional. If the gate responds to sample rate changes, the U4 state control circuitry works and if it changes with resolution settings, then the time base is also probably functional.

Problems affecting the displayed signal may be caused by circuit malfunctions in three areas:

- 1. Strobe circuitry
- 2. Decade divider/segment decoder
- 3. Digit decoder

Since visibility of the signal relies upon repeated triggering, the enabling signal must be long enough to allow the LED segments to respond. Otherwise, the eye will fail to detect a visual signal and the display will appear dim or blank. The usual cause of this condition is an excessively high clock frequency, causing the strobe to be too rapid and enabled for too short a time. Note, however, that the decimals will appear correct; a lamp test will enable the decimal points but not the LED segments. The cause of this is usually an open A1C3. Also, component failures may cause the digit clock to shut down, causing a blank display or decimal point blanking.

A blanked display may be caused by circuitry other than the digit decoder. The digit clock is fed through C29, Q7 and CR18 to the blanking circuitry to synchronize it with segment decoding. If C29 is open, the clock pulse is not fed to the blanking circuit with the result of a dim or blanked display with decimals points showing correctly.

Problems with decimal points and annunciator LEDs could be due to faulty digital comparators or incorrect ROM outputs. The comparators are in three basic configurations:

- 1. Feeding U28A and U32
- 2. Decoding measurment unit data
- 3. Feeding U28B decimal point decoding data.

The comparators themselves are exclusive-OR gates in parallel which process a combination of digit strobe and ROM levels. The comparators translate function and frequency resolution commands to specifically timed digital data which will enable the proper decimal and measurement unit LEDs. Signal tracing is helpful in determining the faulty area.

Problems with decimal points and measurement unit LEDs are also caused by malfunctioning invertors on the ROM outputs. Also, a faulty exclusive OR gate or latch may cause symptoms on the signal lines, similar to those resulting from a defective invertor. Fault symptoms of incorrect decimal points/annunciators and incorrect functions may indicate an incorrect signal input to the latches and comparators. A faulty ROM could cause the same symptoms, though quite unlikely. To verify the ROM, refer to Table 8-A-2.

Troubles in one or more functions without visible symptoms in the decimal points or annunciators are usually traceable to the ROM latches or their associated signal lines. Proper operation of signal lines are crucial because they control board operation. A failure of the instrument to self check or symptoms that indicate the absence of a signal may be caused by an improper code applied to the main gate multiplexer on the A4 Function Selector board. An occasional problem is that an incorrect signal is being counted or the same signal is counted regardless of the function selected. Check the latches that supply the appropriate level codes. While the latches themselves may not be faulty, quicker analysis is allowed by narrowing down the field of investigation.

The counter may show an intermittent failure to self check or a locked up U4. This may be due to a leaky power-up reset capacitor C5. A1C5 must charge up to a full +5V to effectively prevent U4 from going into a reset mode intermittently.

In all the cases listed above, use of Table 8-A-2 Troubleshooting A1 Motherboard Integrated Circuits. The oscilloscope patterns should be useful in determining the faulty components.

8-A-26. Troubleshooting The Function Selector Board A4

By using Figure 8-A-1, board swapping or using the Symptoms-to-Section Troubleshooting Table 8-3, a faulty A4 Function Selector Board may be identified. The troubleshooting flowchart, Figure 8-A-12, may be used to find the faulty component(s) on the board.

8-A-27. Troubleshooting The A16 Display Board

Display problems are usually more easily identified by observing the front panel display while changing the function and resolution switches. Use Test Card 2, Test 8. If the problem is suspected to be on the A16 board use the block diagram and troubleshooting flowchart in Figure 8-A-14.

8-A-28. Troubleshooting Integrated Circuits

To troubleshoot integrated circuits on the (A1) Motherboard:

- a. Set the FUNCTION switch to CHECK.
- b. Set the FREQ RESOLUTION, N switch to 1 MHz, 1.
- c. Disconnect the Model 5328B from its ac power source.
- d. Remove the Model 5328B's top cover.
- e. Remove the Model 5328B's (A4) Function Selector Assembly.
- f. Re-connect the Model 5328B to an operating ac power source.
- g. Turn the Model 5328B's line power "on".
- h. Using a HP Model 10528A Logic Clip or HP Model 10525T Logic Probe, check for the logic states as shown in Table 8-A-2. A dark pattern in the table indicates a logic "high".

Table 8-A-2. Troubleshooting Motherboard Integrated Circuits (Part 1 of 4)

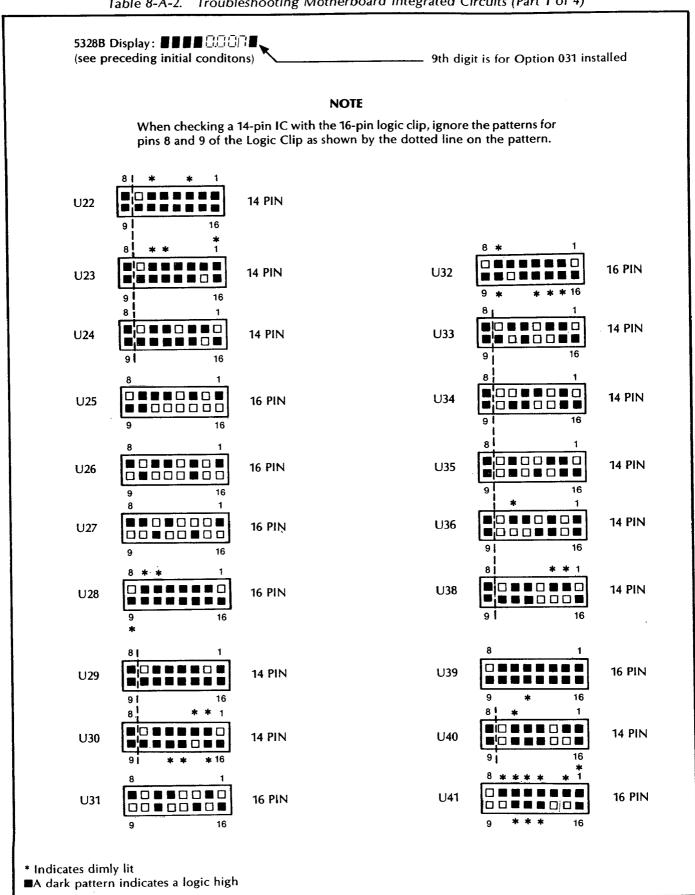


Table 8-A-2. Troubleshooting Motherboard Integrated Circuits (Part 2 of 4)

		FUNCTION															
PINS U2	5	СНЕСК	FREQ C	DVM	START A	STOP	*START CLOCK	*DVM/A	*DVM A→B	FREQ C	PER A	PER AVG A	RATIO B/A	TI A→B	TI AVG A→B	EVENTS C, TI A→B	RATIO C/A
INPUTS	2 3 6 7	L H H	L H L	HHL	HHHH	Н Н Н	H H H H	H H H	H H H	L H H	H L H L	H L H L	HHHH	H L H	H L H L	H H L	H H L
OUTPUTS	9 10 15 16	H H L	H L L	H H L	L L H L	L L H L	L H L	H H L	H H L	L H L	L H L H	L H L	L H L	H	L H L H	L H L	L H L H
U26			ļ					:									
INPUTS	2 3 6 7	L H H L	H L L	L H H L	L H L	L H L	- H	L L H L	L L L	L H H	L H L H	HHHH	HHL	L H L	1 H 1	H L L	H L H
OUTPUTS	9 10 15 16	L H L	L L L	L L L	L L L	L L L	L	L L L	L L L	L L L	L H L	L H L	HLL	L H L	H H L	L L L	L L L
U27																	
INPUTS	2 3 6 7	L L H	L L L	L L L	L H H	L H H	- H H	L H H	L Н Н	L L H	L H L	L H L	L H H	L H L	L H L H	L H H	L H H
OUTPUTS	9 10 15 16	L L L	LHL	L H L	H L H	HHHHH	H	H H L	H H L	L H L	H H L	H L L	H L L	H H L	H H L	H L H L	H L H L
U31					:				,								
INPUTS	2 3 6 7	H L H L	H L H L	L L H	L L H	H	L H H	L H H	L L H	H L H L	L L L	L L L	L H H	L L L	L L L	L L H	L L H
OUTPUTS	9 10 15 16	L L H	LLH	L H H	L L H	L L H	L L H	L L L	H L H L	L L H	L L L	L L L	1111	H L L	H L L	H L L	L L L

Table 8-A-2. Troubleshooting Motherboard Integrated Circuits (Part 3 of 4)

	7	FREQ RESOLUTION								
		z 1	z 10		103	104	105	106	107	
PINS U25		1 MHz	.1 MHz 10	10 kHz 10 ²	1 kHz	.1 kHz	10 Hz	1 Hz	.1 Hz	
INPUTS	2 3 6 7	н н г	г Н Г	ւ Н Լ Н	L H L L	ւ Н Н Н	L H H L	L H L H	լ Н Լ . Լ	
OUTPUTS	9 10 15 16	H H L	н н н	H H L L	Н Н	H H L L	H H L	H H L	Н Н Н L	
U26										
INPUTS	2 3 6 7	L H H L	L H H L	L H H L	L H H L	L H L	L L H L	L L H L	L L H L	
OUTPUTS	9 10 15 16	L H L L	L H L	L H L	L H L L	L H L H	L H L H	L H L H	L H L H	
U27										
INPUTS	2 3 6 7	L L L	L L L	L L H	L L H	L L L H	L L H	L L H	L L L H	
OUTPUTS	9 10 15 16	L L L	L L L	L L H	L L L	L L L	L L L	L L H	L L L H	
U31						•				
INPUTS	2 3 6 7	H L H L	H L H L	H L H L	H L H L	H L H L	H L H L	H L H L	H L H L	
OUTPUTS	9 10 15 16	L L H	L L L	L L L H	L L L	L L L H	L L L H	L L L	L L L H	

U37
(ROM STATES WITH A4 REMOVED)

1 -2V +5V L L L 6 Н L 9 ļΗ 10 Н 11 Н 12 Н 13 L

14

L

15	L
16	GND
17	L
18	L
19	L
20	Н
21	Н
22	L
23	L
24	Н
25	Н
26	Н
27	L
28	+12V

8-A-29. Display Assembly (A16)

To remove the Model 5328B's Display assembly (A16) —

- a. Disconnect the Model 5328B's line cord (as a safety precaution).
- b. Remove the Model 5328B's top cover.
- c. Using a suitable flat-blade screwdriver as a prying tool, gently remove the plastic filler strip from the top of the counter's cast front-panel frame.
- d. Remove the two machine screws that secure the top of the display front panel to the top of the cast front-panel frame.
- e. Turn the Model 5328B on its side and remove the two machine screws that fasten the bottom of the display front panel to the bottom of the cast front-panel frame.
- f. Slightly loosen all remaining machine screws along the top of the cast front-panel frame. This releases the compressive force on the display front panel.
- g. Remove the Display assembly (A16), with front panel attached, by gently pushing the assembly from the rear. Note that the Display assembly is separated from the A1Motherboard assembly during this operation.
- h. Using a suitable allen wrench, remove the SAMPLE RATE control knob from the Display assembly.
- i. Remove the nuts that attach the SAMPLE RATE and RESET switches.
- j. Separate the front panel from the display assembly.

NOTE

If the FUNCTION or FREQ RESOLUTION switch control knob is removed, or if the associated printed circuit board switch is disassembled, the knob and switch must be aligned during replacement as described in the paragraph below.

- k. To realign the display switches with the proper knob positions:
 - 1) Set the rear wafers with the slots directly toward each other.
 - 2) Set the knob of the FUNCTION switch to START A.
 - 3) Set the knob of the FREQ RESOLUTION switch to its 0.1 kHz (104) position.
 - 4) Tighten the two setscrews on each of the above knobs with a suitable allen wrench.

8-A-30. OVEN OSCILLATOR (Option 010)

8-A-31. The Option 010 Oven Oscillator is an extremely stable, compact, low-power source of 10 MHz. The crystal, along with the oscillator, circuit buffer amplifier, and oven control circuits are all mounted inside a thermally insulated housing. A block diagram of the oven oscillator is shown in Figure 8-A-19.

8-A-32. The oscillator is divided into three sections with each section contained on a separate printed circuit board. The boards are connected by cable assemblies. The arrangement allows the unit to be easily disassembled and operated in the disassembled state on the service bench. The three sections can be separated into the following subsections:

- 1. Oscillator
- 2. Automatic Gain Control

Impedance Matching Amplifier

Voltage References

3. Output Buffer Amplifier

Oven Heater and Controller

Precision Voltage Reference

Controller Turn-On Current Limiting Circuit

Heater Transistor Balance Circuit

8-A-33. The oscillator is a Colpitts-type crystal oscillator which uses the crystal as the series inductor. The crystal (Y1) is a "third overtone" crystal and is operated at 10 MHz. To keep the circuit from oscillating at the crystal's fundamental, or at a different overtone, the mode suppression network consisting of C5, L2, C6, and L3 appears capacitive only at frequencies between 9 MHz and 10.5 MHz. Below and above this frequency range, the network appears inductive. This does not allow the proper phase shift around the loop and thus suppresses oscillations at all frequencies other than 10 MHz.

8-A-34. Any reactance in series with the crystal will cause a change in frequency. Tuning capacitor C1 is available from the top of the oscillator outer housing. The change in reactance in C1 allows the oscillator's frequency to be varied over a 20 Hz (2 X 10^{-6}) range.

8-A-35. ELECTRONIC FREQUENCY CONTROL (EFC). To allow for a fine tuning control, a varactor (CR1) is added in parallel with the C1 tuning capacitor. The varactor's capacitance depends on the dc voltage applied to it (reverse bias). The EFC voltage range is +5V to -5V, giving a fine tuning range of about 1 Hz (1×10^{-7}). Since one side of the varactor is tied to a reference (6.4V), a full +5V applied to the EFC input will still keep CR1 reverse biased. C2 and C3 keep the EFC current from flowing into the crystal circuit.

NOTE

EFC is not used in the HP 5328B.

- 8-A-36. AUTOMATIC GAIN CONTROL (AGC). The Automatic Gain Control circuit consists of emitter-follower Q3 and the peak detector circuit formed by C12, C13, CR4, and CR5. The input to the AGC circuit (and output amplifiers; discussed later) is taken across capacitor C10 and applied to Q3. The signal from Q3 goes to the peak detector which develops a dc voltage to control the crystal current. This negative control voltage forms the lower half of a voltage divider for the base of Q1 (with R6 and R7) which controls the bias current and gain of Q1, thus controlling the output signal level. The voltage across C10 is proportional to the current through the crystal. As the output of the oscillator changes, the output of the peak detector circuit changes to counteract the oscillator signal change. The result is a stable output signal amplitude.
- 8-A-37. By adjusting the AGC voltage with R6, the amplitude for the output (at the base of Q3) can be set. R5 sets the AGC limit when R6 is at its minimum resistance.
- 8-A-38. RF OUTPUT IMPEDANCE MATCHING AND OUTPUT BUFFER. The signal for the output amplifiers is taken from the same point as the AGC (across C10). The voltage is buffered by Q5, which is an impedance matching stage. Resistors R14 and R15 set the dc bias level; R14 is bypassed by C14. The signal is then applied to the output buffer stage of Q9. R40 provides a 50 Ohm source impedance when transformed by T1. The typical gain of Q9 (base-to-collector) is approximately 2.
- 8-A-39. VOLTAGE REFERENCES. Constant current diode CR2 feeds 1 mA to zener diode CR3 providing 6.4V dc for the EFC varactor reference. R12 and C15 form a filter to attenuate noise from the zener diode. R13 provides current limiting for Q4 if the 5.7V line is shorted.

8-A-40. OVEN HEATER AND CONTROLLER.

NOTE

In the following theory of operation, the term OVEN MASS is used to describe the cast aluminum block in which the crystal and crystal electronics are located.

- 8-A-41. The purpose of the oven is to shield the oscillator crystal and electronics from normal ambient temperature changes. The oven controller does this by maintaining a constant oven temperature which is higher than the highest expected ambient temperature. The oven circuit is made up of three main blocks: thermistor, amplifier (controller), and heaters.
- 8-A-42. A thermistor (RT1) is secured with epoxy into a hole in the oven mass. U3 is the amplifier, and Q7 and Q8 are the heaters. It is the thermistor that senses the oven mass temperature. The thermistor is in one leg of a bridge circuit consisting of RT1, R18, R19, R20, and R21. When the mass temperature changes slightly, a voltage change occurs across the bridge. Amplifier U3 boosts this voltage change and then uses it to control the current through Q7 and Q8. The current flowing through Q7 and Q8 causes a power dissipation in the form of heat, and it is this heat that warms the oven mass. Therefore, when the mass temperature starts to change, the heaters are biased to adjust their power to cancel the impending temperature change.
- 8-A-43. WARM-UP: GENERAL OPERATION. If the oscillator has been off for several hours, the mass and thermistor will be at the ambient temperature. Assuming this is below the normal oven operating temperature (80° to 84°C), the resistance of the thermistor RT1 is higher than that of R18 + R20, and therefore the voltage at U3(3) is more positive than at U3(2). This causes the output of U3 to be approximately (V_{CC} 1.5V), supplying base current to Q8 through Q6. A separate circuit limits the collector current of Q8 and is described later.
- 8-A-44. As the oven mass warms up, the thermistor's resistance begins to drop, causing the voltage at both U3 inputs to drop (the other U3 input voltage drops because the voltage at the junction of R17 and R18,R19 drops due to the lower RT1 resistance). The voltage at U3(3) decreases at a faster rate than at U3(2) and eventually the U3 inputs are equal when RT1 = R20 + R18. At this

time, the oven controller "cuts back" and begins to operate in a linear mode, adjusting the collector current in Q8 (and therefore the power dissipated in Q7 and Q8) to keep the oven precisely at its set temperature.

- 8-A-45. The purpose of R17 is mainly to reduce the power dissipated in the thermistor which causes it to self-heat above the oven operating temperature.
- 8-A-46. R38 and R39 in parallel provide a means of sensing the heater current. During warm-up, the voltage across the parallel resistors is used in the current limit circuit (described later). During normal linear operation, the junction of R38 and R39 is essentially the feedback point for the oven controller loop.
- 8-A-47. Q6 is necessary primarily for the condition when the oscillator has been stored at -55°C. Since U3 (at -55°C) cannot supply enough base current for Q8, Q6 provides the added current gain required.
- 8-A-48. PRECISION VOLTAGE REFERENCE. U2 is a 10.0V voltage reference. It provides a stable voltage source for the bridge and U1. A change in the bridge reference voltage changes the voltage across the thermistor and hence, the power it dissipates.
- 8-A-49. OVEN CONTROLLER TURN-ON CURRENT LIMITING. The turn-on current limiting circuit consists of U18 and associated components. From an initial turn-on condition, the thermistor senses the oven temperature to be low. To correct this situation, U3 attempts to drive heavy amounts of current through the Q7 and Q8 heaters. If allowed to continue this way, excessive current will flow. When V_{CC} is applied to the oven, U1B forces the voltage across R38 and R39 to equal the voltage at U1B(2) by sinking the base current from Q6. By sensing V_{CC}, the circuit transforms the heater transistors into what appears to be a fixed heater resistance of 47 Ohms typical.
- 8-A-50. HEATER TRANSISTOR BALANCE. Because heater transistors Q7 and Q8 are not equally spaced from the crystal, it is necessary to offset the power dissipation between the two transistors. Amplifier U1A references a voltage divider across V_{cc} (R25, R26) and a second divider (R27, R28) referenced to the midpoint between the heater transistors. This arrangement allows U1A to control the base current of Q7 to ensure the voltage at the midpoint between the heater transistors is a constant percentage of V_{cc} ($\approx 0.57 \times V_{cc} \pm 2\%$).

8-A-51. OVEN OSCILLATOR TROUBLESHOOTING

8-A-52. Inspection

8-A-53. The oscillator should be inspected for indications of mechanical and electrical defects. Electronic components that show signs of overheating, leakage, frayed insulation, and other signs of deterioration should be checked and a thorough investigation of the associated circuitry should be made to verify proper operation. Mechanical parts should be inspected for excessive wear, looseness, misalignment, corrosion, and other signs of deterioration.

8-A-54. Special Parts Replacement Considerations

- 8-A-55. Several mechanical parts and components must be replaced as a pair or require other special consideration. They are:
 - a. Oven mass assembly and thermistor: If the thermistor (RT1) is found to be defective, the thermistor and oven mass assembly must be replaced as one item, HP Part Number 10811-60106. Do not attempt to replace the thermistor alone.

- b. Crystal and Temperature Set Resistor: The replacement crystal for Y1 will be accompanied by the required temperature set resistor (R20) for the oven. This resistor must be installed with the new crystal. The crystal and R20 can be ordered using HP Part Number 10811-60108. If only the temperature set resistor (R20) is found to be defective, it must be replaced with the same value and tolerance. If the resistor (R20) is unreadable, the value required can be determined by finding the oven temperature value marked on the crystal (Y1). The required resistor will be either 1.3K Ohms or a jumper. Refer to Table 6-4 in Section VI. When Y1 is replaced, the nut which secures it to the oven mass should be tightened to a torque of 0.6 newton-meters (5 in.-lbs). This will insure maximum heat transfer without over-stressing the crystal package.
- c. Oven heater transistors Q7 and Q8: Holding screws for Q7 and Q8 must also be torqued to a specific force of 0.6 newton-meters (5 in.-lbs.). There are several available pozidriv torquing screwdrivers.

NOTE

When re-installing or replacing one or both heater transistors (Q7 and Q8), replace both transistor insulators, HP Part Number 0340-0864. This is done to ensure the temperature stability of the oven crystal due to a balanced heat transfer to the oven mass from the heater transistors.

8-A-56. Special Test Connector

- 8-A-57. The following paragraphs describe a special connector fabricated for use in troubleshooting, alignment, and testing of the oven oscillator. (Refer to Figure 8-A-17 for photo of connector.) The connector provides the following:
 - a. Two separate input leads for the power to the oscillator circuits and the oven heater/controller circuits.
 - b. 10-MHz output through a female BNC.
 - c. Oven monitor output for connection to a voltmeter.
 - d. EFC input connection to ground.
- 8-A-58. The following parts are required to construct the special test connector:
 - a. 15-pin pc board connector (HP Part Number 1251-0494).
 - b. 6 banana plugs (HP Part Number 1251-0124).
 - c. BNC female connector with ground lug and nut.

BNC connector 1250-0083 Ground lug 0360-0024 Lock washer 2190-0016 Nut 2950-0001

- d. Approximately 6 feet of 24-gauge wire.
- e. Labels for banana plugs.

8-A-59. To construct the connector:

- a. Solder the center pin of the BNC connector to pin 1 of the printed circuit connector; this is the 10 MHz output signal.
- b. Bend the BNC ground lug to align with pin 2 of the printed circuit connector.
- c. Solder one end of a 2-foot length of wire and the BNC ground lug to pin 2 of the printed circuit connector. This is the oscillator circuit common.
- d. Solder one end of a 2-foot length of wire to pin 3 of the printed circuit connector. This is the oscillator (+) supply.
- e. Connect a jumper wire between pins 5 and 6. This terminates the EFC input.
- f. Solder one end of a 2-foot length of wire to pin 11. This is the oven monitor output.
- g. Solder one end of a 2-foot length of wire to pin 14 of the printed circuit connector. This is the oven (+) supply.
- h. Solder one end of two 2-foot lengths of wire to pin 15 of the printed circuit connector. This is the oven common.
- i. Twist together one of the two wires connected to pin 15 and the wire connected to pin 14. These are the oven controller power supply inputs.
- j. Twist together the remaining wire connected to pin 15 and the wire connected to pin 11.
 This is the oven monitor output.
- k. Twist together the two wires connected to pins 2 and 3. These are the oscillator supply inputs.
- I. Connect one banana plug to the free end of each wire.
- m. Label each banana plug as follows:

Wire connected to:	Label as:
pin 2	oscillator supply (-)
pin 3	oscillator supply (+)
pin 11	oven monitor (+)
pin 14	oven supply (+)
pin 15 (two wires)	oven supply (-)
	oven monitor (-)

n. Inspect the connector for poor solder joints, bent, or damaged pins. Double check the labeling of the plugs to be sure the polarity markings are correct. If the voltages are connected the wrong way, damage to the oven oscillator may occur.

8-A-60. Types of Failures

- 8-A-61. Failures in the oscillator unit can be divided into two sections:
 - a. Failure of the oscillator's circuits.
 - b. Failure in the oven controller circuits.

- 8-A-62. Failures in the oscillator circuits can be divided into the following problems:
 - a. No output.
 - b. Output amplitude is too low or high.
 - c. Output is off frequency (high or low).
- 8-A-63. Poor frequency stability can be difficult to troubleshoot and many times the oscillator is not at fault. Environmental conditions can affect stability and should be ruled out first.
- 8-A-64. Failures in the oven circuitry can be divided into the following problems:
 - a. No oven current (heat).
 - b. Excessive oven current (>600 mA).
 - c. Oven does not cut back after warm-up (this will open the thermal fuse if allowed to continue).
- 8-A-65. Since the main oscillator and oven control power supply inputs are separate from each other, the defective circuit can be operated without applying power to the complete oscillator.
- 8-A-66. Determine which section is defective (oven or oscillator circuit), then proceed as described in the following troubleshooting section. The two circuits can be investigated separately.

8-A-67. Disassembly for Troubleshooting

- 8-A-68. To disassemble the oscillator unit:
 - Step 1. Remove the three screws securing the bottom cover to the outer housing, and remove bottom cover.
 - Step 2. Remove the two screws securing the pc edge connector to the outer housing.
 - Step 3. Remove the foam sheet to expose the oven controller circuit board.

CAUTION

With the cover and foam insulator removed, the thermal fuse cannot protect the oven circuit from thermal runaway. Caution should be used at all times.

If troubleshooting the oven controller, stop here and go to paragraph 8-A-70, Oven Controller Troubleshooting. Go to Step 4 only if the trouble is in the oscillator circuit.

WARNING

THE OSCILLATOR'S INTERNAL OVEN MASS TEMPERATURE MAY BE AS HIGH AS 85°C (185°F). TO AVOID SERIOUS BURNS, DO NOT REMOVE OSCILLATOR CIRCUITS AND/OR OVEN MASS ASSEMBLY FROM THE OUTER CAN UNTIL THE OSCILLATOR HAS SUFFICIENTLY COOLED (APPROXIMATELY ONE HOUR WITH BOTTOM COVER AND FOAM INSULATOR REMOVED). THE OUTER HOUSING TEMPERATURE IS NOT A RELIABLE INDICATION OF THE INTERNAL TEMPERATURE.

Step 4. Using a long, small diameter tool, remove the complete oscillator assembly by inserting the tool into the tuning capacitor access hole (labeled FREQ. ADJ.) and gently pushing on the capacitor until the circuit can be grasped and removed easily.

Step 5. Using a pozidriv screwdriver, remove the two screws securing the heater transistors to the oven mass. Remove the washers and transistor insulators.

NOTE

When reassembling the oven mass, the heater transistor screws must be tightened to a torque of 0.6 newton-meters (5 in.-lbs.) (See paragraph 8-A-55(c)).

Step 6. Tilt the oven oscillator assembly back and remove the foam insulator between the oven controller assembly and the oven mass. Be careful not to break the two black thermistor wires attached to the oven controller assembly.

Step 7. Remove the eight screws (four each side) securing the covers to the oven mass assembly.

Step 8. Use two of the screws from each cover (removed in Step 7) to secure the boards to the oven mass for troubleshooting.

8-A-69. Go to paragraph 8-A-84, Oscillator Troubleshooting. When reassembling unit, reverse the above procedure.

8-A-70. OVEN CONTROLLER TROUBLESHOOTING

8-A-71. General

8-A-72. The oven controller section consists of three major circuits and a 10V voltage reference for increased stability of sensitive circuits. Figure 8-A-20 shows the major circuits and active components involved in their operation.

8-A-73. The temperature sense circuit monitors the temperature of the oven mass and reduces the power drawn by the oven heater transistors when the oven mass has reached operating temperature. After power cut-back, this circuit monitors the oven mass temperature and controls the power in the heaters to maintain the constant temperature. The thermistor (RT1) has a negative temperature coefficient. At room temperature the thermistor resistance is approximately 100K Ohms, while at operating temperature (~82°C) the resistance is approximately 9K Ohms. Shorting the thermistor to oven common makes the oven mass appear too hot to the temperature sense circuit. This in turn causes the temperature sense circuit to shut off power to the oven heaters. This technique is used in the troubleshooting procedure.

8-A-74. The warm-up current limit circuit controls the maximum current the oven may draw during warm-up (380 to 490 mA with 20V dc oven input). This circuit is only active during the warm-up phase of the oven circuit operation.

8-A-75. Normal Operation

8-A-76. When the oven is tested under normal conditions (\approx 25°C ambient temperature) it will initially draw 380 to 490 mA. After 5 to 10 minutes the oven current will start to drop. Over the next 10 to 15 minutes the oven current will fall to the 60 to 150 mA range where it will stabilize. The oven circuit should not oscillate.

WARNING

DO NOT OPERATE THE OVEN CIRCUITS WHEN THE OVEN MASS IS OUTSIDE OF THE OSCILLATOR INSULATED HOUSING. DOING SO WILL OVERHEAT THE OSCILLATOR CIRCUITS INSIDE THE OVEN MASS AND CAUSE PERMANENT DAMAGE. ALL OVEN TEST POINTS ARE AVAILABLE WITH THE OVEN MASS AND OVEN CONTROLLER CIRCUIT INSIDE THE HOUSING.

WHEN OSCILLATOR COVER AND INSULATOR ARE REMOVED, THE THERMAL FUSE WILL NOT PROTECT CIRCUIT FROM OVERHEATING. APPLY OVEN POWER ONLY WHEN ACTUALLY MAKING MEASUREMENTS FOR TROUBLESHOOTING OR AS DIRECTED IN TROUBLESHOOTING TREE, FIGURE 8-A-21.

8-A-77. Troubleshooting Tree

- 8-A-78. Figure 8-A-21 is a troubleshooting tree for the oven circuits. The troubleshooting procedure separates the different functional circuits by monitoring the oven supply current during different operating conditions. For example, if the warm-up current is excessive, this indicates a problem in the warm-up current limit circuit, or the current control and heater circuit. If shorting the thermistor reduces the current being drawn from the power supply, this indicates the current control circuit is operating and the problem is most likely in the warm-up current limit circuit.
- 8-A-79. As with most troubleshooting trees, this is intended to be a guide to the trouble area. It is not a substitute for technical skill in isolating the faulty components.
- 8-A-80. Table 8-A-4, Oven Circuit Voltages, gives normal circuit voltages during warm-up, operation, and when thermistor RT1 is shorted to ground. Use this table during troubleshooting.

8-A-81. Troubleshooting Cautions

- 8-A-82. When oven current is excessive, turn on the power supply only long enough to make the necessary measurements. Do not leave power on if the oven is drawing excessive current. With the housing cover and foam insulator removed, the thermal fuse, F1, cannot protect the circuits in the oven mass from overheating and damage.
- 8-A-83. When power is applied to the oven controller circuit, it will go into its full warm-up mode. In this mode, the maximum heating power is applied to the oven mass. The oven mass is a metal casting surrounding the oscillator circuits and crystal. The OVEN MONITOR output will be approximately 1.5 volts below the oven power supply voltage. In about 10 minutes, the oven will have heated to the proper temperature. The oven controller will begin to regulate at this temperature and the OVEN MONITOR will drop to approximately 3.5 volts. It is normal for the oven temperature to drop momentarily to a low value when the temperature first reaches maximum. This lasts less than a second and is a typical circuit action.

8-A-84. OSCILLATOR TROUBLESHOOTING

- 8-A-85. The oscillator circuits are relatively simple and straightforward. The following paragraphs will briefly describe the major circuit areas, a troubleshooting outline, and some helpful suggestions to make the troubleshooting process easier. The oscillator consists of four sections. They are:
 - 1. Oscillator Q1, Q2, and associated circuitry.

- 2. AGC Q3, CR4, CR5, and R6.
- 3. Output circuit Q5 and Q9.
- 4. 5.7V power supply CR2, CR3, and Q4.

The oscillator is the signal source. Its output level is controlled by the AGC. The 5.7V power supply provides an extra-stable clean voltage source for the oscillator circuits. The output circuits provide a high-level signal capable of driving a 50 Ohm to 1K Ohm load.

8-A-86. Normal Operation

8-A-87. The output of the oscillator circuit at Q2 collector is a 10 MHz undistorted sine wave with an amplitude of approximately 2.8V p-p. The AGC voltage (measured at CR5-C13 junction) is approximately –1.5V. The 10 MHz signal passes through Q5 to Q9 base at about the same level. The voltage gain of amplifier Q5 (base to collector) is approximately 2 with a 50 Ohm load on the output. The output of transformer T1 is approximately 1.5V p-p. All 10 MHz signals found in the oscillator will be undistorted sine waves unless otherwise noted in *Table 8-A-5*, Oscillator Section Normal Voltages.

8-A-88. Troubleshooting

8-A-89. When troubleshooting the oscillator section, remove the oven mass from the housing and the covers from the oven mass as described in paragraph 8-A-67. Connect 12V to the oscillator section; use the special connector described in paragraph 8-A-56, Special Test Connector. Set the power supply current limit to 60 mA. Do not apply power to the oven circuits!

8-A-90. Initial troubleshooting and probing should be done on the backside of the boards (trace side) while they are secured to the oven mass (see paragraph 8-A-68, step 8. This way the circuits are more easily handled. When the fault is isolated to a few components, the unit may then be disassembled for final troubleshooting and repair.

8-A-91. Helpful Hints

- 1. Most points in the oscillator circuits cannot be measured with a dc voltmeter. The reactance of the voltmeter probe and leads will load the circuit and give false readings. Instead, use an oscilloscope with a high input impedance probe for these measurements. Table 8-A-5, Oscillator Section Normal Voltages, indicates when a dc voltmeter can be used.
- 2. Before reinstalling the oven mass into the housing, adjust the output amplitude as instructed in paragraph 8-A-96, Output Amplitude Adjustment.

8-A-92. Symptoms of failures in the oscillator sections will generally fall into one of the following categories:

- 1. No output.
- 2. Output Amplitude is low or high.
- 3. Excessive drift of output frequency.
- 8-A-93. Troubleshooting of these faults will be discussed in the following paragraphs.

8-A-94. NO OUTPUT. This is usually easy to repair by simple signal tracing. Localized fault finding (to actual defective component) can be somewhat more difficult if the problem is in the main oscillator circuit (Q1, Q2, and AGC). If the fault appears to be in the oscillator section and does not yield to normal troubleshooting techniques, measure the AGC voltage at the junction of CR5-C13 (See Note 7 on Table 8-A-4, Oscillator Normal Voltages). If this voltage appears normal, the problem may be a defective quartz crystal (Y1). To verify this possibility, obtain a 10 μ H (HP Part No. 9100-2265) and a 12 μ H inductor (HP Part No. 9100-2242). (Use the HP numbered parts as these have been tested in the circuit.) On the oscillator board, remove the red and blue wires connecting the crystal to the board. Place the 12 μ H inductor in place of these wires. With 12V applied to the circuit, adjust the FREQ. ADJ. (C1) and the amplitude control (R6) for a good sine wave signal.

NOTE

At some settings of C1 and/or R6, intermittent oscillations may appear. Some minor adjustment of C1 and/or R6 should clear this. If this fails, replace the 12 μ H inductor with the 10 μ H inductor and repeat the C1/R6 adjustment.

If replacing the crystal with an inductor produces oscillation, this is a very good indication of a defective crystal. When replacing crystal Y1, read paragraph 8-A-55(b), Special Parts Replacement Considerations. If the circuit will still not oscillate, the problem is most likely one of the oscillator circuit elements.

8-A-95. OUTPUT AMPLITUDE HIGH OR LOW. Many times this can be cured by the adjustment of R6 as described in paragraph 8-A-96. If the correct amplitude cannot be obtained with this adjustment, monitor the signal at Q6 collector with an oscilloscope and set R6 to obtain an amplitude of 2.8V p-p. Then check Q5 and Q9 stages. If the R6 adjustment is not effective, investigate the operation of the AGC circuitry (Q3, CR4, CR5, C5, C6, R5, R6, R7, or Q1).

8-A-96. Output Amplitude Adjustment

8-A-97. The output amplitude is adjusted by the setting of the variable resistor R6 which is in the feedback of the AGC circuitry. It is not accessible from the outside of the oscillator.

8-A-98. The following procedure should be used to adjust the output amplitude only if the output level falls outside the specified level, or repairs have been made to the main oscillator or AGC circuitry.

- 1. Remove oscillator from instrument.
- 2. Remove the three screws holding the bottom cover on the oscillator. Remove the bottom cover and allow the oscillator to cool (if previously operated).
- 3. Remove the two screws securing the P.C. edge connector to the outer can. Remove the top foam insulator to expose the oscillator circuits.

WARNING

THE OSCILLATOR'S INTERNAL OVEN MASS TEMPERATURE MAY BE AS HIGH AS 85°C (185°F). TO AVOID SERIOUS BURNS, DO NOT REMOVE OSCILLATOR CIRCUITS AND/OR OVEN MASS ASSEMBLY FROM THE OUTER HOUSING UNTIL THE OSCILLATOR HAS SUFFICIENTLY COOLED (APPROXIMATELY 1 HOUR WITH BOTTOM COVER AND FOAM INSULATOR REMOVED). THE OUTER HOUSING TEMPERATURE IS NOT A RELIABLE INDICATION OF THE INTERNAL TEMPERATURE.

4. Once the oscillator is cool enough to handle, remove the oscillator assembly by pushing on the tuning capacitor (FREQ. ADJ.) with a long, small diameter tool until the oscillator assembly can be removed easily.

NOTE

Under no circumstances should the oven circuit be operated with the oven mass removed from the outer housing. To do so will cause damage to components inside the oven mass.

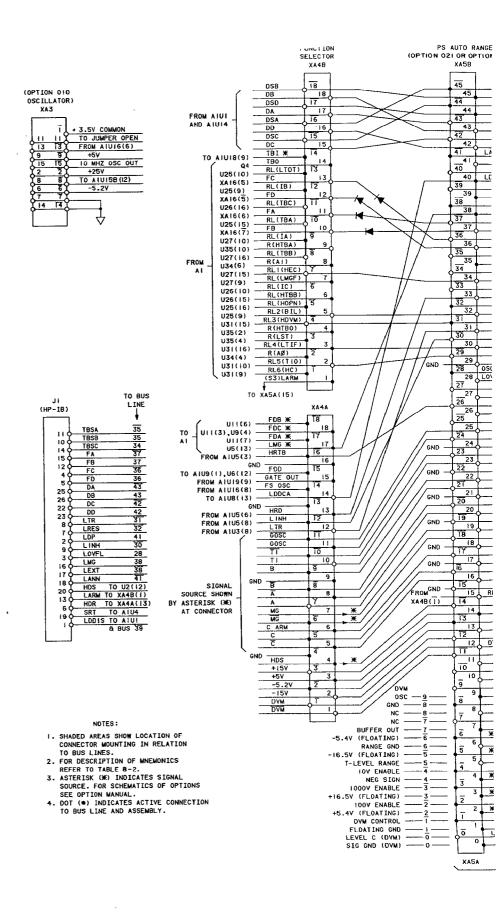
5. Required Equipment:

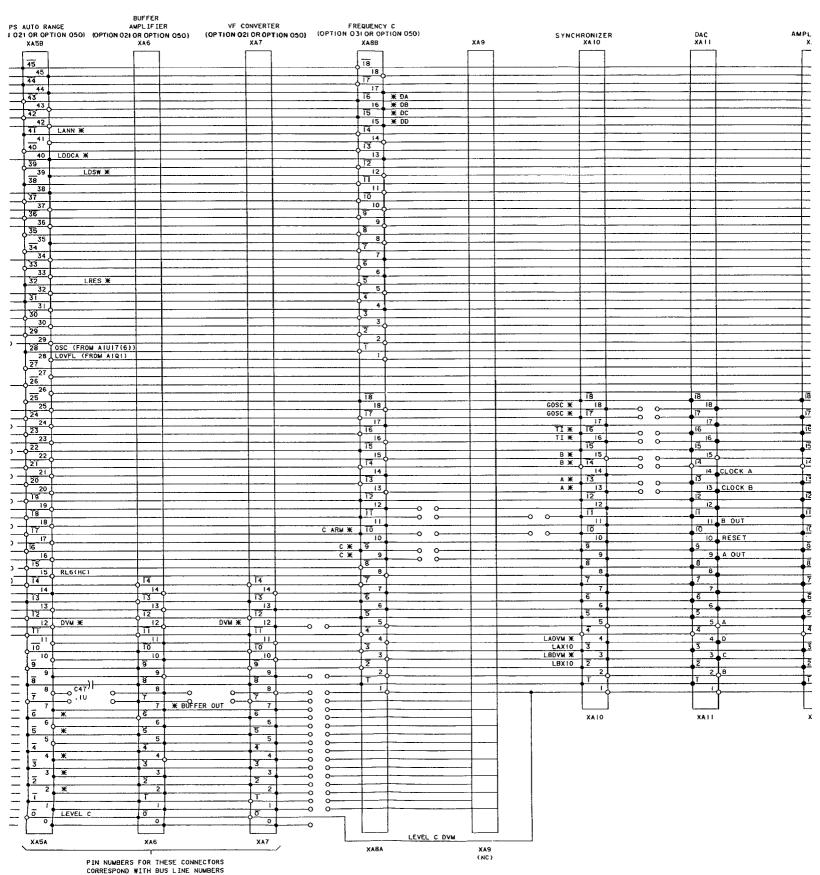
Oscilloscope	HP 1715A
Power Supply	12V dc

Preset power supply to 12V dc. Turn off power supply before proceeding to the next step.

- 6. Connect the power supply to pins 2 (-) and 3 (+) of the 15 pin test connector. (See instructions in paragraph 8-A-56, Special Test Connector, to fabricate the test connector.)
 - a. Insert the oscillator edge connector into the 15 pin test connector.
 - b. Connect pin 1 and 2 of the 15 pin test connector to an oscilloscope using a 50 Ohm coax cable. Set the oscilloscope to 50 Ohm input. Do not apply power to the oven circuits.
- 7. Turn on the power supply and adjust R6 AGC control for 1.56V p-p ±0.14V p-p.
- 8. Turn off the power supply and reassemble the oscillator if the problem is corrected.

8-A-99. EXCESSIVE DRIFT OF OUTPUT FREQUENCY. When the quartz crystal oscillator has not been operated for a long period of time, or if it has been subjected to severe thermal or mechanical shock, the oscillator may take some time to stabilize. In most cases, the crystal will drift and then stabilize at or below the specified rate within a few days after being turned on. In isolated cases, depending on the amount of time the oscillator has been off and the environmental conditions it has experienced, the oscillator may take up to 1 week to reach the specified aging rate. This should be taken into consideration if the drift rate of the unit is out of specifications. If the unit has had sufficient time to stabilize but is still out of specification, the most likely cause of excessive drift is a defective crystal (Y1). If Y1 is to be replaced, read paragraph 8-A-55(b). Other possible causes are an unstable C3 and/or C8.





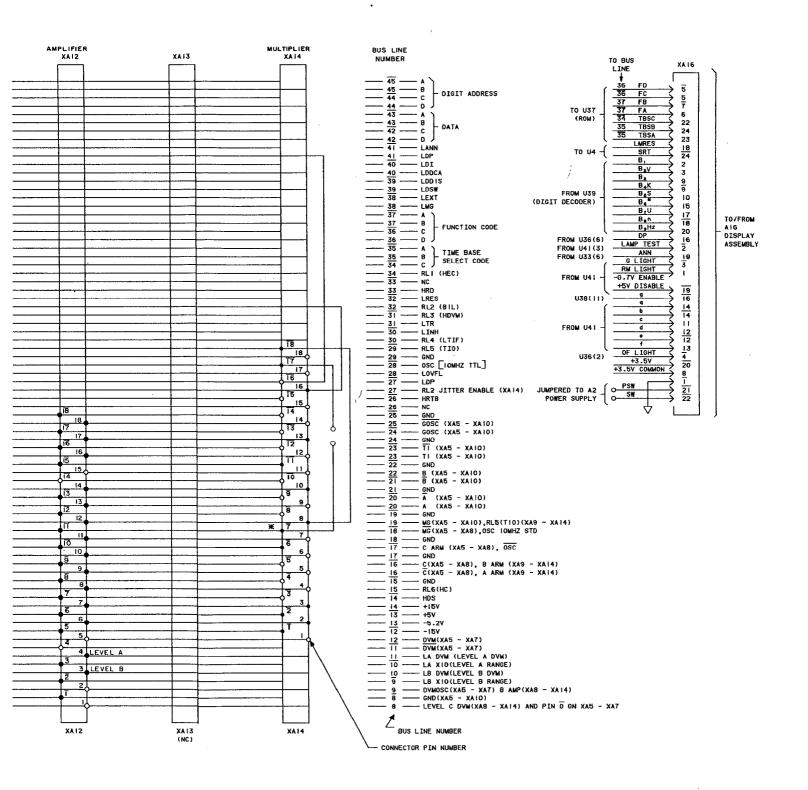


Figure 8-A-6. A1 Motherboard Interconnect Diagram



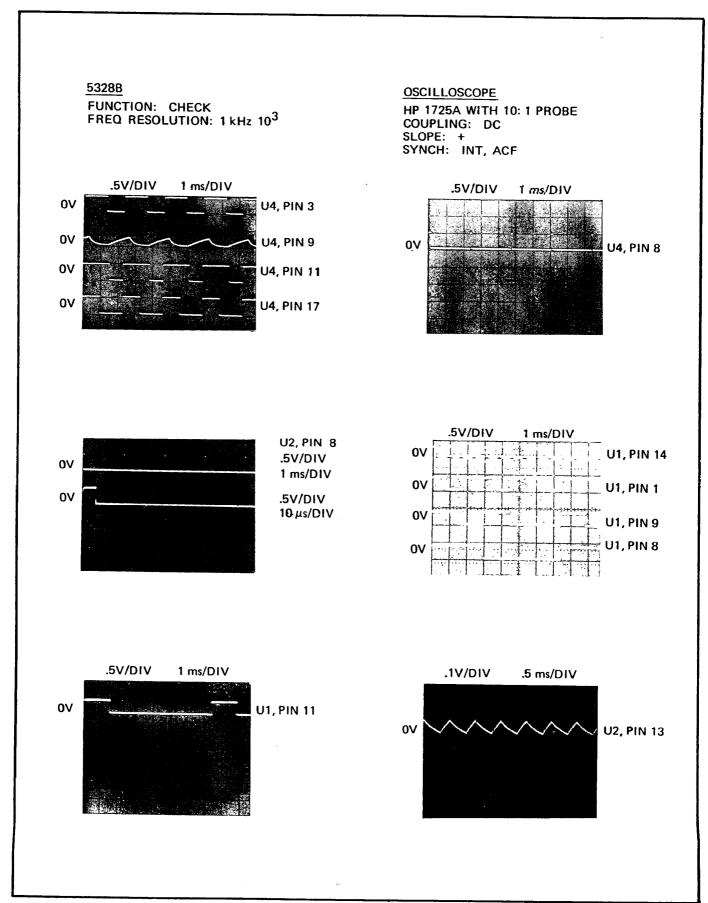


Figure 8-A-7. A1 Motherboard Waveforms (Part 1 of 3)

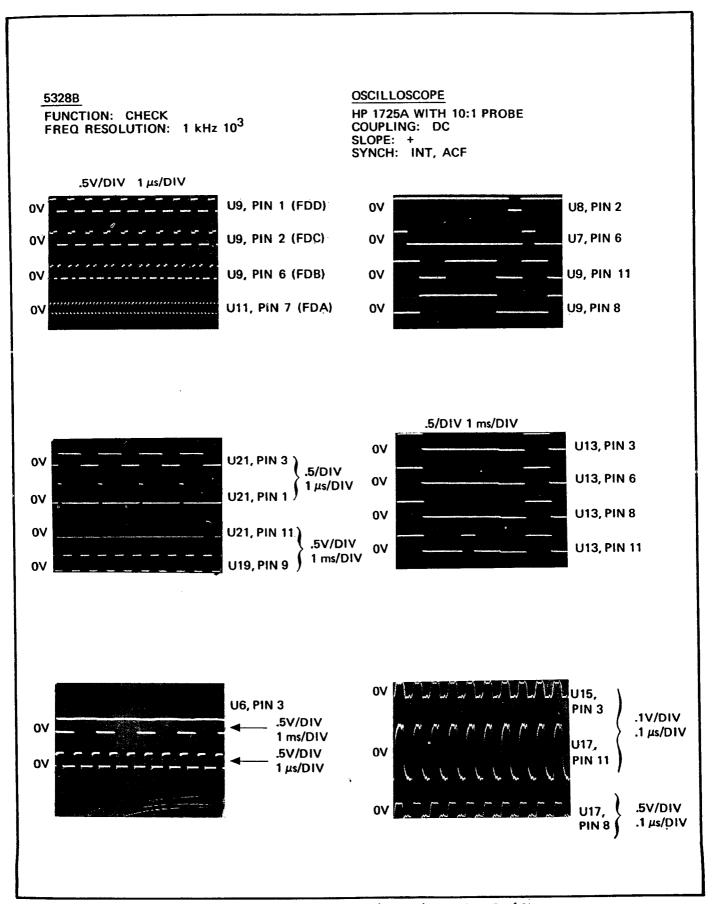
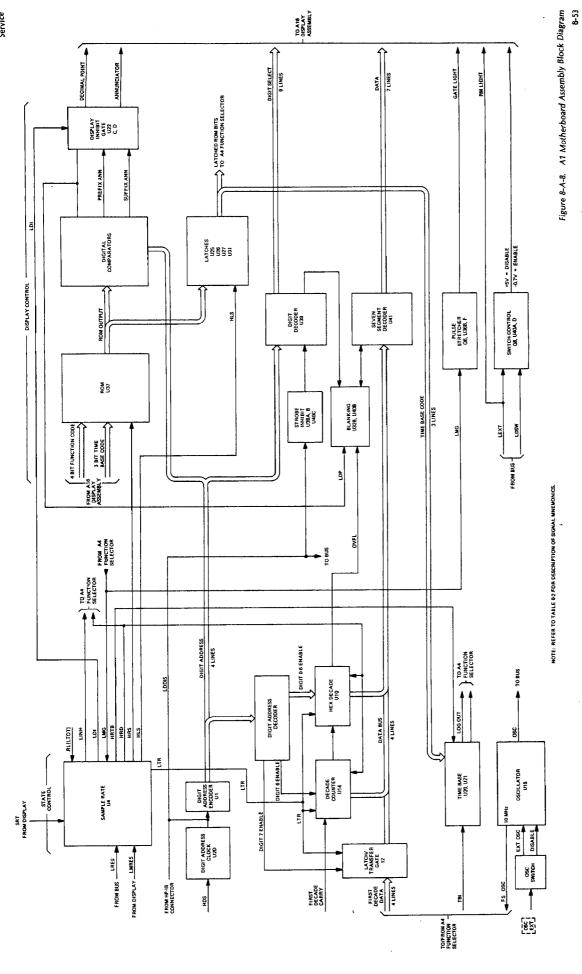
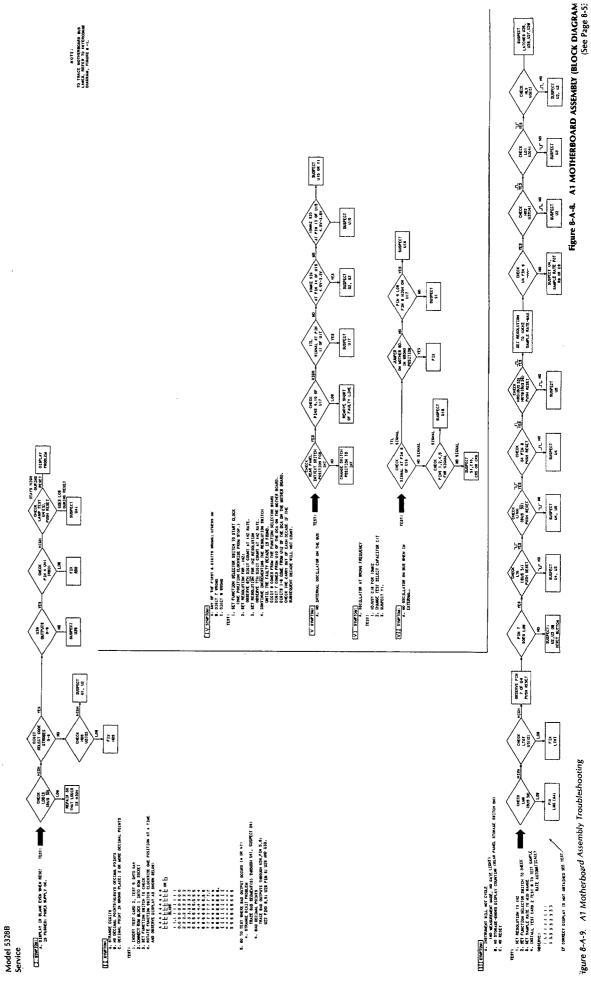


Figure 8-A-7. A1 Motherboard Waveforms (Part 2 of 3)

5328 OSCILLOSCOPE FUNCTION: CHECK HP 1725A WITH 10:1 PROBE FREQ RESOLUTION: 1 kHz 10³ COUPLING: DC SLOPE: + SYNCH: INT, ACF DIGIT BUS **ADDRESS** LINE NO. .5V/DIV 1 ms/DIV 0V 45 Α 0V В 45 0V C 44 **0V** D **BUS** LINE NO. **DATA** .5V/DIV 1 ms/DIV 0V 43 Á 0٧ В 43 **0V** 42 С D 42

Figure 8-A-7. A1 Motherboard Waveforms (Part 3 of 3)





igure 8-A-9. A1 Motherboard Assembly Troubleshooting

A1 MOTHERBOARD 05328-60049 A1 ACTIVE ELEMENTS

C3	A1 ACTIVE ELEMENTS						
C4, C20 C5 C5 C6 C6 C6 C7 C7 C7 C7 C7 C8 C8 C10 C10 C17 C18 C17 C18 C21, C34, C36, C45, C46 C29, C31 C30 C33, C35, C37, C39, C41, C43, C44 C58 C40, C42, C48, C49, C52, C53 C40, C42, C48, C46, C46 CR1, CR3, CR15, CR18 CR3, CR15, CR18 CR3, CR16, CR17, CR19-CR21 CR3 CR4 CR4 CR7 C17 C18 CR1, CR9-CR14, CR16, CR17, CR19-CR21 CR3 CR1, CR9-CR14, C7, CR19-CR21 CR3 CR1, CR9-CR14, C7, CR19-CR21 CR3 CR1, CR16, CR17 CR8 CR5 CR8 CR9 CR1 CR1 CR1, CR16, CR17 CR3 CR1 CR3 CR1, CR18 CR5 CR8 CR8 CR5 CR8 CR8 CR1 CR1 CR3	REFERENCE DESIGNATIONS	PART NUMBER					
C4. C20 C5 C5 C6 C6 C6 C6 C7	C3						
C5	C4 C20	_					
C6		= '					
C7 C8 C8 C10 C10 C10 C17 C18 C17 C18 C17 C18 C18 C17 C18 C19 C21, C34, C36, C45, C46 C22 C29, C31 C30 C30, C35, C37, C39, C41, C43, C44 C38 C40, C42, C48, C49, C52, C53 C47 C50, C51 C7							
C7 C8 0170-0024 0180-0195 150D334X0035A2 0160-0314 663UW10354W2 0160-2264 0121-0060 30432 2/8PF NPO 0180-0210 150D335X0015A2 0160-2255 0180-0230 150D105X0050A2 0160-2055 0180-0230 150D105X0050A2 0160-055 0180-0230 150D105X0050A2 0160-0153 0160-055 0180-0235 150D105X0050A2 0160-055 0180-0230 150D105X0050A2 0160-055 0180-0230 150D105X0050A2 0160-055 0180-0230 150D105X0050A2 0160-055 0180-0255 0180-0255 0180-0255 0180-0355 0180-0155 150D2225X0020A2 0160-4554 0160-4554 0160-4554 0160-4554 0160-4554 0160-4554 0160-3879 0160-055 0180-0155 150D225X0020A2 0160-0153 0160-055 0180-0153 0160-055 0180-0230 150D105X0050A2 0160-055 0180-0230 150D105X0050A2 0160-055 0180-0230 150D105X0050A2 0160-055 0180-0250 0160-055 0180-0255 0180-0255 0180-0255 0180-0255 0180-0255 0180-0255 0180-0255 0180-0255 0180-0250 0160-055 0180-0255 0180-0250 0160-055 0160-055 0180-0250 0160-055 0160-055 0180-025 0160-055 0180-025 0160-055 0180-025 0160-055 0160-055 0160-055	C6						
C8		DM15F401F0300WV1CR					
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C17 C18 C18 C17 C18 C18 C19 C21, C34, C36, C45, C46 C22 C29, C31 C30 C33, C35, C37, C39, C41, C43, C44 C38 C40, C42, C48, C49, C52, C53 C47 C50, C51 CR1, CR9-CR14, CR16, CR17, CR19-CR21 CR7 CR8 CR8 CR7 CR8 CR9 CR8 CR9 CR9 CR8 CR9 CR8 CR9 CR9 CR8 CR9 CR9 CR8 CR9	C10						
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C22 C29, C31 C30 C30 C33, C35, C37, C39, C41, C43, C44 C38 C40, C42, C48, C49, C52, C53 C47 C50, C51 CR1, CR9-CR14, CR16, CR17, CR19-CR21 CR3, CR15, CR6 CR7 CR8 C40, C42, C48, C49, C52, C53 C11 C11 C12 C13 C13 C14 C15 C15 C15 C16 C17 C17 C15 C17 C17 C17 C18 C18 C18 C19	C21 C24 C26 C45 C46	-					
C22 C29, C31 C30 C30 C33, C35, C37, C39, C41, C43, C44 C38 C40, C42, C48, C49, C52, C53 C50, C51 CR1, CR9-CR14, CR16, CR17, CR19-CR21 CR3, CR6 CR7 CR8 CR7 CR8 CR9 CR1 CR1 CR3 CR5 CR6 CR7 CR8 CR5 CR7 CR8 CR5 CR6 CR7 CR8 CR5 CR7 CR8 CR5 CR6 CR7 CR8 CR5 CR7 CR8 CR5 CR6 CR7 CR7 CR8 CR5 CR7 CR8 CR5 CR6 CR7 CR8 CR5 CR7 CR8 CR5 CR6 CR7 CR7 CR8 CR5 CR7 CR8 CR5 CR6 CR7 CR8 CR5 CR7 CR8 CR5 CR6 CR7 CR7 CR8 CR7 CR8 CR7 CR8 CR7 CR8 CR5 CR6 CR7 CR6 CR7 CR6 CR7 CR6 CR7 CR7 CR8 CR6 CR7 CR6 CR6 CR7 CR6 CR6 CR7 CR6 CR6 CR7 CR6 CR6 CR6 CR6 CR6 CR7 CR6	C21, C34, C30, C43, C40	· ·					
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C30 C33, C35, C37, C39, C41, C43, C44 C38 C40, C42, C48, C49, C52, C53 C47 C50, C51 CR1, CR9-CR14, CR16, CR17, CR19-CR21 CR3, CR15, CR8 CR6 CR7 CR8 CR9 CR1 CR1 CR3 CR4 Q6 CR7 CR8 CR5 CR8 CR6 CR7 CR8 CR7 CR8 CR7 CR8 CR8 CR9 CR7 CR8 CR9 CR7 CR8 CR9 CR9 CR9 CR7 CR8 CR9	i - E	Į.					
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C38 C40, C42, C48, C49, C52, C53 C47 C50, C51 CR1, CR9-CR14, CR16, CR17, CR19-CR21 CR3, CR15, CR6 CR7 CR8 L1 Q1, Q4, Q5, Q7-Q9 Q2, Q3 Q6 Q6 U1, U20 U1, U20 U2 1820-0055 SN7490N U2 1820-0175 SN7405N U4 U5 U5 U6, U24, U29, U30 U8, U18, U33-U36 U10 U1, U38 U8, U18, U33-U36 U10							
C40, C42, C48, C49, C52, C53 C47 C50, C51 CR1, CR9-CR14, CR16, CR17, CR19-CR21 CR3, CR15, CR6 CR7 CR8 CR8 CR9 CR1 C11 C11 C12 C13 C14 C14 C15 C15 C15 C15 C15 C16 C15 C16 C17							
C47 C50, C51 C50, C51 C61, CR9-CR14, CR16, CR17, CR19-CR21 CR3, CR15, CR6 CR7 CR8 CR8 CR8 L1 G100-2276 Q1, Q4, Q5, Q7-Q9 Q2, Q3 Q6 Q6 Q10 Q1, U1, U20 Q2 Q3 Q3 Q10 Q2 Q3 Q3 Q10 Q2 Q3 Q3 Q10 Q2 Q3 Q3 Q10 Q2 Q3 Q3 Q4 Q5 Q6 Q6 Q10 Q2							
C47 C50, C51 C750, C51 C750, C51 C750, C51 C750, C51 C750, C766 C750, C766 C750, C76 C77 C77 C787 C787 C788 C787 C788 C787 C788 C790, C79 C79 C70, C70	310, 312, 310, 340, 332, 330						
CR1, CR9-CR14, CR16, CR17, CR19-CR21 CR3, CR15, CR18 CR5, CR6 CR7 CR8 1901-0050 CR8 1901-00535 CR8 1902-3082 L1 9100-2276 Q1, Q4, Q5, Q7-Q9 Q2, Q3 1854-0071 Q2, Q3 1853-0015 2N3640 Q6 1854-0092 2N3563 Q10 1820-0055 SN7490N U2 1820-1056 SN774132N U3 1820-1056 SN7405N U4 105 U5 U6, U24, U29, U30 U7 1820-0513 U8, U18, U33-U36 U10 1820-0511 SN7408N U9, U38 1820-0174 SN7408N U9, U38 1820-0511 SN7408N U9, U38 1820-0511 SN7408N U10 1820-0511 SN7408N 1820-0511 SN7408N 1820-0513 SN20573 U12 1820-0327 U13 U14 1820-1442 SN74LS290N 1820-0537	C47	Programme and the second secon					
CR3, CR15, CR18 CR5, CR6 CR7 CR7 CR8 1901-0050 CR7 1901-0535 CR8 1902-3082 L1 9100-2276 Q1, Q4, Q5, Q7-Q9 Q2, Q3 1853-0015 2N3640 Q6 1854-0092 2N3563 Q10 1853-0036 U1, U20 1820-0055 SN7499NN U2 1820-1056 SN74132N U3 1820-0175 SN7405N U4 1820-1401 U5 1820-0513 U6, U24, U29, U30 1820-0511 SN7408N U8, U18, U33-U36 U10 1820-0511 SN7408N U9, U38 1820-0661 SN7432N 1820-0661 SN7432N 1820-0513 SN20573 U12 1820-0513 SN20573 U12 1820-3237 U13 NOT USED U14 1820-1442 SN74LS290N 1820-0803 MC10105P U16	C50, C51	0160-3879					
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CR7 CR8 1901-0535 CR8 1902-3082 L1 9100-2276 Q1, Q4, Q5, Q7-Q9 Q2, Q3 1853-0015 2N3640 Q6 1853-0036 U1, U20 1820-0055 SN7490N U2 1820-1056 SN74132N U3 1820-1075 SN7405N U4 1820-1075 SN7405N U5 1820-0513 U6, U24, U29, U30 1820-0511 SN7408N U7 1820-0513 U8, U18, U33-U36 U9, U38 1820-0661 SN7432N U10 1820-0513 SN7404N U9, U38 1820-0661 SN7432N U10 1820-0513 SN2408N U10 1820-0513 SN7408N U10 1820-0513 SN7408N U10 1820-0511 SN7408N U10 1820-0513 SN720573 U12 U13 SN20573 U12 U13 SN74LS290N U15 1820-0803 MC10105P U16	CR3, CR15, CR18						
CR8 L1 9100-2276 Q1, Q4, Q5, Q7-Q9 Q2, Q3 1854-0071 R53-0015 R53-0015 R53-0036 Q6 1854-0092 R53563 R53-0036 U1, U20 1820-0055 R57490N R520-0156 R57490N R520-0175 R57405N R520-0513 R520-0513 R520-0513 R520-0513 R520-0513 R520-0511 R520-0513 R520-0537		1					
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Q1, Q4, Q5, Q7-Q9 Q2, Q3 Q6 Q6 Q6 Q6 Q6 Q10 Q10 Q10 Q2 Q10 Q2 Q13563 Q10 Q2 Q10 Q2 Q10 Q2 Q13563 Q10 Q2 Q10 Q2 Q13563 Q10 Q2 Q10 Q2 Q2 Q13563 Q10 Q2 Q2 Q3563 Q10 Q2 Q10 Q2 Q2 Q3563 Q10 Q2 Q10 Q2 Q2 Q3563 Q10 Q2 Q2 Q3563 Q10 Q2 Q2 Q3663 Q2 Q2 Q2 Q3663 Q2 Q2 Q3663 Q2 Q2 Q2 Q3663 Q2 Q2 Q2 Q3663 Q2 Q2 Q2 Q3663 Q2 Q2 Q2 Q3663 Q3663 Q3663 Q3663 Q3664 Q3664 Q36664 Q36664 Q36664 Q366664 Q366664 Q366664 Q366664 Q3666664 Q3666664 Q3666664 Q36666666666		1902-3082					
Q2, Q3 Q6 Q6 Q6 Q10 Q10 Q10 Q10 Q2 Q13563 Q11, U20 Q2 Q33663 Q11, U20 Q3 Q4 Q5 Q7 Q5 Q7 Q5 Q7 Q5 Q7 Q7 Q7	L1	9100-2276					
Q6	•	-					
Q6	Q2, Q3	I					
Q10	06						
Q10 U1, U20 I1820-0055 SN7490N U2 I1820-1056 SN74132N U3 I1820-0175 SN7405N U4 U5 U5 I1820-0513 U6, U24, U29, U30 I1820-0282 SN7486N U7 I1820-0511 SN7408N U8, U18, U33-U36 I1820-0174 SN7404N U9, U38 I1820-0661 SN7432N U10 I1820-0513 SN20573 U12 I1820-0513 SN20573 U12 I1820-3237 U13 U14 I1820-3237 U15 SN74LS290N U15 U16 U16 I1820-0803 MC10105P U16							
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U4		1					
U4 U5 U5 U6, U24, U29, U30 U7 U8, U18, U33-U36 U9, U38 U10 U10 U10 U11 U11 U11 U12 U12 U13 U14 U14 U15 U15 U15 U16 U16 U16 U16 U16 U1820-0537 U19	03	B					
U5	114						
U6, U24, U29, U30 U7 1820-0282 SN7486N 1820-0511 SN7408N 1820-0174 SN7404N U9, U38 1820-0661 SN7432N U10 1820-2316 ID82-4 U11 1820-0513 SN20573 U12 1820-3237 U13 NOT USED U14 1820-0803 MC10105P U16 1820-0537							
U7 1820-0511 SN7408N 1820-0174 SN7404N 199, U38 1820-0661 SN7432N U10 1820-2316 ID82-4 U11 1820-0513 SN20573 U12 SN20573 U13 U14 SN74LS290N U15 1820-0803 MC10105P U16 1820-0537		1					
U8, U18, U33-U36 U9, U38 U10 U10 U10 U11 U12 U12 U13 U14 U14 U15 U15 U16 SN7408N 1820-0174 SN7404N 1820-0661 SN7432N 1820-2316 ID82-4 IB20-0513 SN20573 V12 1820-3237 NOT USED 1820-1442 SN74LS290N 1820-0803 MC10105P 1820-0537	ļ ,. <u>.</u>						
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U10 1820-2316 ID82-4 U11 1820-0513 SN20573 U12 1820-3237 U13 NOT USED U14 1820-1442 SN74LS290N U15 1820-0803 MC10105P U16 1820-0537	U9, U38						
U11 1820-0513 SN20573 U12 1820-3237 U13 1820-3237 U14 1820-1442 SN74LS290N U15 1820-0803 MC10105P U16 1820-0537	1140						
U11 1820-0513 SN20573 U12 1820-3237 U13 NOT USED U14 1820-1442 SN74LS290N U15 1820-0803 MC10105P U16 1820-0537	010						
SN20573 U12 1820-3237 U13 U14 1820-1442 SN74LS290N U15 SN74LS290N 1820-0803 MC10105P U16 1820-0537	U11	1					
U13 NOT USED U14 1820-1442 SN74LS290N U15 1820-0803 MC10105P U16 1820-0537		· _					
U14 1820-1442 SN74LS290N U15 1820-0803 MC10105P U16 1820-0537		1820-3237					
. SN74LS290N U15 1820-0803 MC10105P U16 1820-0537		li .					
U15 1820-0803 MC10105P U16 1820-0537	J U14						
MC10105P U16 1820-0537	U15	l .					
1		1					
SN7413N	U16	1					
		SN7413N					

A1 MOTHERBOARD 05328-60049 A1 ACTIVE ELEMENTS (Continued)

REFERENCE DESIGNATIONS	PART NUMBER
U17	1820-0068
	SN7410N
U19	1820-0077
	SN7474N
U21	1820-2317
	IDB9-TL
U22	1820-0269
	SN7403N
U23	1820-0328
	SN7402N
U25-U27, U31	1820-0301
1100 1100	SN7475N
U28, U32	1820-0538
1107	SN7423N
U37	1818-2252
U39	1820-0214
1140	SN7442N
U40	1820-0054
U41	SN7400N
041	1820-1644
	SN74LS248N

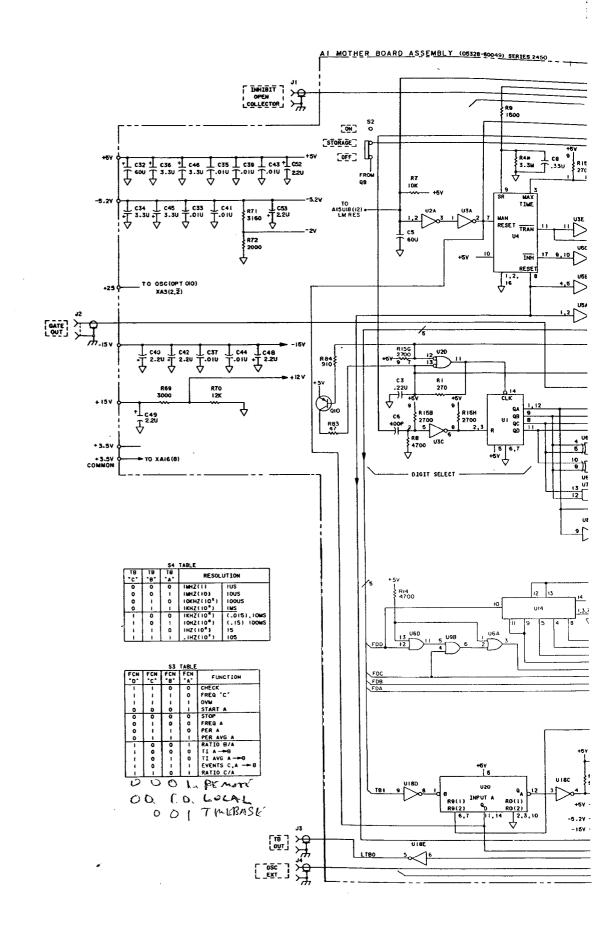
FACTORY SELECTED PART

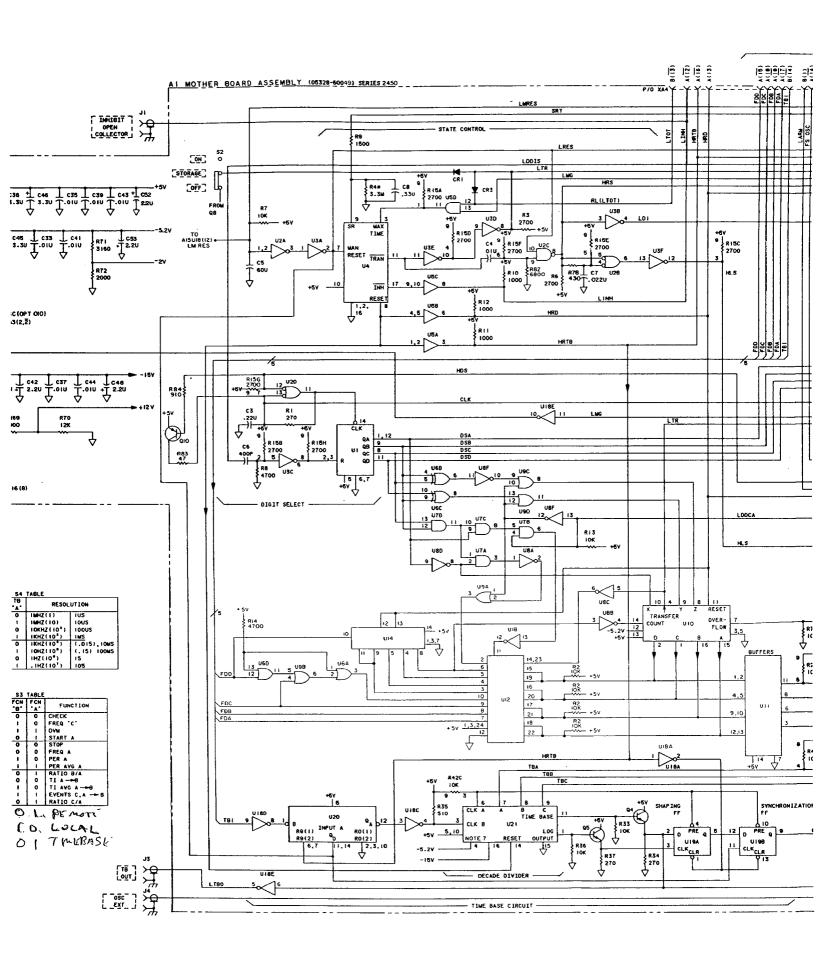
REFERENCE	SELECTED	NORMAL
DESIGNATOR	FOR	VALUE RANGE
C17	10 MHz oscillation adjusted by C18	20 pF ±5 pF

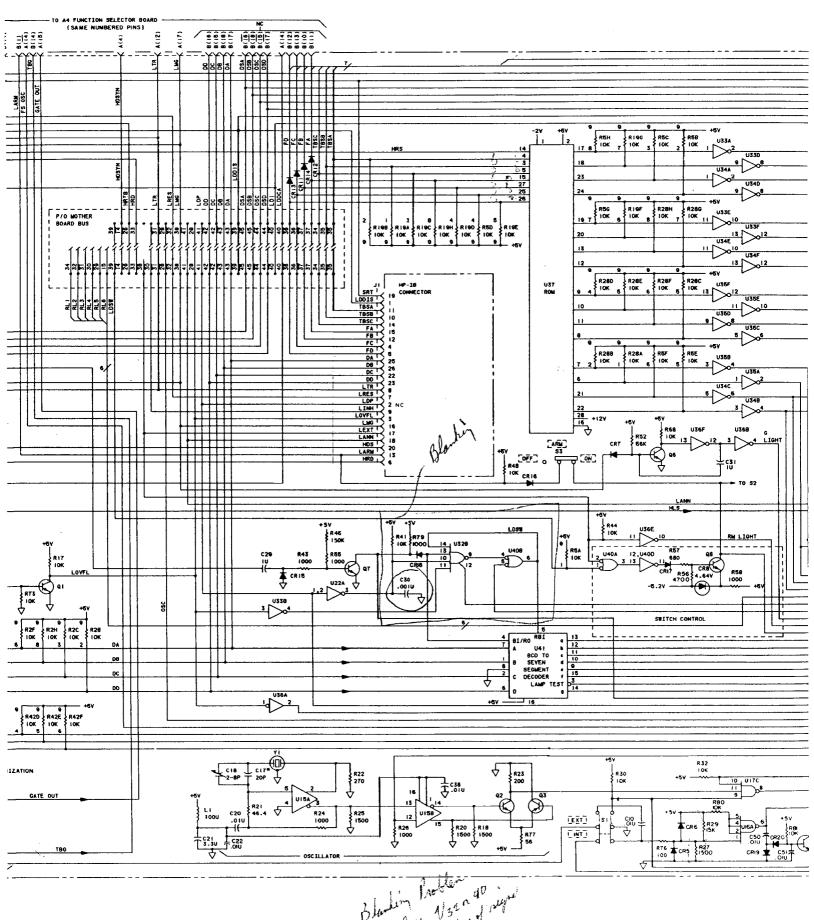
A1 MOTHERBOARD 05328-60049 REFERENCE DESIGNATIONS

REFERENCE DESIGNATIONS	
A1	
C1, C2, NOT ASSIGNED C3-C8 C9 NOT ASSIGNED	
C10 C11-C16 NOT ASSIGNED C17-C18	
C19 NOT ASSIGNED C20-C22	
C23-C28 NOT ASSIGNED C29-C31 C32 NOT ASSIGNED C33-C53	
CR1 CR2, CR4 NOT ASSIGNED CR3, CR5-CR21	
L1	
Q1-Q10	
R1-R15 R16 NOT ASSIGNED R17-R46	
R47 NOT ASSIGNED	
R49-R51 NOT ASSIGNED	
R53, R54 NOT ASSIGNED R55-R84	
S1-S3	
U1-U12 U13 NOT ASSIGNED U14-U41	

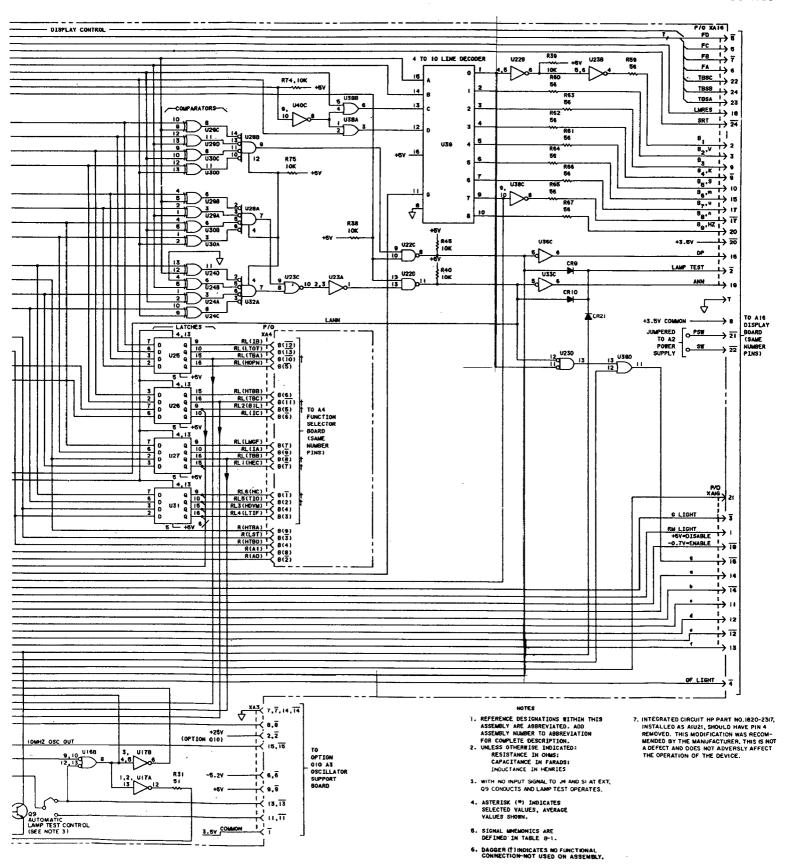
```
STORAGE -
U22 쯽 U23 쯽 U24
R41
 U34
              U35
          R28
         , ∪37
, ↑ R5
R19
```







Blacking Problem 40 C36 V32 r 40 Lynn C36 V32 r por 5



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Figure 8-A-10. A1 Motherboard Assembly Schematic Diagram

Table 8-A-3. 5328B Functional Signals

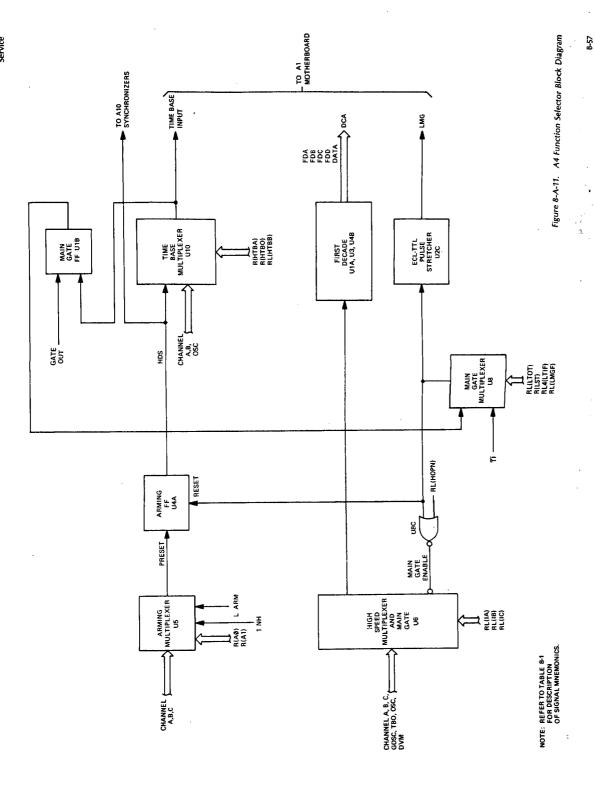
NOTE

N=0→7 (Exponent of 10 on FREQ RESOLUTION, N switch. N=0 is position 1 on switch. All other positions N≠0). CLK = 10 MHz * = Don't care

	<u> </u>			* = Don't care						
Function Switch	Displayed Number FREQ • TIME (Hz) (Seconds)	Signal (Output IF N=0		Signal to 1 (Outpu IF N=0	t A4U6)	Armi (Output Norm A	A4U5)	Main (Input IF N≔0	A4U6)	Gate (Options 030 and 031 (Input A8U4)
FREQ A	A ● 10(N+1) CLK	CLK	CLK	A	A	A	В	MGFF	MGFF	*
PER A	$\frac{\text{CLK}}{10^{\text{N}}} \bullet \text{PER A}$	*	CLK	GOSC	ТВО	Free	В	Open	TI	*
PER A (Option 040)	100 MHz 10 ^N ● PER A	* (N=0 or 1)	CLK (N≠0 or 1)	GOSC (IF N=	TBO 1, CLK)	Free	В	Open	RI	*
PER AVG A	CLK • 10 ^N PER A	*	Α	GOSC	CLK	Free	В	Open	MGFF	•
PER AVG A (Option 040 & 041)	100 MHz • 10 ^N PER A	*	A	GOSC	100 MHz	Free	В	Open	MGFF	*
ті А⊸в	CLK 10 ^N • TI A−B	*	CLK	GOSC	ТВО	Free	CA	Open	TI	•
TI A→B (Option 040 & 041)	100 MHz 10 ^N • TI A→B	* (N=0 or 1)	CLK (N≠0 or 1)	GOSC (IF N=	TBO 1, CLK)	Free	CA	Open	TI	*
TI AVG A→B	(CLK • 10 ^N) • TI A−B	*	В	GOSC	GOSC	Free	CA	Open	MGFF	•
TI AVG A-B (Opt. 040 & 041)	(100 MHz • 10 ^N) • Tt A−B	*	В	GOSC	GOSC	Free	CA	Open	MGFF	•
FREQ C (Opt. 030 & 031)	C ◆ 10 ^(N+1) CLK	CLK	CLK	<u>C</u> 10	<u>C</u> 10	CA	В	Open	Open	MGFF
DVM (Option 020, 021)	DVM • 10(N+1) CLK	CLK	CLK	DVM	DVM	Free	8	MGFF	MGFF	•
tDVM, A→B	(In DVM, A—B TB Switch ignored)	*	*	DVM	DVM	Free	CA	TI	TI	*
EVENTS C, A-B	(EVENTS C A→B TB Switch ignored)	*	•	<u>C</u> 10	<u>C</u>	Free	CA	Open	Open	Tŧ
START A, STOP	A TI START 10N TO STOP	* 3	Α .	A	тво	Free	Free	LStart	Open	*
RATIO B/A	B • 10 ^N /A	††*	A	В	В	Free	CA	.TI	MGFF	*
RATIO C/A (Opt. 030 & 031)	C • 10 ^N / _A	*	A	<u>C</u>	<u>C</u>	Free	В	Open	Open	TI IF N=0 MGFF IF N≠0
†DVM/A	DVM ● $\frac{10^{N}}{A}$	*	A	DVM	DVM	Free	В	TI	MGFF	*
†START CLOCK	CLK • TI START	*	CLK	* (тво	Free	Free	Open	Open	•
CHECK	CLK • 10(N+1) CLK	CTK	CLK	CLK	CLK	free	В	MGFF	MGFF	*

NOTES

t = Phantom Function (see 5328A Users Manual) tt = ROM makes "A" into period = gate time CA = CARM



A4 FUNCTION SELECTOR TROUBLE SHOOTING FLOWCHART

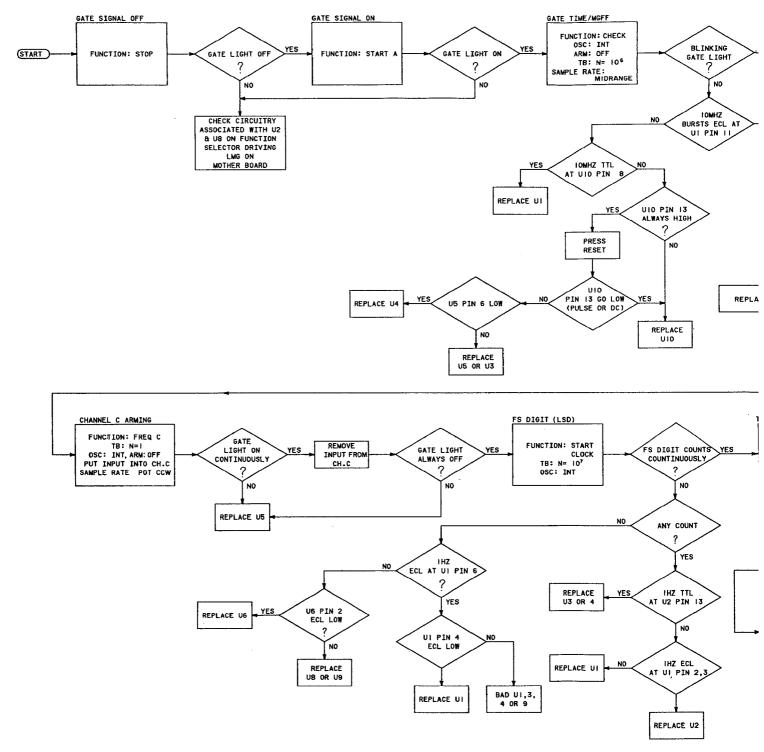
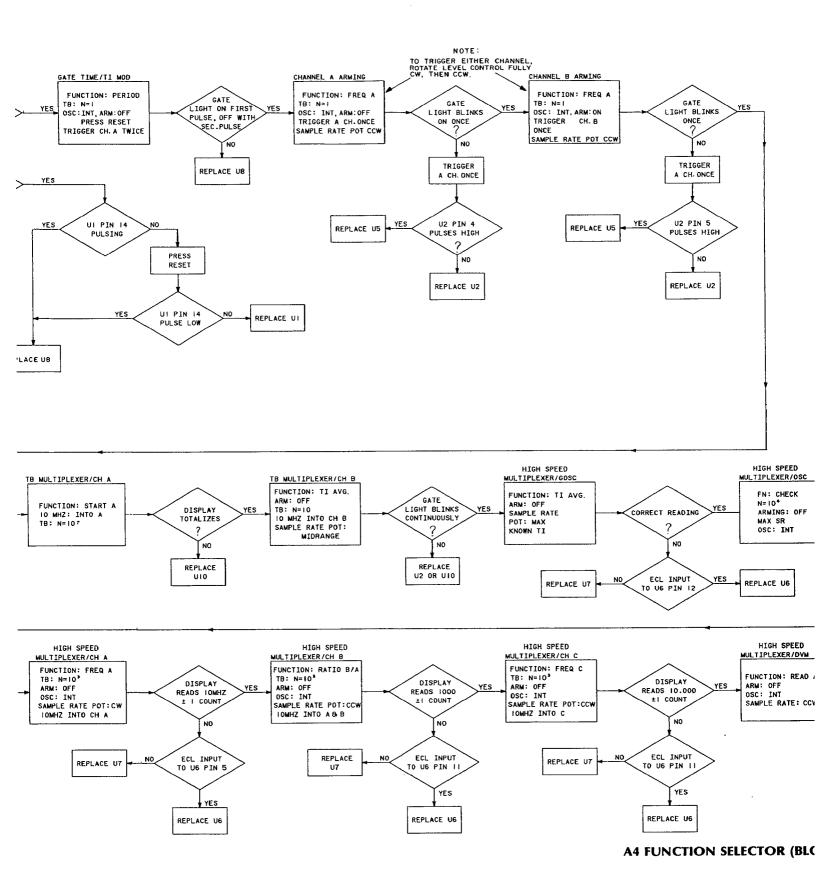


Figure 8-A-12. A4 Function Selector Troubleshooting



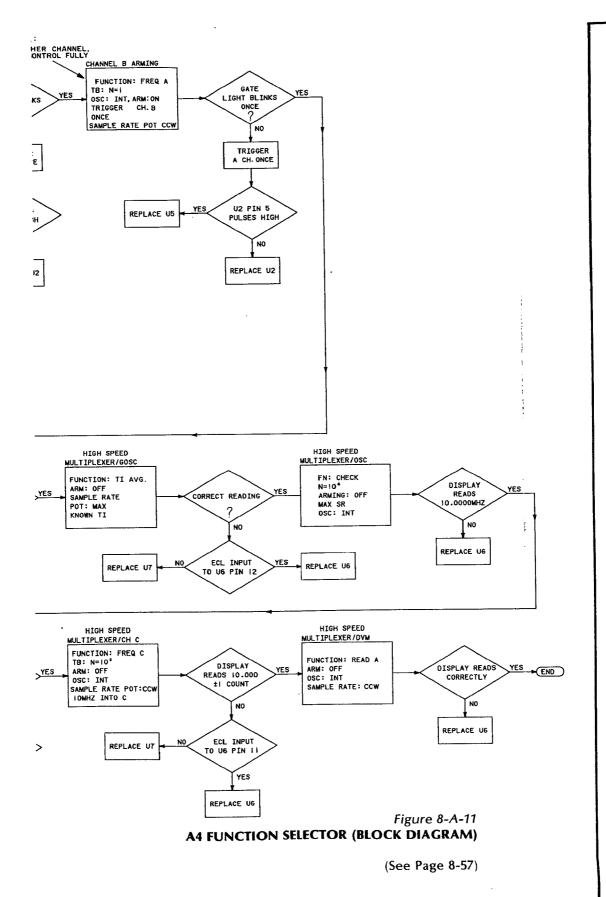


Figure 8-A-12
A4 FUNCTION SELECTOR (TROUBLESHOOTING)

(See Page 8-58)

†DENOTES

*SIGNAL SC

P1A PINS

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TI

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LT

HF

LC

G/

Gt

FC

- LN

REFERENCE DESIGNATIONS

A4

C1 NOT ASSIGNED C2-C16 Q1-Q3 R1-R46 U1-U10

A4 ACTIVE ELEMENTS

REFERENCE DESIGNATIONS	PART NUMBERS
Q1-Q3	1854-0215 2N3904
U1	1820-1225 MC10231P
U2	1820-1052 MC10125L
U3, U4	1820-0629 SN74S112N
U5	1820-0622 SN74151N
U6	1820-0829 MC10164L
U7	1820-0809 MC10115P
U8	1820-0802 MC10102P
U9	1820-0328 SN7402N
U10	1820-0074 SN7454N

P1Á PINS

P1B PINS

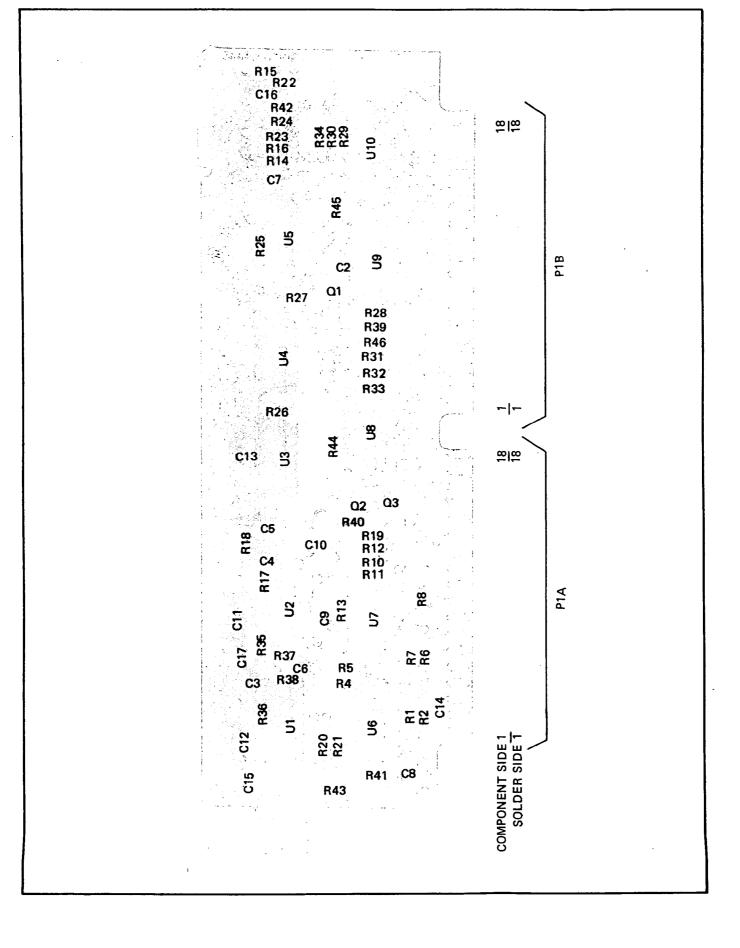
		=										
1	_	DVM†		1	_	DVM	1		LARM	1	_	RL6(HC)†
2	_	-15V†		2		-5.2V	2	_	RL5(TIO)†	2		R(AØ)
3	_	+5V		3	_	+15V†	3	_	RL4(LTIF)	3	_	R(LST)
4	_	HDS*		4	_	GND	4		R(HTBO)	4	_	RL3(HDVM)†
5	_	<u>C</u>		5	_	С	5		RL2(BIL)†	5	_	RL(HOPN)
6	_	C ARM		6		MG*	6	_	RL(HTBB)	6	_	RL(IC)
7	_	MG*		7		Α	7		RL(LMGF)	7	_	RL1(HEC)†
8	_	Ā		8		B	8	_	R(A1)	8		RL(TBB)†
9	_	GND	•	9		В	9	_	R(HTBA)	9	_	RL(IA)
10		Tiţ		10	_	TI	10		FB†	10	_	RL(TBA)†
11	_	GOSC		11		GOSC	11	_	FA†	11	_	RL(TBC)†
12	_	LTR†		12		LINH	12		FD†	12	—	RL(IB)
13	_	HRD		13	_	GND ,	13	_	FC†	13		RL(LTOT)
14	_	LDDCA†		14	_	FS OSC	14		TBO	14	_	TBI*
15		GATE OUT		15	_	FDD*	15	_	DATA C†	15		DS C†
16	_	GND		16		HRTB	16		DATA D†	16	_	DS A†
17		LMG*		17	_	FDA*	17	_	DATA A†	17	_	DS D†
18	_	FDC*	,	18	_	FDB*	18	_	DATA B†	18	_	DS B†

de en

*SIGNAL SOURCE

†DENOTES NO FUNCTIONAL CONNECTION — NOT USED ON ASSEMBLY

+END



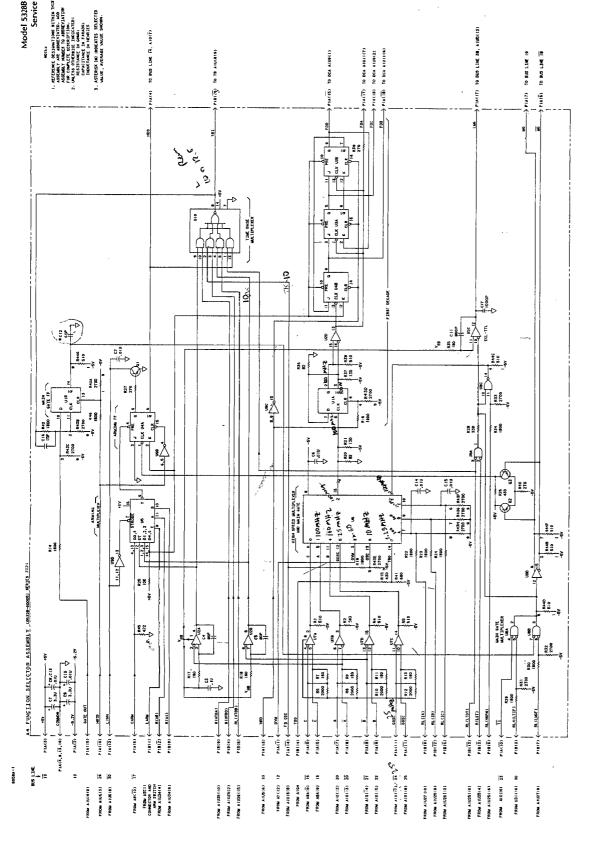


Figure 8-A-13. A4 Function Selector Assembly Schematic Diagram

REFERENCE DESIGNATIONS

A16	
C1	Γ
CR1-CR6	ĺ
DS1-DS9	l
DS10-DS20	
P1	l
Q1-Q22	
R1-R10	
S1-S4	

A16 ACTIVE ELEMENT

REFERENCE DESIGNATIONS	PART NUMBERS
CR1, CR2, CR3, CR5, CR6	1901-0040
CR4	1910-0016
DS1-DS9	1990-0452
DS10-DS20	1990-0404
Q1-Q9	1853-0326
Q10-Q22	1854-0492 2N3643

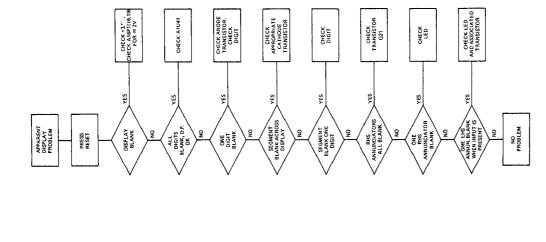
P1 PINS

```
T 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
                                          GND
          RM LIGHT
                                          LAMP TEST
 2
          B1
                                          G LIGHT
 3
          B2
 4
          LSWL
                                          OF LIGHT
                                          FD
 5
          FC
                                          NC
          FΑ
                                          FB
          NC
 7
          GND
                                          NC
 8
                                          B4
 9
          В3
                                          NC
10
          B5
                                          NC
11
          С
12
          d
                                          е
                                          NC
13
          f
                                          b
14
                                          NC
15
          В6
16
          DP
                                           g
                                          B8
17
          В7
18
          LMRES
                                          NC
                                          +5V = DISABLE, -0.7V = ENABLE
          ANN
19
                                          +3.5V
20
          B9
21
          LEXT NC OR CLK
                                             POWER SWITCH
22
          TBS C
23
          TBS A
                                           +5V
24
          TBS C
                                           SRT
```

LHS ANNUNCIATORS DS17, DS18, DS19 CATHODE DRIVER TRANSISTORS Q10-Q12

FROM A1 MOTHER BOARD

Ω. M. A. Figure 8-A-14. A16 Display Assembly Block Diagram and Troubleshooting



RHS ANNUNCIATORS DS12-DS16

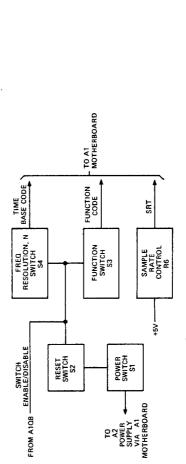
ANODE DRIVER TRANSISTORS Q1-Q9

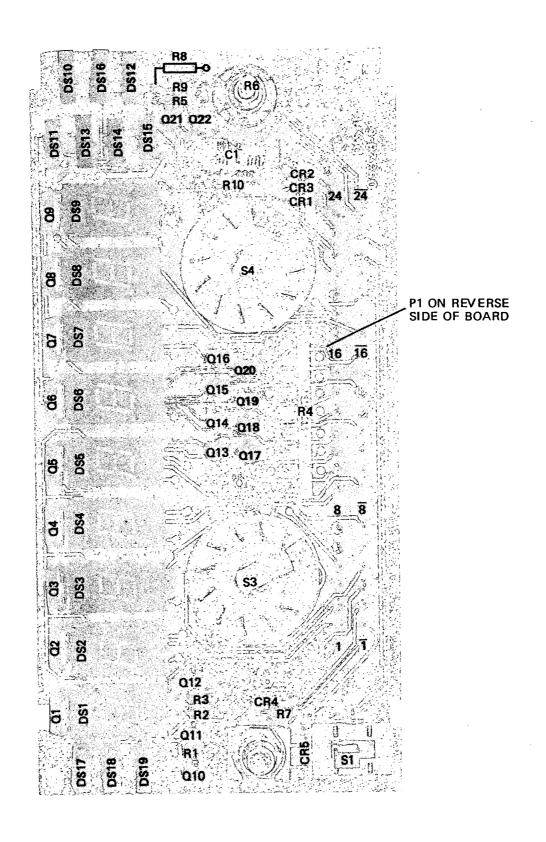
FROM A1U39
(DIGIT DECODER)

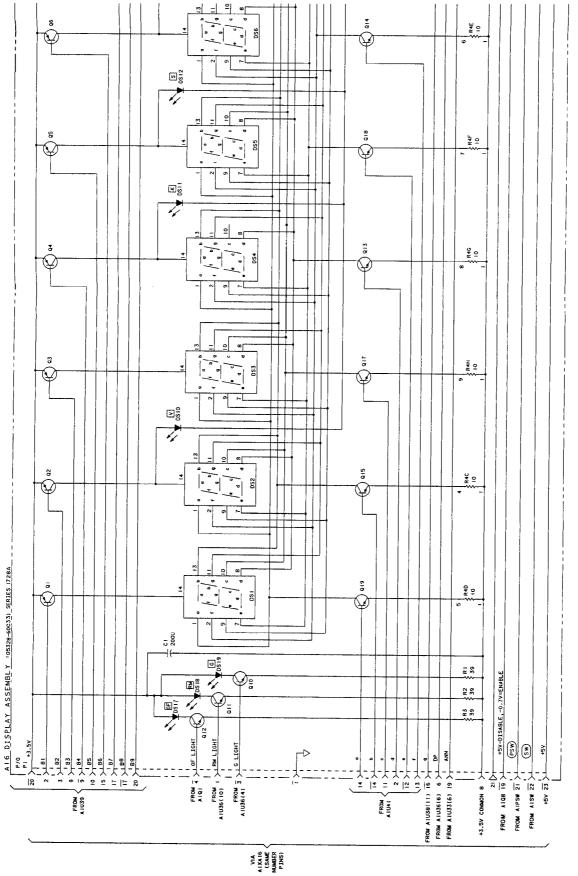
DISPLAY DIGITS DS1-DS8 (DS9 OPT 30) ANNUNCIATOR CATHODE TRANSISTOR 021

> CATHODE DRIVER TRANSISTORS 013-020

INPUTS FROM ATU41 (BCD TO SEVEN SEGMENT DECODER)







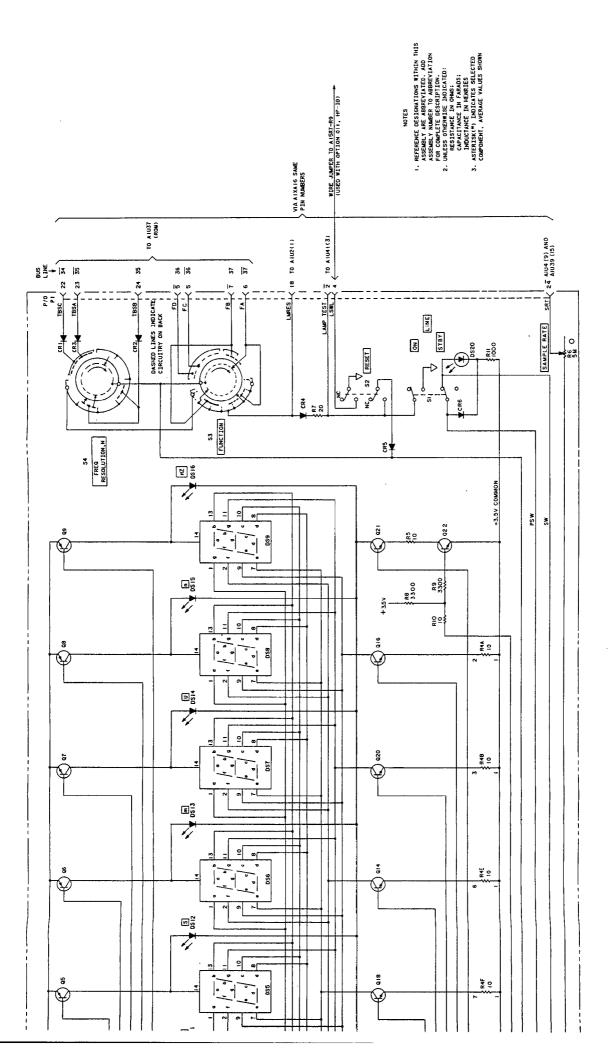
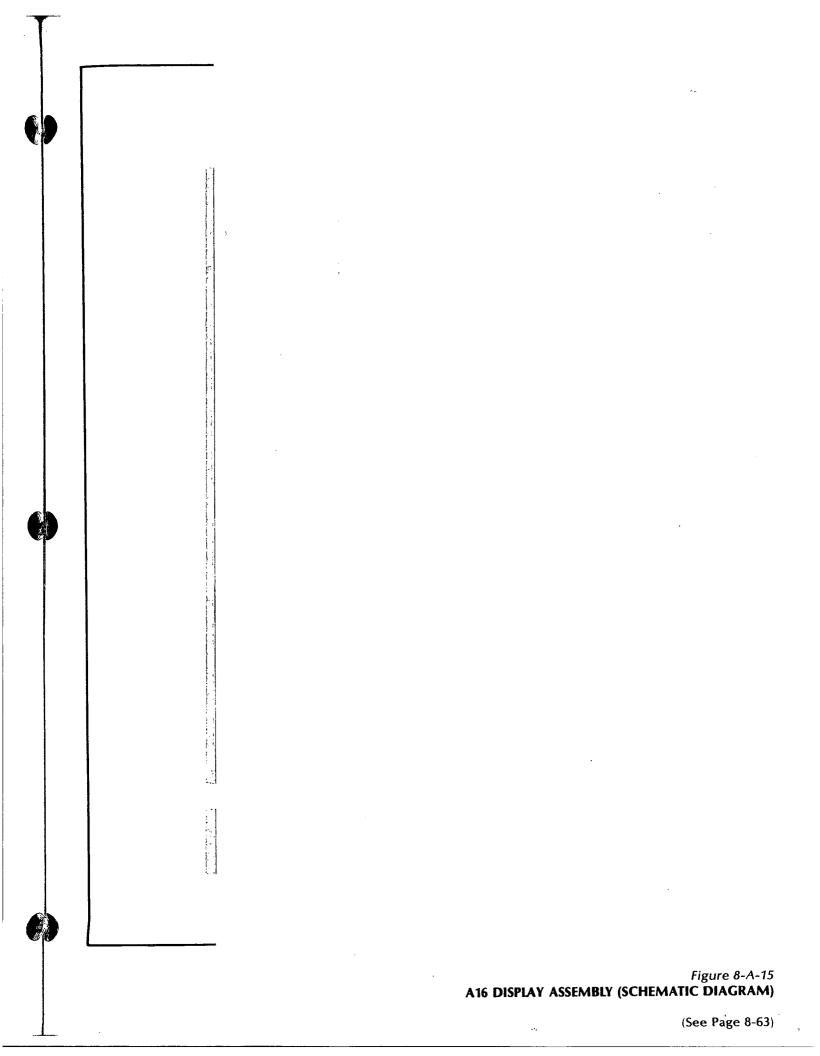
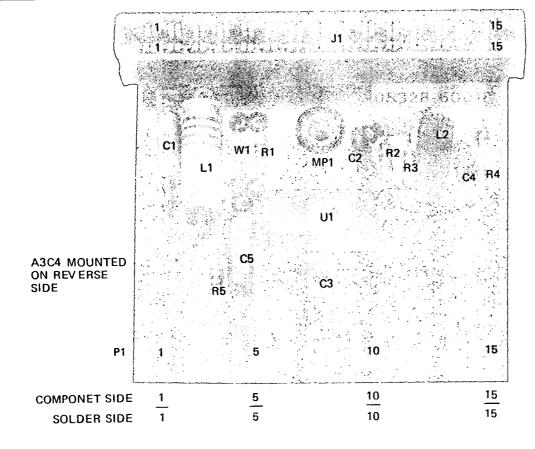
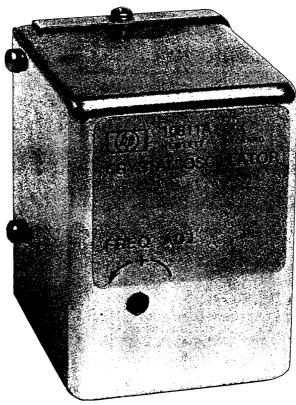


Figure 8-A-15. A16 Display Assembly Schematic Diagram







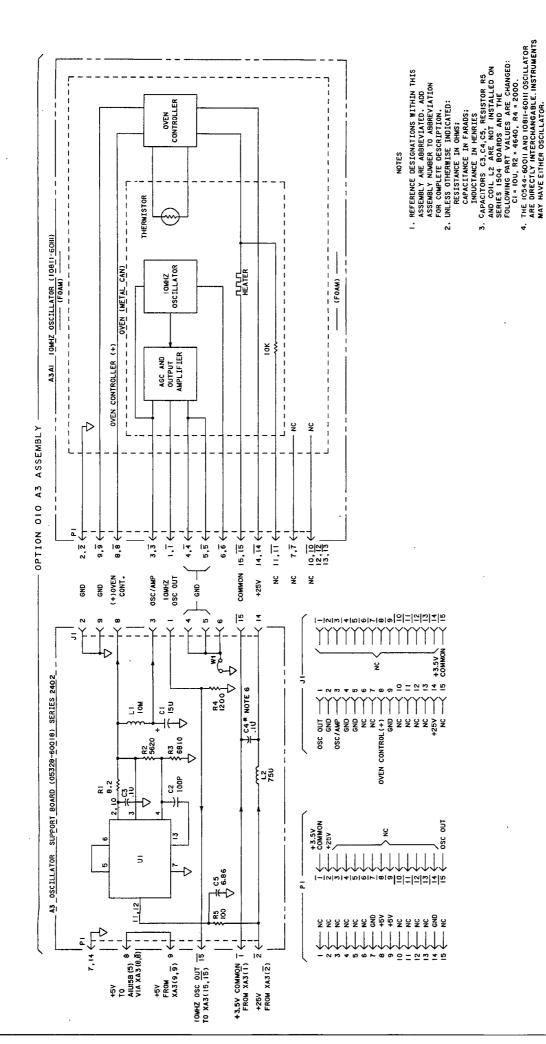


Figure 8-A-16. A3 Oscillator Support Board Schematic Diagram (Part of Option 010)

5. THE 10811-6011 OSCILLATOR IS FIELD SENVEZBELE. REPAIRS MAY BE ACCOMPLISHED BY REFERRING TO THE 10811AB OPERATING AND SERVICE MANUAL 10811-90002).

6. INDICATES FACTORY SELECTED VALUE.

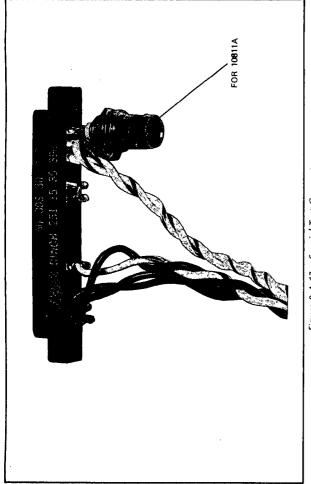


Figure 8-A-17. Special Test Connector

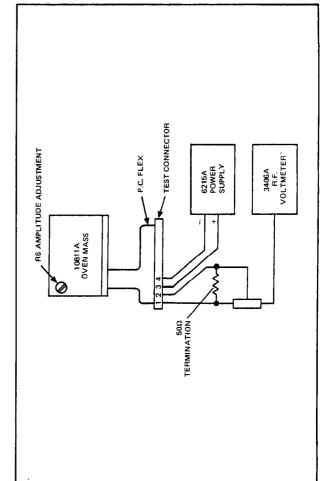
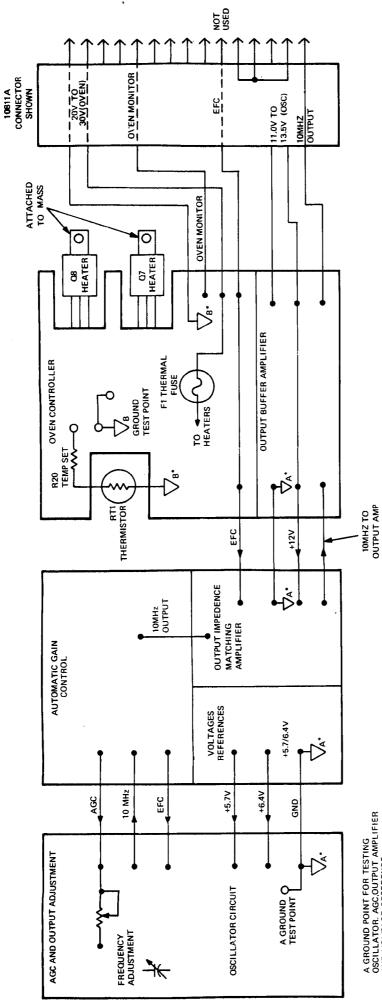


Figure 8-A-18. Output Amplitude Adjustment Setup

9-66

Figure 8-A-16 A3 OSCILLATOR SUPPORT BOARD (SCHEMATIC DIAGRAM) (See Page 8-65)



A GROUND POINT FOR TESTING OSCILLATOR. AGC.OUTPUT AMPLIFIER AND VOLTAGE REFERENCE

B GROUND POINT FOR TESTING OVEN CONTROLLER B GROUND POINT FOR TESTING OVEN CONTROLLER

* A AND B GROUNDS MAY BE TIED TOGETHER OR FLOATED AT DIFFERENT POTENTIALS EXTERNAL TO THE OSCILLATOR.

Figure 8-A-19. Oven Oscillator Overall Block Diagram

Table 8-A-4. Oven Circuit Voltages*

VOLTAGE POINT	OVEN AT OPERATING TEMP.	OVEN COLD (JUST AFTER TURN-ON)	RT1 GROUNDED
Q6B	1.6	2.	.25
Q6C	11.4	11.4	11.4
Q6E	1.	1.3	0
Q7B	12.5	12.7	11.9
Q7C	20.	20.	20.
Q7E	11.4	11.4	11.4
Q8B	1.	1.3	0
Q8C	11.4	11.4	11.4
Q8E	.07	.23	0
U1 Pin 1	8.9	1.8	8.9
U1 Pin 2	.07	.23	0
U1 Pin 3	.2	.23	.2
U1 Pin 5	4.	4.1	3.8
U1 Pin 6	4.	4.1	3.8
U1 Pin 7	3.3	4.8	1.5
U2 Pin 2	10.	10.	10.
U3 Pin 6	3.5	19.0	.5

^{*}Voltage readings taken with oven supply voltage of 20V dc and insulating foam and cover removed. Voltages are approximate and will vary slightly from unit-to-unit.

Table 8-A-5. Oscillator Section Normal Voltages

VOLTAGE POINT	NORMAL V	OLTAGES '	REMARKS		
VOLIAGETORY	AC (p-p)	DC			
C3/R3	1 to 4		Note 8		
CR5/C13	-	-1.5	Notes 4 and 7		
CR3(C)	0	6.3	Note 4		
Q1(B)	1	.75	Note 8		
Q1(C)	0	5.5	Note 4		
Q1(E)	.9	.03	Note 8		
Q2(B)	0	2.7	Note 4		
Q2(C)	2.7	5.6	Note 8		
Q2(E)	.06	2	Notes 4 and 5		
Q3(B)	2.7	5.6	Note 8		
Q3(C)	0	11.8	Note 4		
Q3(E)	2.4	4.9	Notes 4 and 6		
Q4(B)	0	6.3	Note 4		
Q4(C)	0	10.3	Note 4		
Q4(E)	0	5.6	Note 4		
Q5(B)	2.7	3.1	Note 8		
Q5(C)	0	11.8	Notes 8 and 9		
Q5(E)	2.8	2.6	Note 8		
Q9(B)	2.8	2.8	Note 8, 9		
Q9(C)	5.1	11.8	Note 8, 9		
Q9(E)	2.5	1.9	Note 8		

NOTES:

- 1. All voltages taken with 12V oscillator supply.
- 2. Voltages are approximate and will vary slightly from unit-to-unit.
- 3. All ac voltages are sine waves except Q2(E) and Q3(E).
- 4. This dc voltage may be measured with a standard dc voltmeter. All other voltages should be measured with an oscilloscope and high impedance probe to minimize circuit loading.
- 5. Waveform is $\underbrace{\qquad}_{\perp}$.06V.
- Waveform is slightly flattened on the bottom.
- This is the AGC voltage. Value shown is nominal with oscillator operating. If the oscillator is not
- oscillating, the AGC voltage will be ~+2.5V.

 Measure both ac and dc voltages with an oscilloscope and a high impedance probe to minimize circuit loading.
- 9. AC voltage at Q9(C) measured with 50-ohm load on the output.

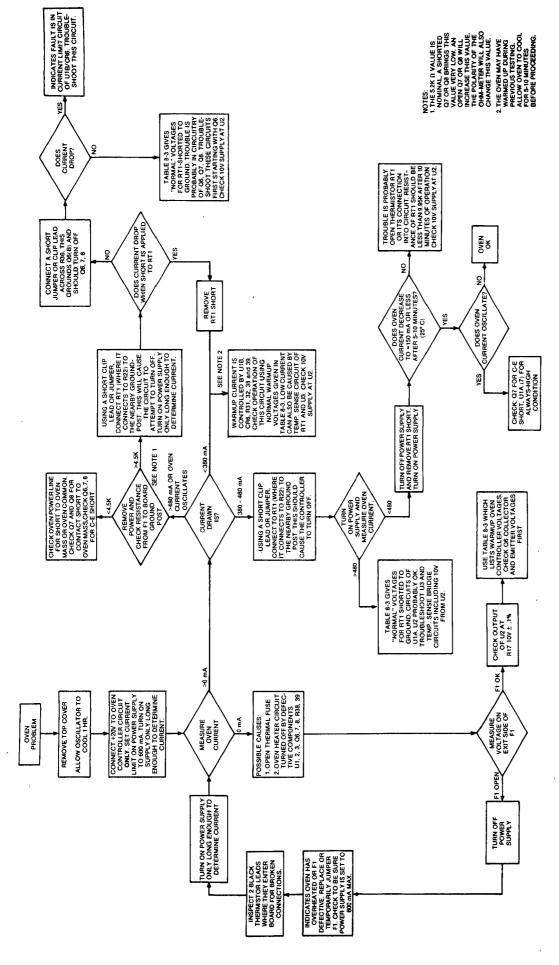


Figure 8-A-21. Oven Controller Troubleshooting T.

OVEN CONTI

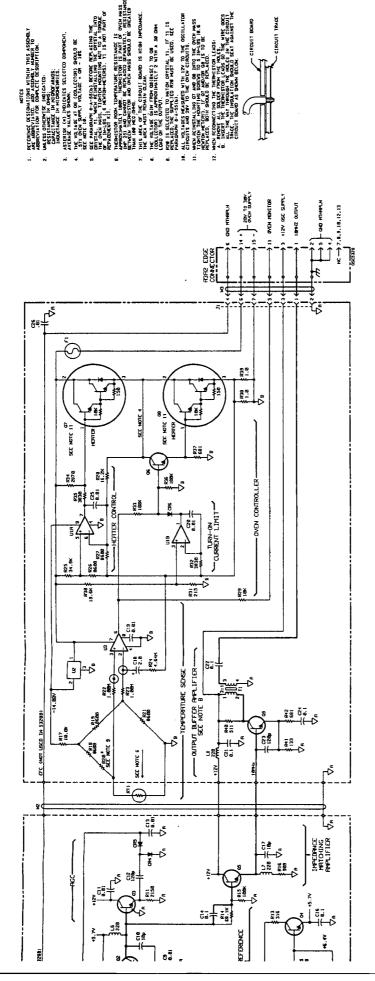
Figure 8-A-22. Oven Oscillator Assembly Schematic Diagram

(See Page 8-70)

Figure 8-A-22 10811A OVEN OSCILLATOR ASSEMBLY (SCHEMATIC DIAGRAM)

(See Page 8-69)

Figure 8-A-21
OVEN CONTROLLER TROUBLESHOOTING TREE



8-BA-1. INPUT OPTIONS SECTION

The Model 5328B's Input Options Section provides the main interface between the instrument and the outside environment.

The Input Options Section has one standard module — the Programmable Input Module — which is found in all instruments. It may also include an additional one or two optional modules that extend the Model 5328B's capabilities. One of the optional modules is the Digital Voltmeter Module; the other optional module is the "C" Channel Module which extends the instrument's frequency range to 1300 MHz (1.3 GHz).

Each of the three modules operates separately from any of the others that might be installed, except that the Digital Voltmeter Module can be used to measure the trigger levels set for the "A" and "B" channels of the Programmable Input Module.

All three of these modules are described later in this manual section.

NOTE

In order to help you know where you are in this part ("8-B-") of Section 8, the numbering scheme described in the beginning of Section 8 is modified as follows:

After the introductory material:

- Information about the Programmable Input Module is presented with headings, figure numbers and table numbers that begin "8-BA-".
- Information about the Digital Voltmeter Module is presented with headings, figure numbers and table numbers that begin "8-BB-".
- Information about the Channel "C" Module is presented with headings, figure numbers and table numbers that begin "8-BC-".

8-BA-2. Programmable Input Module (A10, A11, A12, A14, A19)

8-BA-3. INTRODUCTION

The Channel "A" and Channel "B" inputs have matched .2 ns) 100 MHz amplifiers with:

- · ac or dc coupling
- ±2.5V trigger level range
- three-position ("X1", "X10", "X20") attenuators
- 50-ohm or 1-Megohm input impedance, switch selectable
- trigger slope controls
- trigger indicators
- · Channel "A" high-speed marker
- time interval "A"-to-"B" high-speed marker
- variable delay

The circuitry for the Model 5328B's Channel "A" and "B" is contained in the Programmable Input Module. This module contains:

- the main input amplifiers
- Schmitt triggers
- · high-speed synchronizers for complex timing measurements.

The Model 5328B's Programmable Input Module uses a phase-locked multiplier to provide a basic clock rate of 100 MHz, which results in a basic timing unit of 10 ns. In the Time Interval Averaging FUNCTION, the multiplier, when commanded by the main-controller ROM, phase modulates the 100 MHz clock with band-limited noise to prevent the synchronous lockup problem that could otherwise be associated with this measurement.

8-BA-4. Principles of Operation

8-BA-5. GENERAL

The Model 5328B's Programmable Input Module consists of the boards listed in Table 8-BA-1.

Table 8-BA-1. Circuit Diagrams

Assy Ref Des	Description	Circuit Diagram
A10	Synchronizer Assembly	8-BA-9
A11	DAC Assembly	8-BA-11
A12	Amplifier Assembly	8-BA-8
A14	Multiplier Assembly	8-BA-10
A19	Switch Control Assembly	8-BA-7

For the descriptions that follow, refer to the overall block diagram (Figure 8-BA-1). Also, when necessary, refer to the block or circuit diagrams for the individual boards (see Table 8-BA-1 for listing).

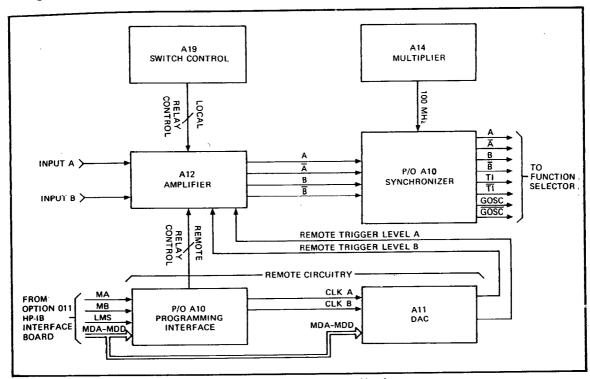


Figure 8-BA-1. Input Module — Block Diagram

In the "local" mode, the Switch Control Board (A19) generates TTL levels that control the Signal Conditioning relays on the Amplifier Board (A12). These levels allow front-panel control of the "A" and "B" channel input signal conditioning. The Switch Control Board accepts inverted "A" and "B" channel signals from the Amplifier board. These signals are routed through pulse stretcher and driver circuits to the "A" and "B" channel trigger LEDs, located on the Switch Control Board. The inverted signals are also translated from ECL to TTL levels and supplied to the "A" and "B" MARKER OUTPUTs.

Input circuitry for the "A" and "B" channels is on the Amplifier Board (A12) and part of the Synchronizer Board (A10). The Amplifier Board contains the 100 MHz "A" and "B" channels, with signal-conditioning "SLOPE", "AC/DC", "ATTENUATORS", "SEP/COM", "1M/50-OHM" controls, amplifiers, and Schmitt triggers. Signal conditioning circuitry is controlled by relays K1 through K11 synchronizing circuitry for PERIOD and Time Interval-type measurements. The "A", "A", "B", "B", "TI", "TI", "GOSC", and "GOSC" outputs from the Synchronizer Board are routed to the Function Selector (A4).

Multiplier Board A14 generates a 100 MHz differential ECL clock signal which is phase-locked to the internal 10 MHz reference oscillator. This allows the Model 5328B to accomplish 10 ns time-interval resolution. A random phase jitter circuit is also included on the Multiplier Board, to introduce phase jitter into the 100 MHz clock for time interval average measurements to be made at the frequency of the clock or at a harmonic of the clock's frequency.

The Synchronizer Board's programming interface section allows remote control of all input signal conditioning relays. The DAC board (A11) contains two identical Digital-to-Analog Converters—one for each input channel, "A" and "B", allowing remote control of trigger levels. The outputs of these DACs are sent to a relay on the Amplifier board. In "REMOTE", the relay connects these DAC levels to the Schmitt trigger on the A12 board.

There are two modes of accepting remote commands; the "non-DAC" and the "DAC control" modes.

When the Model 5328B is placed in REMOTE operation, front-panel switch control is disabled. At the same time, the programming interface takes control of the input signal conditioning relays.

In the "non-DAC" control mode, the interface accepts and decodes serial data bytes, stores the information in latches, and controls signal conditioning via the latched outputs.

When the interface receives a data byte for control of trigger levels, it goes into the "DAC control" mode. This is a result of the interface receiving a "+" or "-" on its input data lines. Once in the DAC control mode, the programming interface latches disregard the information at their inputs. At the same time, the information on the input data lines (MDA-MDD) is accepted by the DAC board (A11).

The DAC board shifts the polarity indicator and the three following numerical bytes of information into its shift registers. Following the polarity indicator and the three numerical data bytes, an asterisk ("*") appears on the MDA-MDD lines (see Table 8-BA-2 for proper format). The asterisk causes the programming interface to revert back to the non-DAC control mode. In this mode, the DAC board stops accepting data, and the programming interface latches again accept the input data.

The DAC board processes the four serial data bytes and produces one parallel BCD output. The BCD output provides the information for generating a square-wave train by using a series of rate multipliers. The square-wave train has an average duty cycle proportional to the input code supplied to the rate multipliers. This square-wave train switches on a precision current source that feeds a voltage averager to produce a dc output.

8-BA-6. Switch Control Board (A19)

In "local" mode, -0.7V is applied to switches S1 through S8. This voltage allows the switches to control their respective functions by supplying an active "low" available at each switch. In a "closed" switch position, the -0.7V will forward-bias the associated diode, pulling its anode "low", and will cause a "low" to be sent to the Amplifier Board (A12) through J3.

In "remote" mode, +5V is applied to switches S1 through S8; this allows only the output lines, transmitted through J3, to be "high". When the switch is closed, the associated diode is reverse-biased and the line remains "high".

Trigger LEDs DS1 and DS2 are driven by the inverted "A" and "B" outputs of the Schmitt trigger (A12U4). These signals enter pins "14" and "10" of ECL-to-TTL translator A19U1. Feedback capacitors C8 and C9 stretch the 5 ns ECL pulse to approximately a 25 ms TTL output pulse. This 25 ms pulse is long enough to be seen via lighting of the associated LED. Since this pulse stretcher is dc-coupled to the Schmitt trigger, it functions like a logic probe with adjustable threshold voltage. When Channel "A"'s input is higher than its trigger level setting, its trigger LED is lighted. When the input signal is lower than its trigger level setting, the trigger LED is off. Whenever the input signal crosses the trigger threshold, the trigger LED flashes "on" or "off", depending on the polarity of the input signal.

The 5 ns inverted "A" and "B" outputs are also applied to U1's pins "2" and "6", respectively. The signals are translated from ECL to TTL levels and connected to the MARKER OUTPUTs.

8-BA-7. Amplifier Board (A12)

Since the "A" and "B" channels have identical circuitry, so only the "A" Channel circuitry will be discussed.

Codes shown in bold face are start-up conditions. These conditions are set by the code "P", Remote Program Initialize, or by the bus commands Device Clear or Selected Device Clear.

- 1. Initialization
 - Remote Program Initialize
- **Function**

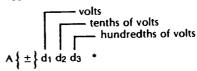
FØ	Stop	F8	T.I. A→B
F1	Start A	F9	Ratio B/A
tF2	Start Clock	F:	T.I. Avg. A→B
†F3	DVM/A	F≺	Check
F4	Freq. A	F=	Ratio C/A
tF5	DVM/T.I. A→B	F>	Freg. C
F6	Period A	F?	DVM
F7	Per. Avg. A		

3. Time Base

Code	Freq Res	Multiplier	Time Res (Std)	Time Res (Opt. 040 and 041)
GØ	1 MHz	1	100 ns	10 ns
Ğ1	100 kHz	10	1 μs	100 ns
G2	10 kHz	102	10 μs	1 μς
G3	1 kHz	103	100 μs	10 μs
G4	100 Hz	104	1 ms	100 μs
Ğ5	10 Hz	105	10 ms	1 ms
G6	1 Hz	106	100 ms	10 ms
G7	0.1 Hz	107	1 s	100 ms

- Single-Multiple Measurement
 - Single measurement. Hold sample rate.
 - Trigger required.
 - Multiple measurement. Not Hold. No trigger required.
- Measurement Cycle
 - Hold measurement until address to output. SRQ at end of measurement.
 - Output only if a device is ready to accept data **S3** when 5328A is ready to output.
- **Output Mode**
 - Output at end of measurement most universal. Output during open gate - usable only in **S5**
 - START/STOP mode or if the decade reset is disabled.
- Sample Rate
 - Maximum
 - Manual control (from front panel) **S7**
- Arming Off
 - On
- Display Storage
 - On (normal)
- Off 10. Decade Reset
- **S>** Normal
 - Disabled (for cumulative measurements)
- 11. Display Blanking
 - Normal Display
 - Blank display (digits and decimal point)

- 12. Channel A Signal Conditioning
 - a. Impedance
 - AØ 1 Megohm
 - 50 Ohms A1
 - Coupling
 - A2 **A3** DC
 - Slope
 - A4 +slope
 - A5 -slope
 - Attenuator X10
 - **A7** X1
- 13. Separate/Common
 - Separate
 - Common A A9
- 14. Check
 - A< **Normal Operation**
 - Check, Measures internal clock A?
- 15. Trigger Level A



Permissible trigger level range: -2.50V to +2.50V.

The program sequence to set trigger level starts with the channel designation letter followed by a "+" or "-" sign. Next, three digits set the voltage level. An "*" terminates the sequence. The same sequence must be used even to set Ø volts:†

> Examples: "A+000*" Ø volts "A-123*" -1.23 volts

- 16. Channel B Signal Conditioning
 - a. Impedance
 - 1 Megohm BØ
 - R1 50 ohms
 - Coupling
 - **B2**
 - DC **B3**

 - Slope
 - **B4** +slope
 - **B5** -slope
 - Attenuator
 - **B6** X10
 - **B**7 X1
- 17. Trigger Level B
- B {±} d₁ d₂ d₃
 - See Group 15, Trigger Leval A, for details
- 18. Channel Invert
 - Normal 88
 - Invert A and B inputs
- 19. Reset; Trigger
 - (Also see Bus Command GET)
 - Reset, no trigger
 - Reset and trigger
- 20. Trigger levels set to Ø volts when counter is initialized with code "P" or bus commands DCL and SDC.

†Functions not labeled on instrument front panel.

Input signal "A" enters the Amplifier Board through A12J2 and, depending on the setting of relay A12K7, is either ac-coupled through capacitor A12C30 or dc-coupled across relay A12K7. The signal then enters the two-position attenuator ("X1", "X10"), and is passed from the selected attenuation mode through either A12K3 or A12K8 to the input of the FET impedance-converter stage. This stage consists of A12Q3A and A12Q3B and resistors A12R32, A12R30, A12R27, and A12R28, connected in a zero-offset totem-pole configuration. The signal at the node between resistors A12R30 and A12R32 follows closely the signal at the gate of A12Q3A.

Resistor A12R28 is used to adjust any initial offset voltage. Diodes A12CR5 and A12CR6, resistors A12R39, A12R37, and A12R34, and capacitors A12C23 and A12C24 form an overvoltage-protection network to limit the signal sent to transistor A12Q3 and successive circuits to ± 2.61 V.

The SEP/COM A relays, A12K4 and A12K5, connect the input of the Channel "B" attenuator to either the "A" or "B" channel inputs. The signal then passes through A12U4, a dual Schmitt trigger. Trigger A12U4B compares the signal at pin 12 to a dc reference between ±2.5V on pin 11. This dc reference is selected by A12K1, and is supplied by either the DAC Board (A11) or the Switch Control Board (A19). The output of A12U4 changes state whenever the input crosses the reference voltage on A12U4(11). The output is ECL (-0.8V to -1.6V), and drives both the "A" trigger LED circuit on the A19 board and the exclusive-OR gate, A12U2. Schmitt trigger A12U4 has approximately 15 mV peak-to-peak hysteresis at its input. Exclusive-OR gate A12U2 selects the desired slope of the input waveform. When pin 7 of A12U2 is held to an ECL "high" level (SLOPE switch in "+" position), A12U2 is an inverter. When pin "7" goes "low" (SLOPE switch in the "-" position), A12U2 does not invert the signal passing through it.

Input signal conditioning control is done by inverters A12U1 and A12U3 and relays A12K1 through A12K11. This control is supplied from either the Synchronizer Board (A10) or the Switch Control Board (A19). When the Model 5328B is in "remote", relay control is received through A12J1 from the A10 board. In the "local" mode, relay control arrives via A12P2 from the A19 board. Since all of the relay control lines contain inverters, relay activation is caused by a "high" at the input.

8-BA-3. Synchronizer Board (A10)

The differential "A" Channel outputs from A12U2 feed through connector A10P2 pins "7" and "8", respectively, to A10U1. Circuit A10U4 is a one-shot that triggers only on a negative edge, and, therefore, passes only triggering events that occur on the slope selected by A12U2. The output of A10U4(15) is an ECL pulse of approximately 5-to-10 ns width. In the FREQ A check mode, the oscillator signal from A10U1(2), (either 10 MHz or 100 MHz, as selected by A10S1) is injected via A10U5B to A10U10(13). The normal "A" input entering A10U10(12) is disabled at A12U4 by LCHK being "low". The oscillator signal at A10U10(13) is passed through A10U10C and A10U10D to A10U11 (a dual 4-to-1 multiplexer). In a non-inverting mode, multiplexer A10U11 always routes the "A" channel signal to the "start" synchronizer, A10U6A. In a PERIOD function, A10U11 routes the "A" channel signal is supplied to "stop" synchronizer A10U6B. For Time interval measurements, the "B" channel signal is supplied to "stop" synchronizer A10U6B. ROM line "R6" controls the "stop" synchronizer input switching. In REMOTE, "HINV", from A10U11(7) allows the "A" and "B" channel outputs of A10U11 to be inverted. The outputs of A10U11A feed A10U12C, which drives the "A" and "B" outputs to the Function Selector, A4.

In T.I., T.I. AVG, PER, and PER AVG functions, A10U5, A10U6, A10U12, and A10U13 are used to generate synchronized time interval and gated oscillator pulses for the mainframe. After a reset pulse arrives on the "HDS" line, the RS Flip-flops (A10U5 and A10U12) and the "D" Flip-flops (A10U6A and A10U6B) are reset. At the same time, the TI and GOSC outputs are at an ECL "high". When a "start" event enters A10U5D(12), it sets the A10U5A output to A10U6A(7) "high". The next clock pulse to A10U6(6) will cause A10U6A(3) to go "low". This pulls the TI output "low", signalling to the function selector that the Time Interval has started. When TI goes "low", GOSC (A10U13B) starts to output oscillator pulses. When a "stop" event occurs at A10U5(10), the output of A10U12C goes "high". This, synchronous to the next clock pulse, sets A10U16(15) "high".

When A10U16(15) goes "high", the $\overline{11}$ and \overline{GOSC} outputs go "high", stopping the Time Interval measurement. The "Q" output of A10U6B, through A10U13C, A10U14B, and A10U14A, resets all Flip-flops, and thus prepares them for the next measurement.

ROM line "RL6(HC)", connected to A10U14(11) by A10R6, is set "low" in PERIOD measurements, holding the Stop Flip-flop (A10U5C and A10U12C) "off" until the start synchronizer, A10U6A, clocks a "high" to its "Q" output.

In a PER AVG function, where time base scaling takes place, ROM line "RL5(T10)" is driven "high". This TTL "high" is converted to an ECL "high", by resistors A10R18 and A10R21, and applied through A10U14D to A10U12(11). This causes the Stop Synchronizer Flip-flop (A10U5C and A10U12C) to ramain in a "reset" condition.

The programming interface has two operating modes — "non-DAC", and "DAC control". These modes refer to the operation of the interface with respect to incoming data. When the incoming data is for control of signal conditioning (not trigger level), the interface will be in the "non-DAC" mode. The interface will be in the "DAC control" mode when incoming data is for DAC (trigger level) control.

When the Model 5328B goes into "remote", "LEXT" goes "low". The "low", on "LEXT", causes the output of the Motherboard switch control circuit to go from -0.7V to +5V. This change, disables front-panel switch control on the A16 and the A19 boards. When "LEXT" is "low", A10U17(4), connected to tri-state buffers A10U2(1) and A10U9(1,15) is also "low". This "low" returns the outputs of tri-state buffers A10U2 and A10U9 to their "active" states. With the outputs of A10U2 and A10U9 enabled, the outputs of addressable latches A10U8 and A10U15 control the A12 signal conditioning relays.

The interface is reset by a "high" on the HRPR line. This "high" is generated by the HP-IB Interface Board (A15) when it receives an ASCII "P".

When the interface is reset, it defaults to the "non-DAC" control mode. The reset causes the latched outputs of A10U8 and A10U15 to go "low". This sets A10U17(9,10) "low", giving a "low" at A10U17(8). The "low" at A10U17(8) is connected to A10U7(14), where it causes the interface to be in the "non-DAC" mode. The "low" at A10U17(8) is also connected to clock multiplexer A10U16(1) where it causes A10U16 to route clock ("LMS") pulses to only A10U8 or A10U15.

Clock multiplexer A10U16 decodes the "MA" and "MB" lines from the A15 board to determine whether the input data byte, on MDA-MDD is "A" or "B" channel information. It then routes the clock pulse to A10U8(14) for "A" channel information, or to A10U15(14) for "B" channel information. The clock pulse latches the information into the intended latch.

After reset, the interface defaults to all of the underlined functions in Table 8-BA-2, Program Code Set. To change one of the signal conditioning controls, it is necessary to program that function.

As an example, assume a channel "A" function setting of "X1" is desired. This means that an "A7" must be included in the data string sent by the system controller to the Model 5328B. When the "A" is decoded by the HP-IB Interface (A15), it causes the "MA" line to be "high" and the "MB" line to be "low". The "MA" and "MB" lines are decoded by A10U6, and it routes the following clock pulses to A10U8.

When the "7" is sent, "1110" appears at the output of ROM A10U7, on MDA-MDD, respectively. As shown in Table 8-BA-2, the "1110" at the input causes an output of "000011" on A10U7(1-6).

The clock pulse arrives at A10U8(14) and latches the "high" on A10U8(13) to A10U8(5). The "high" on A10U8(5) is buffered by A10U9, and appears at pin "13" of its output. The "high" at A10U9(13) is inverted on the A12 board and used to close the relay to "X1" attenuation on Channel "A".

All "non-DAC" information is latched in the same manner. "B"-channel information is latched into the outputs of A10U15. It has the same code into A10U7, and thus, the same code out of A10U7, for the same function as Channel "A".

Refer to Table 8-BA-2, Program Code Set, for the proper format with which to program a trigger level. The proper format is "±X.YZ*", and follows an "A" or "B", which indicates the channel to which it applies.

For the following discussion, assume that a trigger level is programmed, following the proper format, and preceded by an "A".

The interface resets to the "non-DAC" mode when the A15 board receives an ASCII "P" from the system controller. When the A15 board receives the "A", the "MA" line is set "high" and the MB line is set "low". Integrated circuit A10U16 decodes the MA and MB lines, in the "non-DAC" mode, and clocks the "A"-channel latch, U8.

When a "+" or "-" appears on the MDA-MDD lines, a "high" is latched into A10U8(12). Latching occurs on the positive clock pulse transition from A10U16. The "high" at A10U8(12) will cause A10U17(8) to go "high". Pin A10U17(8) is connected to A10U7(14), where the "high" changes the input address to ROM A10U7, and locks the interface into the "DAC control" mode. The "high" at A10U17(8) is also connected to A10U16(1). A "high" at A10U16(1) causes A10U16 to supply clock pulses to either the "A"- or "B"-channel DAC. Since the condition of the MA or MB lines remains the same, the Channel "A" DAC receives the clock pulses. On the negative transition of the clock pulse, the "+" or "-" is shifted into the "A" Channel DAC shift registers (A10U7 and A10U11).

Following the format, the next data byte on MDA-MDD will be a number. The MDA-MDD lines supplied to A10U7 are also connected to the DAC Board (A11) Shift Registers. Since the interface is in the "DAC" mode, neither A10U8 nor A10U15 is clocked, and thus disregard data on MDA-MDD. The number is clocked into the "A"-channel DAC shift registers. The condition of the MA and MB lines, determines which DAC is clocked and accepts the number. Following the first number, a decimal appears at the input to ROM A10U7. When A10U7 decodes the decimal, it sends A10U7(5) "high".

This "high", applied to A10U2(15), causes A10U2 to block the clock pulse associated with the decimal data byte. In this manner, the DAC disregards the decimal.

Following the format, two more numbers are input, serially, and each is clocked into the "A"-channel DAC shift registers. The final character in the string, an asterisk ("*"), appears on the input data lines to A10U7. When A10U7 decodes the asterisk, A10U7(5) goes "high", again blocking the positive clock pulse transition to the "A"-channel DAC. This causes the "A"-channel DAC to disregard the "*". At the same time, A10U7(6) goes "low", allowing the negative transition of the clock pulse to latch the "low" at A10U8(13) into A10U8(12). The "low" at A10U8(12) causes A10U17(8) to go "low", returning the interface to the "non-DAC" control mode.

8-BA-9. Multiplier Board (A14)

The Multiplier section uses the phase-locked-loop method of frequency multiplication to generate the 100 MHz clock signal. The varactor-diode-tuned VCO (Voltage-controlled Oscillator), consisting of A14U3 and A14CR1, is tuned at 100 MHz by A14C19. The VCO's output is passed to decade divider A14U1, where it is divided by 10. The 10 MHz ECL output from A14U1 is translated by A14Q1 and A14Q2 to a TTL level and applied to A14U4(3). The reference input, applied to A14U4(1), is either the system clock or the phase-jittered system clock. The TTL phase detector (A14U4), in conjunction with the active filter section, generates a dc voltage at A14TP5 proportional to the phase and/or frequency difference between the reference and feedback inputs to A14U4. This correction voltage, applied to varactor diode A14CR1, adjusts the frequency of the VCO until there is zero phase and frequency difference between the reference and feedback signals.

The phase jitter circuit consists of a Random Noise Generator, an Amplifier, a Phase Modulator, and an Output Buffer Amplifier.

Random noise is generated by reverse-biased zener diode A14CR3. The noise is coupled by A14C24 to amplifier A14U7 where the noise is amplified and passed to amplifier A14U6. Variable resistor A14R28 adjusts the amplitude of the noise from A14U6.

The phase modulator consists of a varactor diode (A14CR2) in a tuned LC circuit. The input to this circuit, applied at the base of A14Q3, is the 10 MHz system clock. The amplified noise from A14U6 is applied across varactor diode A14CR2, thus causing a phase shift, of the 10 MHz clock, proportional to the noise signal amplitude. The phase-modulated 10 MHz signal is routed through emitter-follower Buffer Amplifier A14Q4 to ROM-controlled multiplexer A14U4. Gate A14U5 accepts the phase-modulated signal at pin "13", and the system clock at pin "9". ROM line "RL2" selects output from A14U5D in Time Interval Average, and from A14U5C in all other functions.

8-BA-10. DAC Board (A11)

Since this board contains two identical DACs (Digital-to-Analog Converters), only the Channel "A" converter will be discussed.

For the description that follows, assume that the Channel "A" DAC is programmed for a +2.22V trigger level. Refer to Table 8-BA-2, Program Code Set, for an explanation of the format.

The first data byte, a "+", appears on the input data lines, MDA-MDD. This data byte is supplied to the inputs of shift registers A11U7 and A11U11. A "LMS" clock pulse, routed through A10U6, applied to A11U7(1), shifts the "+" into A11U7 and A11U11. The next three data bytes, all "2"'s are shifted into A11U7 and A11U11 in the same manner.

With the "+" and three numerals shifted into A11U7 and A11U11, the shift registers provide a parallel-BCD output. This parallel output is static until the Channel "A" DAC is reprogrammed. The parallel output is supplied to the input of rate multiplier chain, A11U8, A11U9, and A11U10.

The output of the oscillator consisting of A11U5C and related components is coupled through A11Q7 to the clock input of rate multipliers, A11U8, A11U9, and A11U10. The clock signal is also supplied through inverter A11U5D to "D" flip-flop A11U2A, a synchronizer and wave shaper.

With 1000 pulses entering pin "9" of each rate multiplier, the output at A11U10(6) will be 222 pulses. These pulses are supplied through level-shifter-and-inverter A11U5B to A11U2(12). The input pulses are synchronized and shaped by A11U2A. The "Q" and " \overline{Q} " outputs from A11U2A supply level-shifter networks composed of resistors A11R31, A11R34, and A11R36 and A11R37, A11R38, and A11R39. The pulse outputs from the level-shifter networks arrive at the cathode of A11CR6 and the anode of A11CR8.

The "+" shifted into A11U11 causes A11U6(2,6) to go "high". The "highs" on pins "2" and "6" cause pins "1" and "7" to go "low". The "low" at A11U6(1) causes A11CR11 to be forward-biased. Forward-biasing A11CR11 causes A11U6A to sink all of the current from the positive current source. This disables the positive current source, A11U3A and A11Q3. With A11U6(7) "low", A11CR12 is reverse-biased, enabling the negative current source A11U3B and A11Q4.

The signal at the anode of A11CR8 is the inverted output from the rate multipliers. When the anode of A11CR8 is "low", A11CR8 is reverse-biased, and current flows through A11CR10 into A11U4(2). When the anode of A11CR8 is "high", A11CR8 is forward-biased, and current flows from A11Q4 through A11CR8.

Averager A11U4 converts the current pulses supplied via A11CR10 into a dc output voltage. The averager generates the output voltage proportional to the duty cycle of the input current pulses.

8-BA-11. IN-CABINET PERFORMANCE CHECK

The Performance Check and Operational Verification procedures for the Model 5328B are given in Section 4 of this manual.

8-BA-12. EXTENDER BOARD(S)

A special extender board (HP Part Number 05328-62016) is available to make testing of certain parts of the Model 5328B either easier.

- One of these boards is required to extend the A10 or A14 Assembly.
- Two of these boards are required to extend the (optional) Frequency "C" Assembly (A8).

8-BA-13. TROUBLESHOOTING THE PROGRAMMABLE ("A" AND "B") INPUT Module

This section performs input signal conditioning in both the local mode and the remote mode through a controller. Thus troubleshooting is divided into two sections: local mode and remote mode troubleshooting. Figure 8-BA-12 shows a recommended troubleshooting sequence.

8-BA-14. Local Mode Troubleshooting

Local Mode Troubleshooting consists of the troubleshooting flow charts in Figure 8-BA-4, Figure 8-BA-5, and Figure 8-BA-6. These flow charts are intended to help isolate "local" operation problems.

The flow chart in Figure 8-BA-4 is intended for overall "local" operation troubleshooting. Table 8-BA-3, "Relay Operation", shows required levels, control lines, and the relay involved for any function. Table 8-BA-4, "Relay Control Logic", shows the output line and level required for proper relay operation in a function. These tables (Table 8-BA-3 and Table 8-BA-4) are to be used with the Local Mode Troubleshooting Flowchart Figure 8-BA-4.

1 1 D' #	5	J-1	l Pin	Dalan Cantrollad
J-1 Pin #	Function	н	l ro	Relay Controlled
2	Ch A Slope	_	+	
5	Ch B Slope		+	
6	Ch B Atten	X1	X10	K6, K11
7	Ch B Coupling	DC	AC	K9
8	Ch B Impedance	50Ω	1 MEG	K10
10	SEP/COM	COM	SEP	K4, K5
12	Ch A Atten	X1	X10	K3, K8
13	Ch A Impedance	50Ω	1 MEG	K2
14	Ch A Coupling	DC	AC	K 7

Table 8-BA-3. Relay Operation

The troubleshooting flowchart in Figure 8-BA-5 is designed to aid in troubleshooting problems in the phase-locked loop on the Multiplier Assembly (A14). The troubleshooting flowchart in Figure 8-BA-6 is for suspected phase jitter problems.

Table 8-BA-4. Relay Control Logic

Function	Channel A	Channel B
Slope +	A10J3 Pin 2 low A10J3 Pin 2 high	A10J3 Pin 5 low A10J3 Pin 5 high
Atten X1	A10J3 Pin 12 high	A10J3 Pin 6 high
X10	A10J3 Pin 12 low	A10J3 Pin 6 low
Coupling AC	A10J3 Pin 14 low	A10J3 Pin 7 low
DC	A10J3 Pin 14 high	A10J3 Pin 7 high
Impedance 50Ω	A10J3 Pin 13 high	A10J3 Pin 8 high
1 Meg	A10J3 Pin 13 low	A10J3 Pin 8 low
SEP, COM A, CHK	SEP A10J3 Pin 10 low COM A A10J3 Pin 10 high	

The programming interface section of the Synchronizer Board (A10) is used only when the Model 5328B is in "remote"; it is used in conjunction with the DAC (A11) board to control "A"- and "B"-channel signal conditioning. When the Model 5328B is in remote, addressable latches U8 and U15 control all of the signal conditioning relays. The DAC board is also used in "remote" to allow programming of the "A" and "B" channel trigger levels.

8-BA-15. Remote Mode Troubleshooting

The following information includes Programming Logic Troubleshooting and DAC Troubleshooting. These areas will help isolate "remote" operation problems where the HP-IB Interface operates correctly in "local" control.

Programming Logic Troubleshooting includes Table 8-BA-5 and Table 8-BA-6. Table 8-BA-5 ("Program Interface Operation") shows the necessary levels that the A10 must generate in any function. Input and output codes for the ROM (A10U7) are contained in Table 8-BA-6.

Table 8-BA-5. Program Interface Operation (Part 1 of 2)

Program Code	Function	Channel A A 10 Assy Logic Levels	Channel B A 10 Assy Logic Levels	9825A Program Steps
Α0	1 MegΩ	A10U8 Pin 7 Low A10U9 Pin 9 Low		wrt 701, "PR"; dsp "A0B0"; stp
В0	1 Meg Ω		A10U15 Pin 7 Low A10U2 Pin 9 Low	
A1	50Ω	A10U8 Pin 7 High A10U9 Pin 9 High		wrt 701, "PA1B1R"; dsp "A1B1"; stp
В1	50Ω		A10U15 Pin 7 High A10U2 Pin 9 High	
A2	AC	A10U8 Pin 6 Low A10U9 Pin 11 Low		wrt 701, "PR"; dsp "A2B2"; stp
В2	AC		A10U15 Pin 6 Low A10U2 Pin 3 Low	
А3	DC	A10U8 Pin 6 High A10U9 Pin 11 High		wrt 701, "PA3B3R"; dsp "A3B3"; stp
В3	DC		A10U15 Pin 6 High A10U2 Pin 3 High	

Table 8-BA-5. Program Interface Operation (Part 1 of 2) (continued)

	Table 0-b/1-3.	Frogram interface O	Relation (rait 1 of 2)	(continuea)
Program Code	Function	Channel A A 10 Assy Logic Levels	Channel B A 10 Assy Logic Levels	9825A Program Steps
A:4	+SLOPE	A10U8 Pin 4 Low A10U9 Pin 3 Low		wrt 701, "PR"; dsp "A484"; stp
B4	+SLOPE		A10U15 Pin 4 Low A10U2 Pin 7 Low	
A5	-SLOPE	A10U8 Pin 4 High A10U9 Pin 3 High		wrt 701, "PA5B5R"; dsp "A5B5"; stp
B5	-SLOPE		A10U15 Pin 4 High A10U2 Pin 7 High	
A6	X10	A10U8 Pin 5 Low A10U9 Pin 13 Low		wrt 701, "PR"; dsp "A6B6"; stp
В6	X10		A10U15 Pin 5 Low A10U2 Pin 5 Low	
A7	X1	A10U8 Pin 5 High A10U9 Pin 13 High		wrt 701, "PA7B7R"; dsp "A7B7"; stp
B7	X1		A10U15 Pin 5 High A10U2 Pin 5 High	
A+1	DAC	A 10U8 Pin 12 High A 10U17 Pin 8 High		wrt 701, "PA+1R"; dsp "A+1"; stp
B+1	DAC		A10U15 Pin 12 High A10U17 Pin 8 High	wrt 701, "PB+1R"; dsp "B+1"; stp
A*	NORM	A10U8 Pin 12 Low A10U17 Pin 8 Low		wrt 701, "PA*R"; dsp "A*"; stp
B*	NORM		A10U15 Pin 12 Low A10U17 Pin 8 Low	wrt 701, "PB*R"; dsp "B*"; stp

Table 8-BA-5. Program Interface Operation (Part 2 of 2)

Program Code	Function	A 10 Assy Logic Levels	9825A Program Step
A8	Separate	A10U8 Pin 9 Low A10U9 Pin 7 Low	wrt 701, "PR"; dsp "A8"; stp
A9	Common A	A10U8 Pin 9 High A10U9 Pin 7 High	wrt 701, "PA9R"; dsp "A9"; stp
В8	Normal	A10U15 Pin 9 Low A10U9 Pin 5 Low	wrt 701, "PR"; dsp "B8"; stp
В9	Inverted	A10U15 Pin 9 High A10U9 Pin 5 High	wrt 701, "PB9R"; dsp "B9"; stp
A <	Normal Op	A10U8 Pin 10 Low	wrt 701, "PR"; dsp "A<"; stp
A?	Check	A10U8 Pin 10 High	wrt 701, "PA?R"; dsp "A?"; stp

Table 8-BA-6. ROM (A10U7) Input/Output Codes

	Table 0-b/1-0. ROM (711						- utp			•		\neg
	Input Code						Output Code					
	A	10U7 P	ins									
14	13	12	11	10	6	5	4	3	2	1		
0	0	0	0	0	0	0	0	1	1	0		
0	0	0	0	1	0	0	0	1	1	1		
0	0	0	1	0	0	0	0	1	0	0		
0	0	0	1	1	0	0	0	1	0	1		
0	0	1	0	0	0	0	0	0	0	0		
0	0	1	0	1	0	0	0	0	0	1		
0	0	1	1	0	0	0	0	0	1	0		
0	0	1	1	1	0	0	0	0	1	1		
0	1	. 0	0	0	0	0	1	0	0	0		
0	1	0	0	1	0	0	1	0	0	1		
0	1	0	1	0	0	1	1	1	1	0		
0	1	0	1	1	0	0	1	1	1	1		
0	1	1	0	0	0	0	1	0	1	0		
0	1	1	0	1	0	0	1	1	1	1		
0	1	1	1	0	1	1	0	0	0	0		
0	1	1	1	1	0	0	1	0	1	1		- 1
1	0	0	0	0	1	0	0	1	1	0		
1	0	0	0	1] 1	0	0	1	1	1		
1	0	0	1	0	1	0	0	1	0	0		
1	0	0	1	1	1 1	0	0	1	0	1		
1	0	1	0	0	1	0	0	0	0	1		-
1	0	1	0	1	1	0	0	0	0	0		
1	0	1	1	0	1 1	0	0	0	1	0		ı
1	0	1	1	1	1	0	0	0	1	1		
1	1	0	0	0	1 1	0	1	0	0	0		
1	1	0	0	1	1	0	1	0	0	1		
1	1	Ö	1	0	l 0	1	1	1	1	0		1
1	1	Ö	1	1	1	o	1	1	1	1		ı
1	1	1	o	Ö	li	Õ	1	ò	1	o		-
i	1	i	C	1	li	Õ	1	1	1	1		ŀ
l i	i	1	1	Ö	1	1	0	Ö	ò	o		I
i	i	1	i	1	ò	ò	1	Ö	1	1		Ì

DAC Troubleshooting includes a checkout procedure that does not require a programming source. Table 8-BA-7 ("DAC Logic Levels") gives the required logic output levels for proper operation. The information in Table 8-BA-8 ("DAC Signals") is designed to aid in troubleshooting. It should be used to troubleshoot problems where the logic levels are correct, yet the analog output is incorrect.

Table 8-BA-7. A11 DAC Logic Levels

		st Sig				Most Signific U9(13) Digit U8(1)			Sign	Output		
	14	15	2	3	14	15	2	3	14	15	TP2(1)	TP5(6)
Reset Pulse U11 Pin 14	0	0	0	0	0	0	0	0	0	0	. 0	0.000 ±0.050V dc
1 Pulse J1 Pin 14(13)	0	1	0	0	0	0	0	0	0	0	0	-0.020 ±0.050V do
2 Pulses J1 Pin 14(13)	0	1	0	0	0	1	0	0	0	0	0	-0.220 ±0.070V do
3 Pulses J1 Pin 14(13)	0	1	0	0	0	1	0	0	0	1	0	-2.220 ±0.070V do
4 Pulses J1 Pin 14(13)	0	1	0	0	0	1	0	0	0	1	1	+2.220 ±0.070V do

NOTE

This procedure does not exercise every bit. If DAC symptoms are that **some** voltages are not programmable, exercise each bit high by leaving that bit not shorted to ground.

Table	8-BA-8.	A11	DAC	Signals
				J. 7

	TP3(4)	A11U2 Pin 8(6) Pin 9(5)	Anode CR5, CR6(2, 4)	Cathode CR8 CR10(1, 3)	TP5(6)
Reset Pulse U11 Pin 14	No Pulses	No Pulses	No Pulses	No Pulses	0.00 ±0.05V dc
1 Pulse J1 Pin 14(13)	10 ms ±4 ms period pulses	10 ms ±4 ms period pulses	10 ms ±4 ms period pulses	≈+13V dc	-0.02 ±0.05V dc
2 Pulses J1 Pin 14(13)	1 ms ±0.4 ms period pulses	1 ms ±0.4 ms period pulses	1 ms ±0.4 ms period pulses	≈+13V dc	-0.22 ±0.07V dc
3 Pulses J1 Pin 14(13)	100 μs ±40 μs period pulses	100 μs ±40 μs period pulses	100 μs ±40 μs period pulses	≈+13V dc	-2.22 ±0.07V ac
4 Pulses J1 Pin 14(13)	100 μs ±40 μs period pulses	100 μs ±40 μs period pulses	≈-13V dc	100 μs ±40 μs period pulses	+2.22 ±0.07V dc

NOTE

Pulse period is approximate; 40% variation may be normal since pulse spacing is not constant out of rate multiplier. Fainter pulses between brighter pulses may be seen. This is normal.

8-BA-16. PROGRAMMING LOGIC TROUBLESHOOTING

To troubleshoot a remote programming problem:

a. Set up the equipment as shown in Figure 8-BA-2.

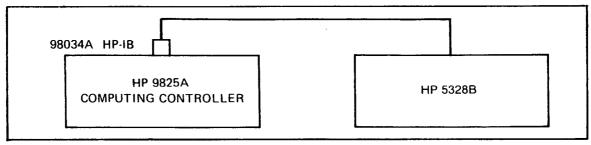


Figure 8-BA-2. Set-up for Programming Logic Troubleshooting

b. Set the Model 5328B's rear-panel address switches as shown in Figure 8-BA-3.

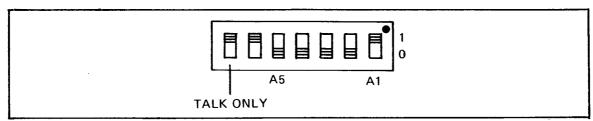


Figure 8-BA-3. Address Switch Setting for Programming Logic Troubleshooting

- c. Program the Model 5328B, using the Model 9825A.
 - 1) Find the line containing the inoperative function in Table 8-BA-5.
 - 2) Press the appropriate keys on the Model 9825A to cause the indicated "Program Steps" to be displayed.
 - 3) Press the Model 9825A's EXECUTE key.

- d. Using a logic probe, check the levels on the same line of Table 8-BA-5 as the program code you chose in step "1".
- e. Repeat for all inoperative functions. Suspect any integrated circuits which generate an incorrect output level.

8-BA-17. DAC TROUBLESHOOTING

To perform DAC troubleshooting —

- a. Check the +5V, -5.2V, +15V, and -15V supply lines on the Motherboard (A1). (Refer to the A2 troubleshooting procedure for repair.)
- b. Check for a clock signal at U5(8,11), and on Q7's collector.

If the clock signal is not correct, suspect U5, Q7, or C12.

- c. Perform the following setup procedure:
 - 1) Turn the Model 5328B's power off.
 - 2) Unplug the 14-conductor cable from A11J1.
 - 3) Remove the DAC Board (A11) from the Model 5328B.
 - 4) Install jumpers in J1 as follows:

from pin 8 to pin 10 from pin 5 to pin 6 from pin 4 to pin 7 from pin 3 to pin 12

NOTE

Be sure the jumper from pin "8" to pin "10" does not short to any of the other jumpers.

- 5) Reinstall the DAC board, using a HP 05328-62016 extender board, into XA11.
- 6) Apply power to a HP 10526T Logic Pulser.
- 7) Connect an HP 3468A Voltmeter between A11TP5 and the Model 5328B's chassis, so you can monitor the Channel "A" DAC output).
- 8) Turn the Model 5328B's power "on".
- d. Pulse A11U11(14) with the Logic Pulser.

This resets the DAC storage registers.

If the Voltmeter's display is $0V \pm 50$ mV, perform step "e".

If the Voltmeter's display is not 0V \pm 50 mV, refer to Table 8-BA-7, and check the logic levels listed on line "1" (Reset Pulse U11(14)) for an improper level. Suspect any integrated circuit listed, if it has an improper output.

- e. Pulse A11J1(14) once with the Logic Pulser, and verify line "2" of Table 8-BA-7.
- f. Pulse A11J1(14) three more times, stopping after each pulse to verify the next line of Table 8-BA-7.
- g. Steps "d" through "f" checked the Channel "A" DAC. To check the Channel "B" DAC —
- h. Change the Voltmeter connector connections to A11TP6 and the Model 5328B's chassis.
- i. Repeat step "d" through step "f", pulsing A11J1(13) instead of A11J1(14).

NOTE

The numbers in parentheses in Table 8-BA-Z refer to pins related to Channel "B".

- j. If the Channel "A" and Channel "B" DAC output voltages were the same as listed in Table 8-BA-7, the board is functioning correctly. If an output is not correct, suspect the integrated circuit that generates the level. If the digital outputs are correct and the analog output is incorrect, continue in step "k", below.
- k. Reset the storage registers (U16, U15, U11, U10) by pulsing A11U10(14) with a Logic pulser.

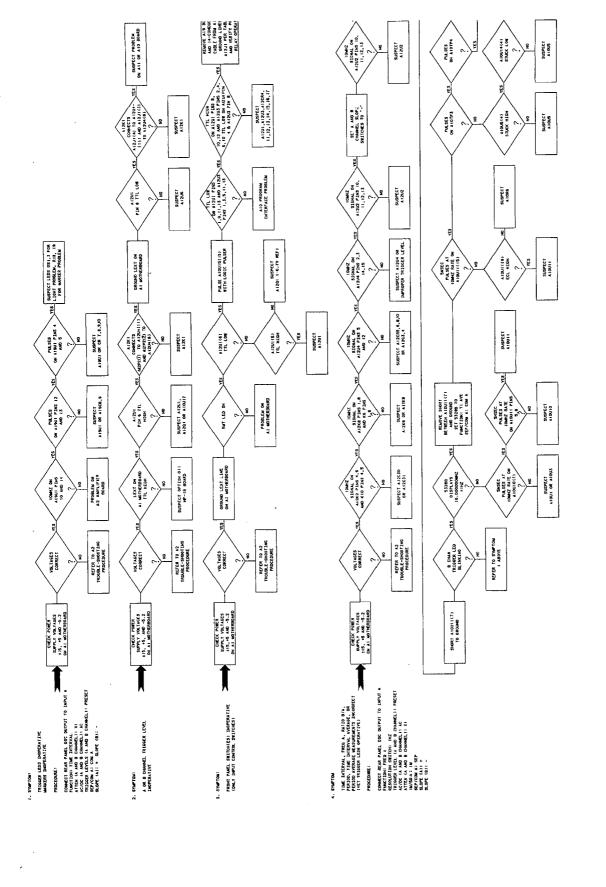
 Use an Oscilloscope to check the signals listed in line "1" of Table 8-BA-8.
- I. Pulse A11J1(14).
- m. Using the Oscilloscope, Check for signals in line "2" of Table 8-BA-8.
- n. Pulse A11J1(14) three more times, stopping after each pulsing to verify the corresponding line in Table 8-BA-8 with an Oscilloscope.

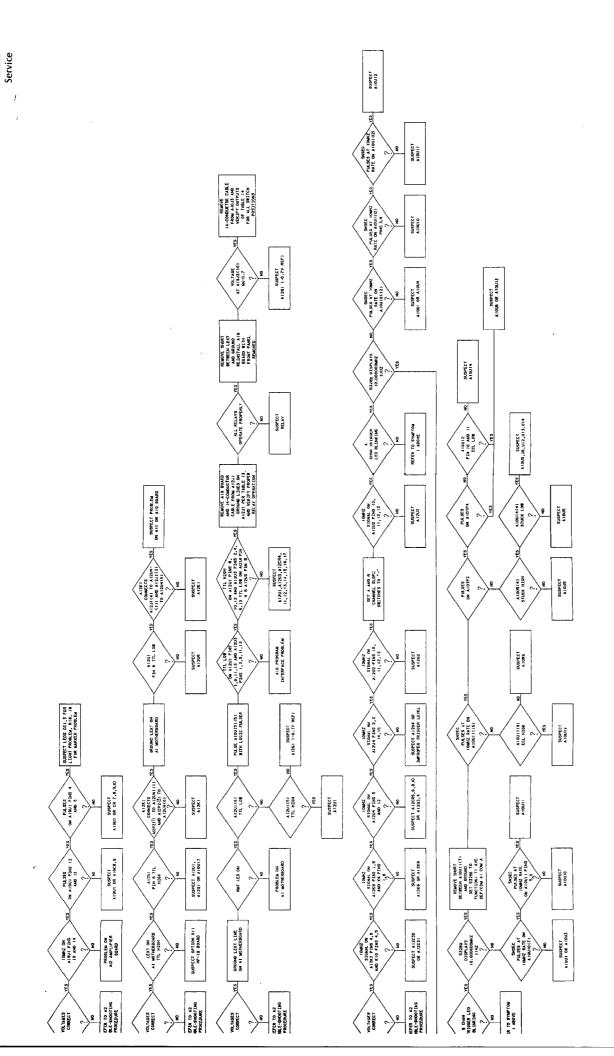
Steps "k" through "h" have checked the Channel "A" DAC signal path. To check the Channel "B" DAC, perform steps "k" through "h", above, except pulse A11J1(13) instead of A11J1(14). Stop after each pulsing to verify the locations listed in parentheses in Table 8-BA-8.

o. Refer to Table 8-BA-9 to match the symptom detected with the probable cause of the trouble.

Table 8-BA-9. A11 DAC Troubleshooting

Symptom	Probable Cause
Pulses wrong at TP4	U12, U13, or U14
Pulses wrong at TP3	U8, U9, or U10
Pulses wrong at U2 output	U2
Pulses wrong at drain of Q2	Q2, CR2, CR4, U1, or U3
Pulses wrong at drain of Q1	Q1, CR1, CR3, U1, or U3
Pulses wrong at drain of Q4	Q4, CR8, CR10, U4, or U3
Pulses wrong at drain of Q3	Q3, CR5, CR6, U4, or U3
If Pulses at all points good	U4 for Channel A U1 for Channel B





Model 5328B

Figure 8-BA-4. "Local" Mode Troubleshooting Flowchart

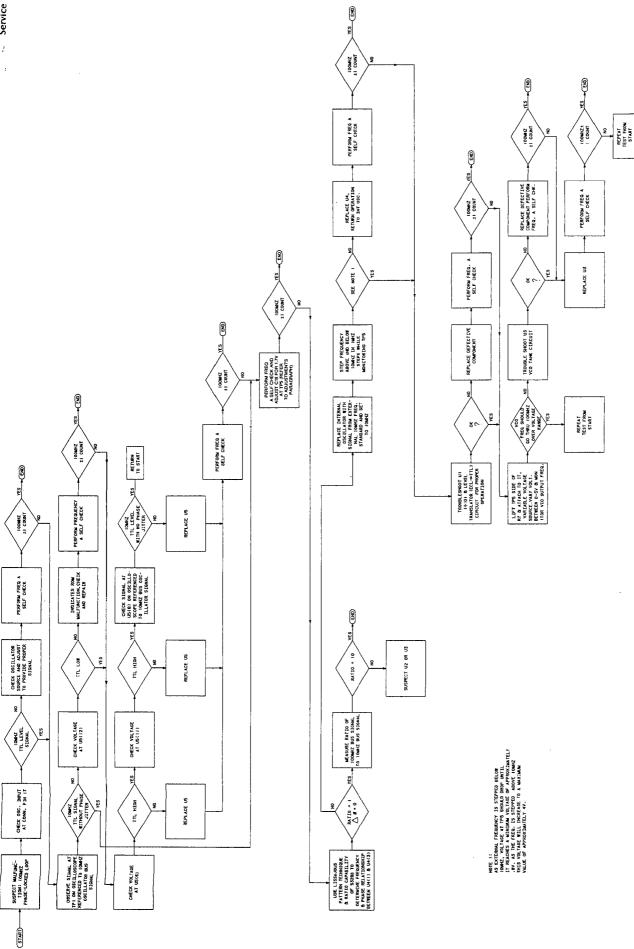


Figure 8-BA-5. Multiplier Phase-Locked Loop Circuit Troubleshooting Flowchart

Figure 8-BA-6. Multiplier Phase/litter Troubleshooting Flowchart

8-BA-18. REMOVAL AND REPLACEMENT INSTRUCTIONS

Removal and replacement instructions are provided for the counter's cover, the Time Interval Module (assemblies A10 and A19).

8-BA-19. Instrument Cover Removal

To remove the Model 5328B's top or bottom cover:

- a. Remove the screw at the rear of the cover that secures the cover to the counter's frame.
- b. Slide the cover toward the rear of the counter and lift it off.

To replace the Model 5328B's top or bottom cover, reverse the procedure above.

WARNING

THE AC LINE (MAINS) (110V/120V/220V/240V) WIRES ARE EXPOSED WHEN EITHER THE TOP OR BOTTOM IS REMOVED. USE EXTREME CAUTION DURING TROUBLESHOOTING, ADJUSTMENT, OR REPAIR.

REDUCE THE CHANCE OF PERSONAL INJURY OR DAMAGE TO THE COUNTER BY ALWAYS DISCONNECTING THE AC POWER CORD BEFORE YOU REMOVE OR REPLACE THE COUNTER'S COVERS OR ANY ASSEMBLY OR COMPONENT WITHIN THE COUNTER.

8-BA-20. Time Interval Module (A10 And A19) Removal

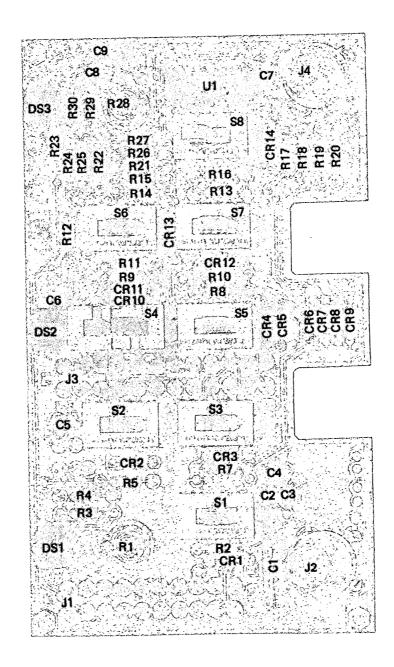
To remove the Time Interval Module:

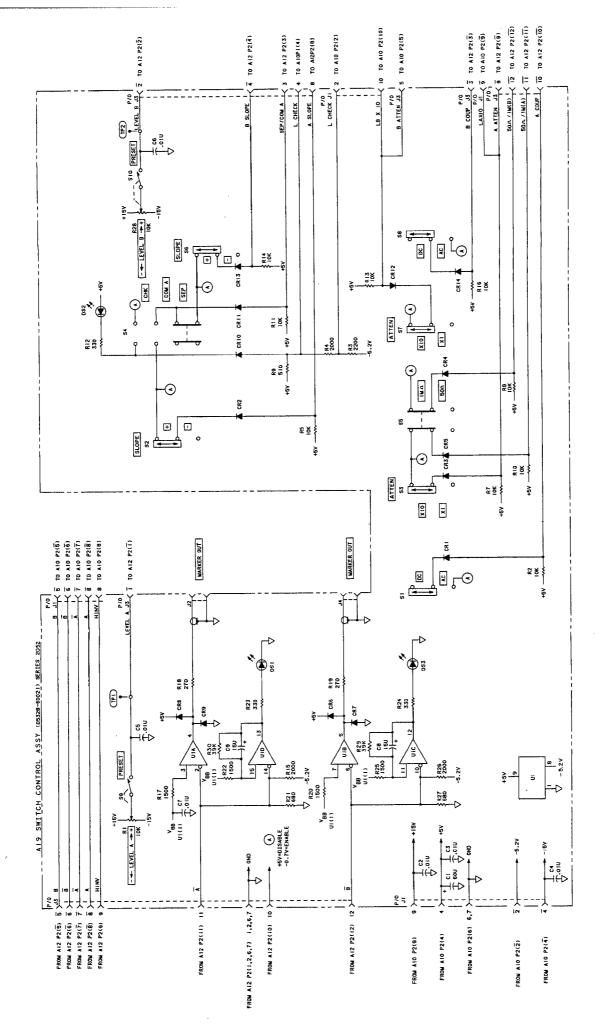
- a. Disconnect the Model 5328B's line cord (as as safety precaution).
- b. Remove the Model 5328B's top cover.
- c. Using a suitable flat-blade screwdriver as a prying tool, gently remove the plastic filler strip from the top of the counter's cast front-panel frame.
- d. Remove the two machine screws that secure the top of the module front panel to the top of the cast front-panel frame.
- e. Turn the Model 5328B on its side and remove the two machine screws that fasten the bottom of the module front panel to the bottom of the cast front-panel frame.
- f. Slightly loosen all remaining machine screws along the top of the cast front-panel frame. This releases the compressive force on the module front panel.
- g. Using a suitable allen wrench, remove the LEVEL A control knob from the module front panel.
- h. Remove the A19 Attenuator assembly, with front panel attached, by gently pushing the assembly from the rear. Note that the attenuator assembly is separated from the A10 Synchronizer assembly during this operation.

- i. Remove the front panel from the A19 Attenuator assembly by removing the nut attached to each connector and removing the LEVEL B control knob.
- j. Remove the A10 Synchronizer assembly by pulling the assembly upward.

This ends the Time Interval Module removal procedure.

The procedure for replacing the Time Interval Module is essentially the reverse of the above procedure.





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Figure 8-BA-7. A19 Switch Control Assembly Schematic Diagram

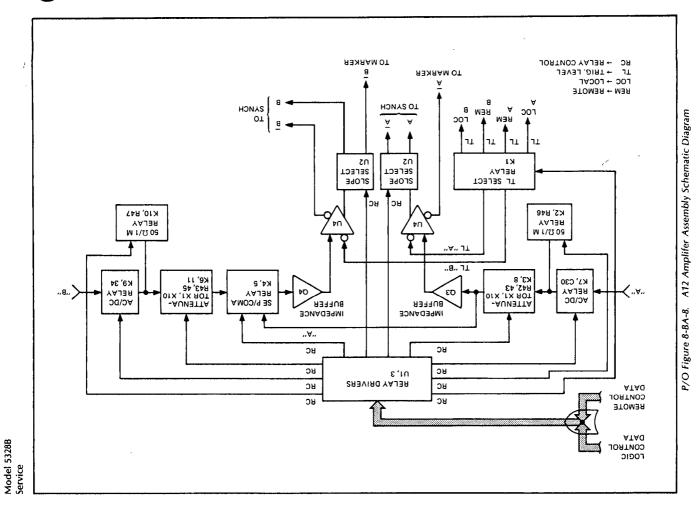
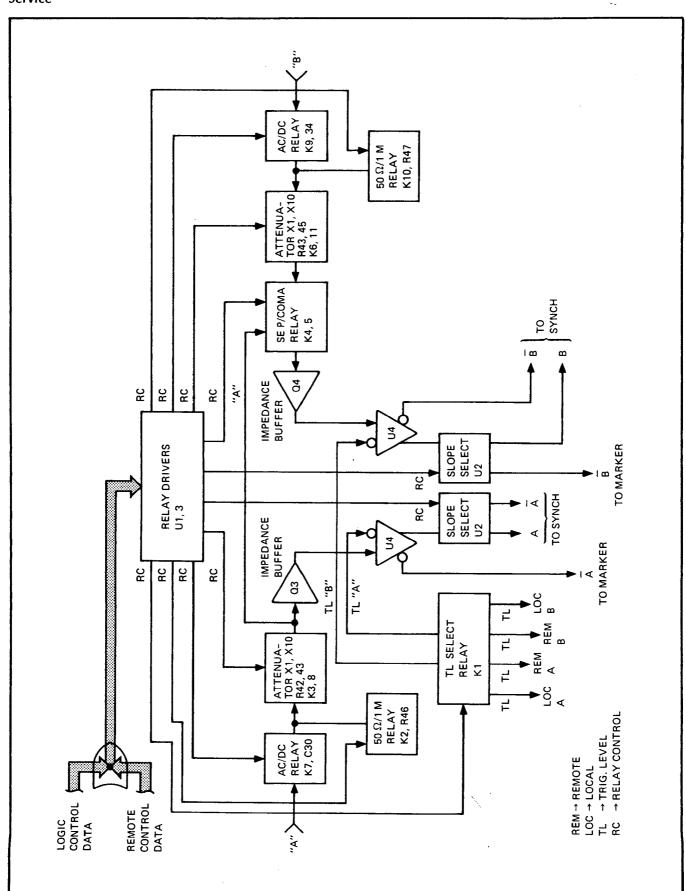


Figure 8-BA-7 A19 SWITCH CONTROL ASSEMBLY (SCHEMATIC DIAGRAM)

96-8

(See Page 8-95)



P/O Figure 8-BA-8. A12 Amplifer Assembly Schematic Diagram

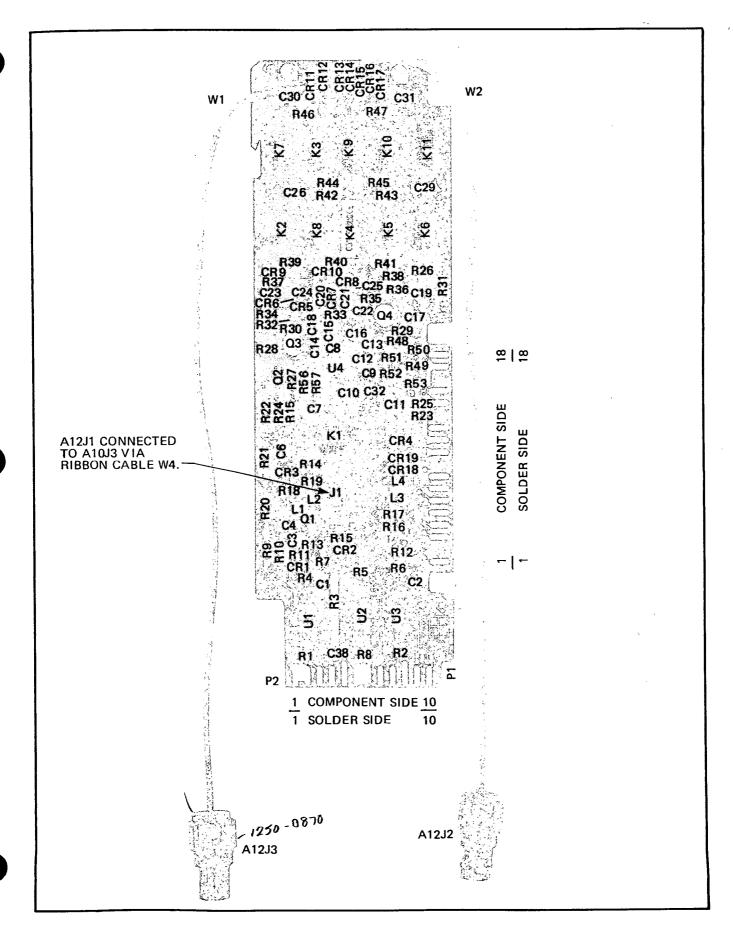
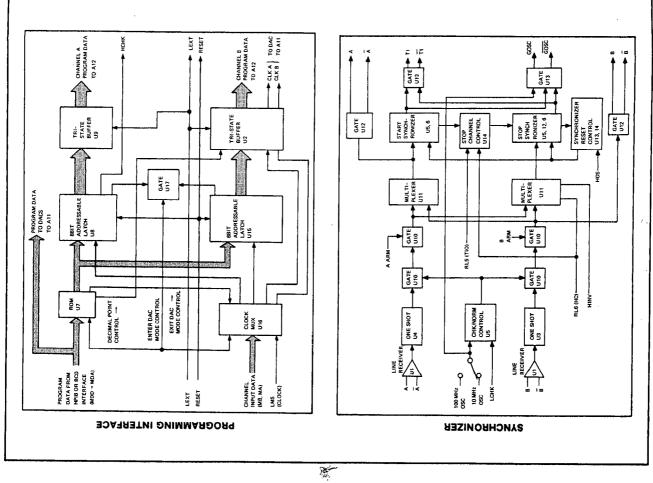


Figure 8-8A-8. A12 Amplifier Assembly Schematic Diagram

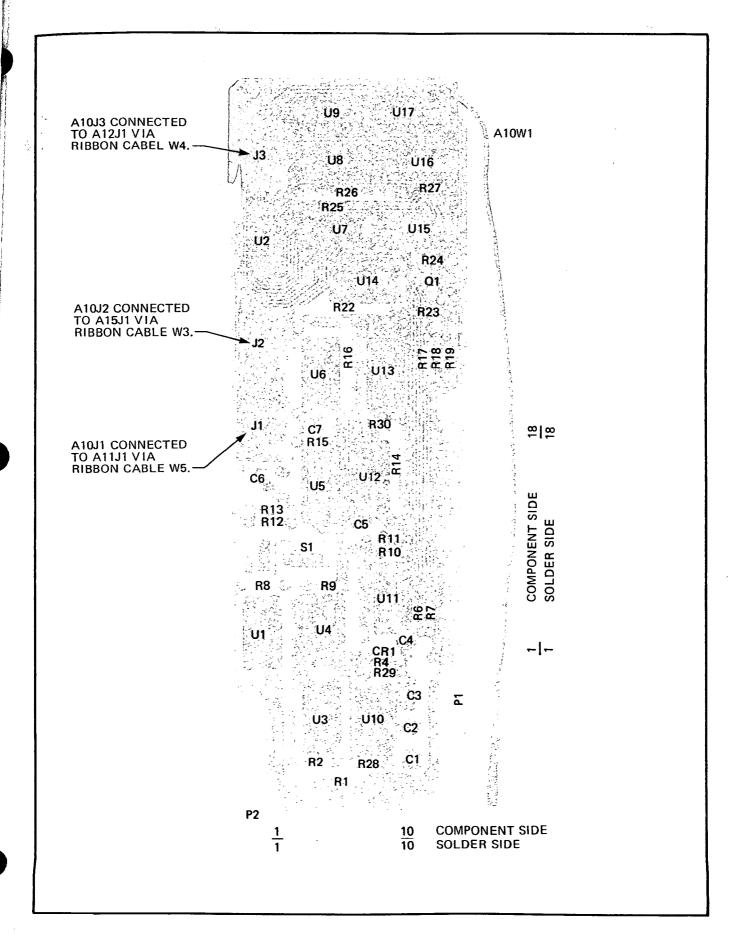


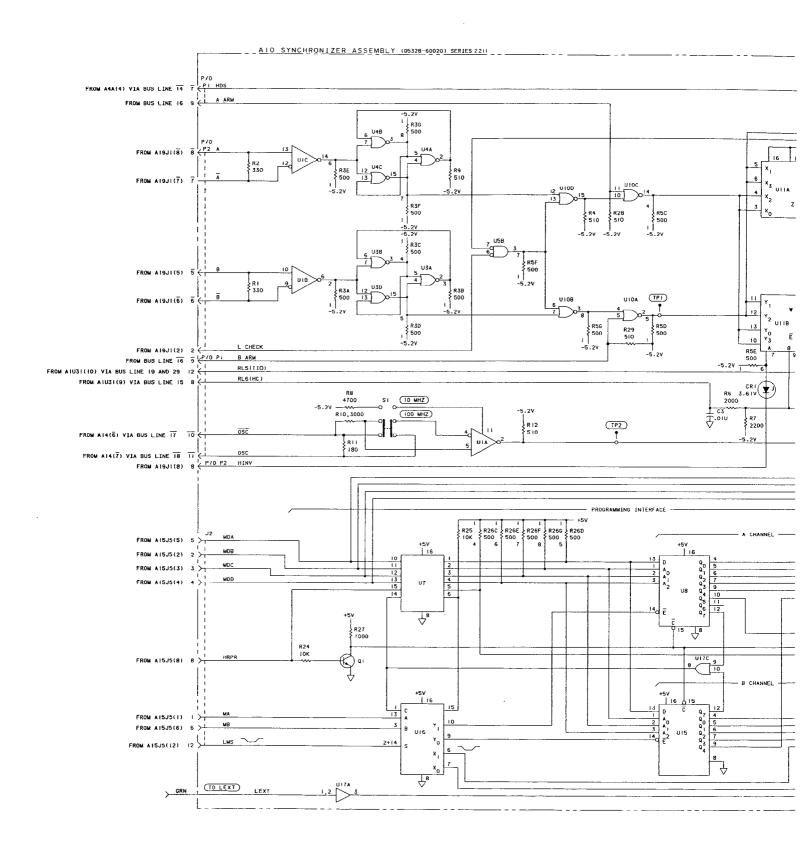
P/O Figure 8-BA-9. A10 Synchronizer Assembly Schematic

8-98

(See Page 8-97)

Figure 8-8A-8 A12 AMPLIFIER ASSEMBLY (SCHEMATIC DIAGRAM)





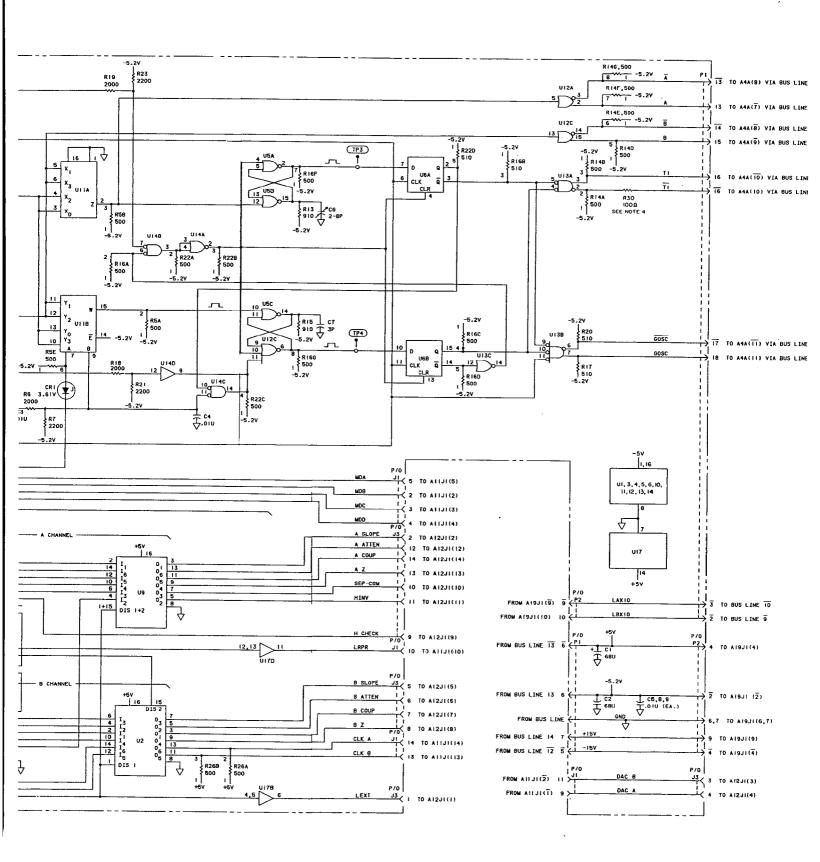
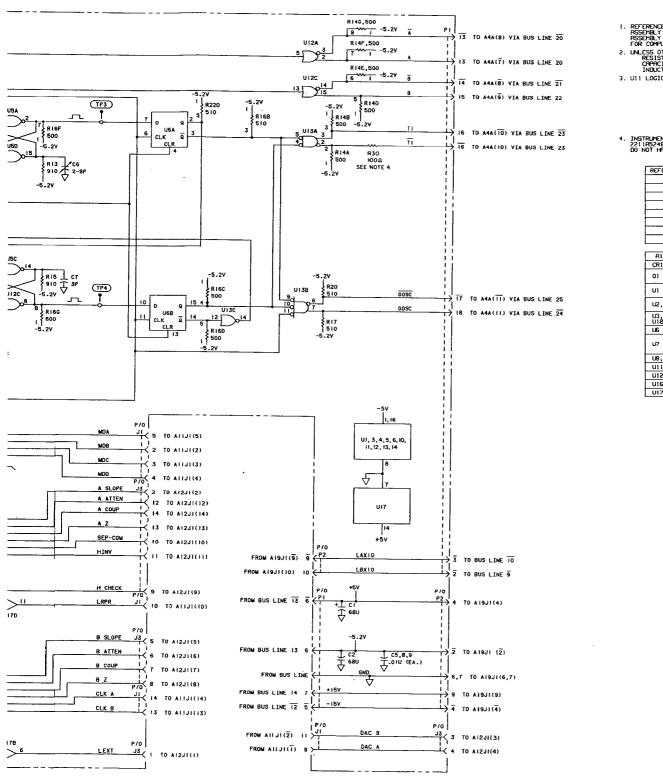


Figure 8-BA-9. A10 Synchroniz€



NOTES

1. REFERENCE DESIGNATIONS WITHIN THIS RESEMBLY RRE ABBREVIATED. ADD RESEMBLY INDEED TO ABBREVIATION FOR COMPLETE DESCRIPTION.

2. UNLESS OTHERNISE INDICATED:
RESISTANCE IN OHMS;
CAPACITANCE IN FRANCE;
INDUCTANCE IN HENRIES.
3. U11 LOGIC TABLE;

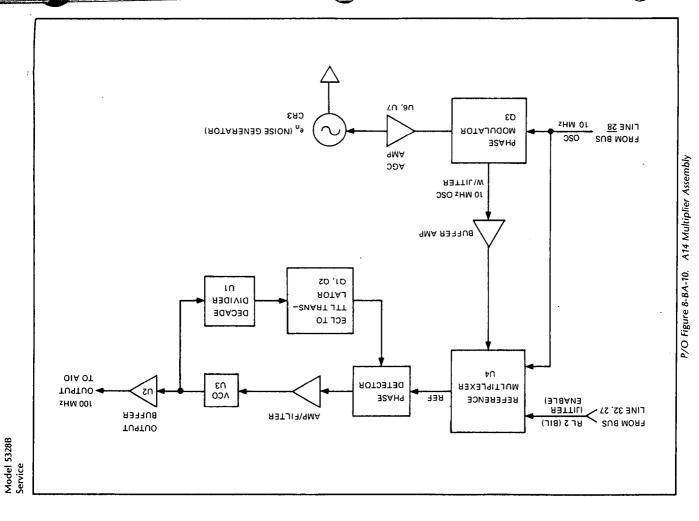
_					
	7	9	z	М	
Ø	L	L	ค	А	
2	L	н	A	В	
1	Н	L	В	B	
3	H	н	В	А	

A10				
REFERENCE DESIGNATIONS				
C1-C9				
CR1				
J1-J3				
Q1				
R1-R29				
SI				
TP1-TP4				
UI-U17				

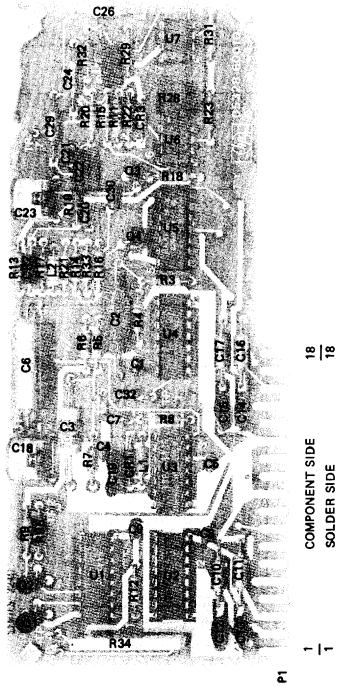
A10 ACTIVE ELEMENTS				
CR1	1902-3036			
01	1954-9071 2N3391			
U1	1920-1320 MC 10216			
ns'ra	1920-1049 DM8097N			
ບ3,U4,U5, ບາຍ,ບາ4	HC 10102			
U6	1820-0817			
יט	1816-1174 OR Ø5328-8ØØØ3			
UB,U15	1920-0833			
U11	1820-1359			
U12,U13	1920-0803			
U16	1820-1245			
UI7	1820-1208			

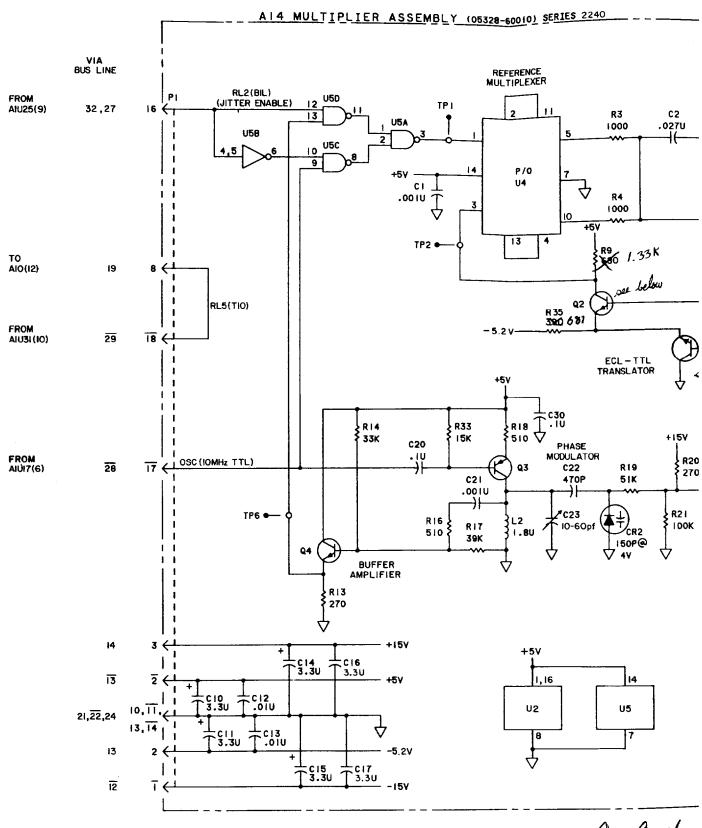
Figure 8-BA-9. A10 Synchronizer Assembly Schematic Diagram



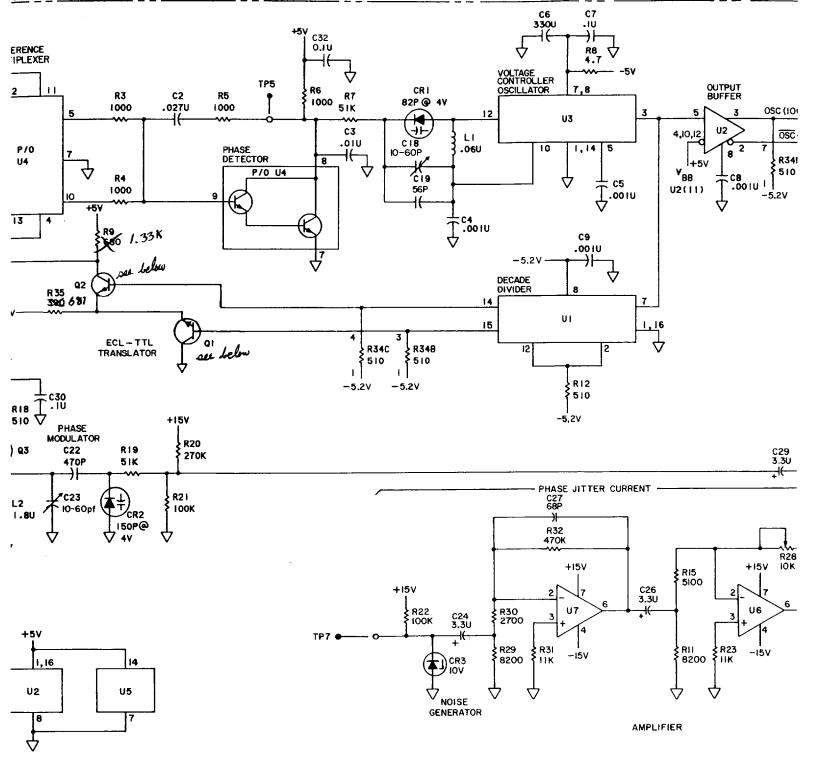


8-100





Q, + Qr clarge 1854-009 Min Miscourte lumour no



Q, + Qr clarged from
1854-0092 -> 1854-0583

Min Miscounting caused by excessive

Lemonic noise PCU 2-19488
2/15/89 JA

Figure 8-B

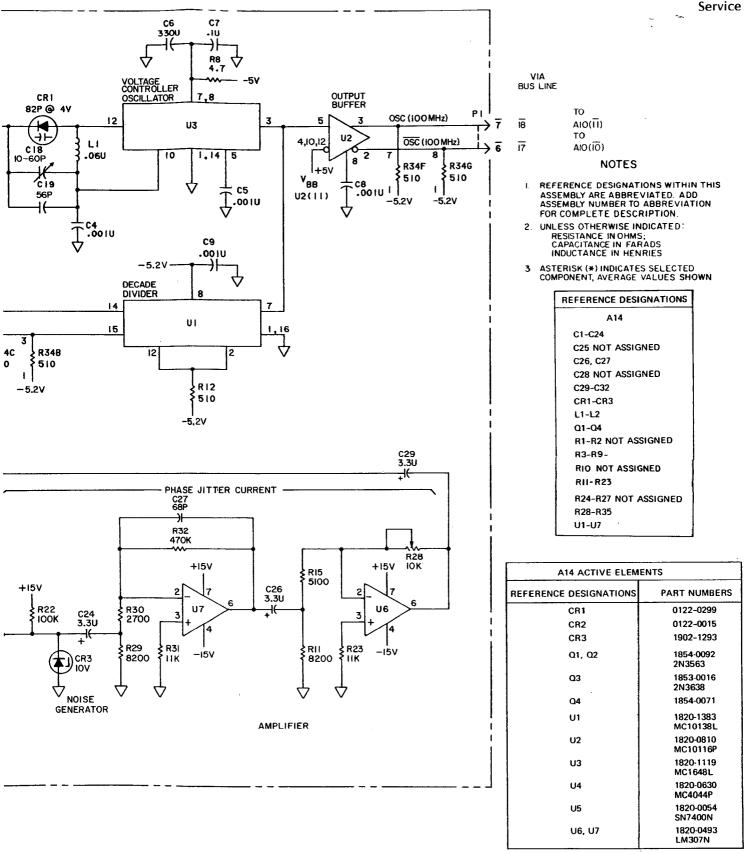
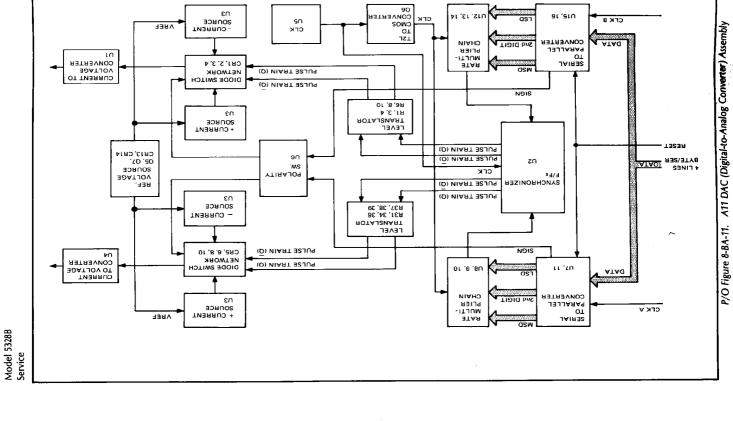


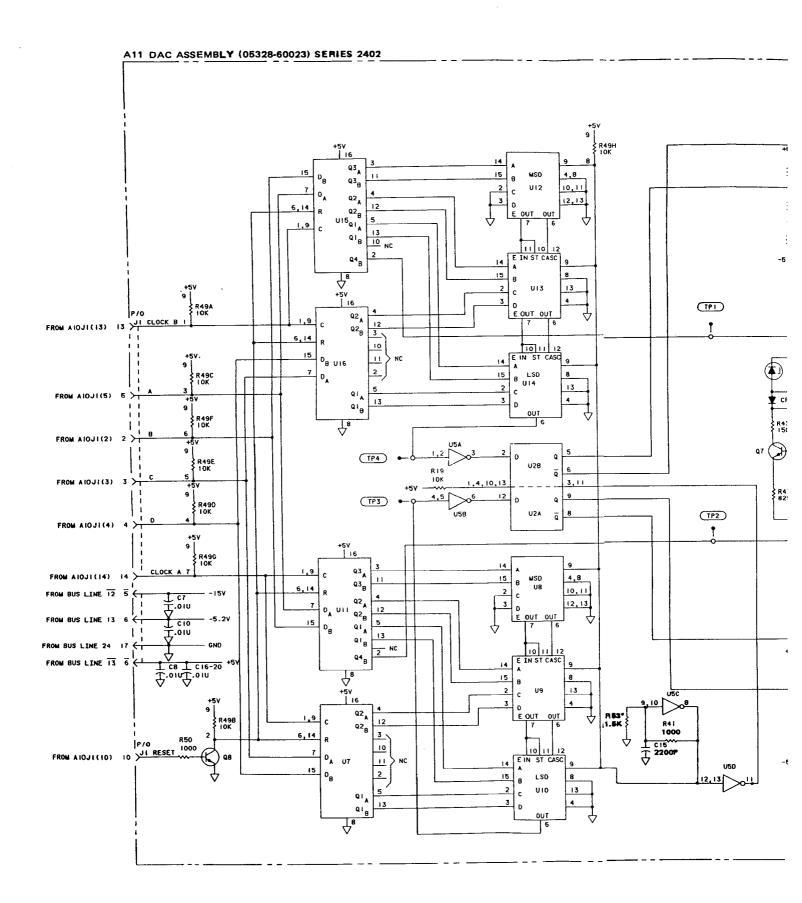
Figure 8-BA-10. A14 Multiplier Assembly Schematic Diagram

8-102

(See Page 8-101)



	2 9	
A11J1 CONNECTED	J1 & R50	
TO A10J1 VIA RIBBON CABLE W5.	0.8	
0.4 0.4 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5		
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	5 618 5	
	C17	
	20 C16 S	
• •3 • ;		
	R48 R46 CR16 R47	
	R46 CR16 R47 Q7 R44 R45 CR15 R43	
	S Q5 R42 C15 CR13 R53	8 8
<i>(</i> -)	CR12 R41 CR11 R39 CR10 R38	
	CR9 CR11 R39 CR7 CR8 R37 R35 C4 CR6 R36 R33 CR5 R34 R30 R52 C14 R28 C13 C14	ш
	R30 R29 Q3 C13 C14	T SID JE
	C11 5	COMPONENT SIDE SOLDER SIDE
, .	R26 U4 R25 C10	OMPC
	R24 R25 C10 R23 C9 R21 R22 C8	Ö Ø
er Personal de la companya de la compan Personal de la companya de la comp	R20 R19	
	R18	- -
	R17 R16 S	
	044	
	R7 R8 R8	
	O2 C3 C4 F R4 O1 R2 C2 R3	
	O1 R2 C2 R1 CR3 CR4 C1 CR1 CR2 C1	



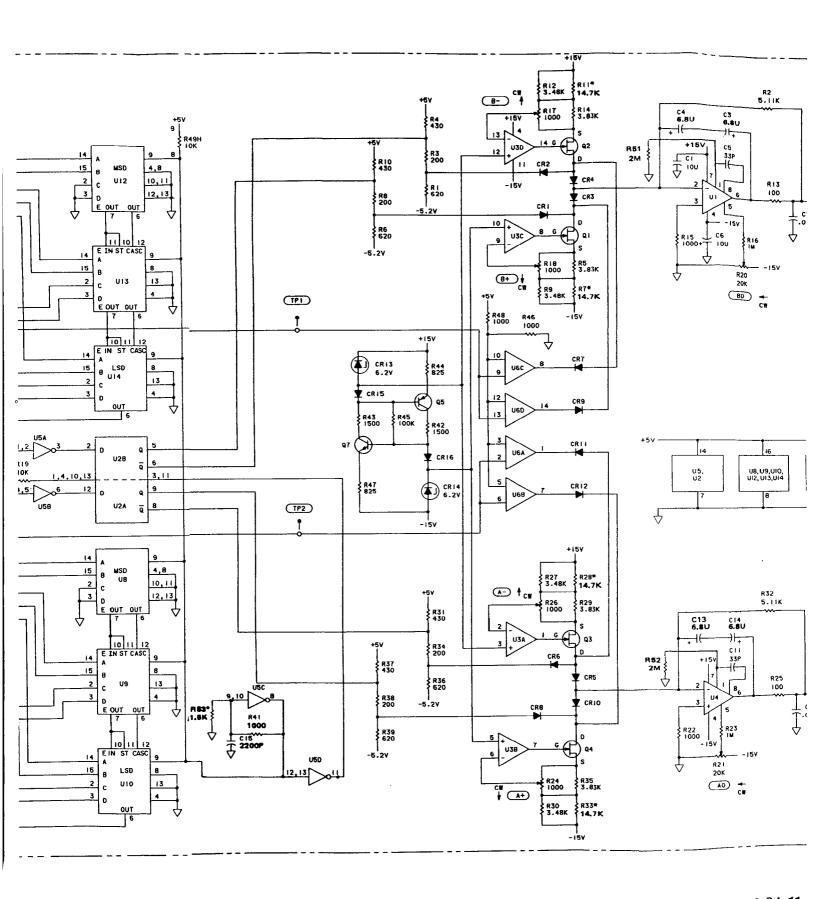


Figure 8-BA-11.

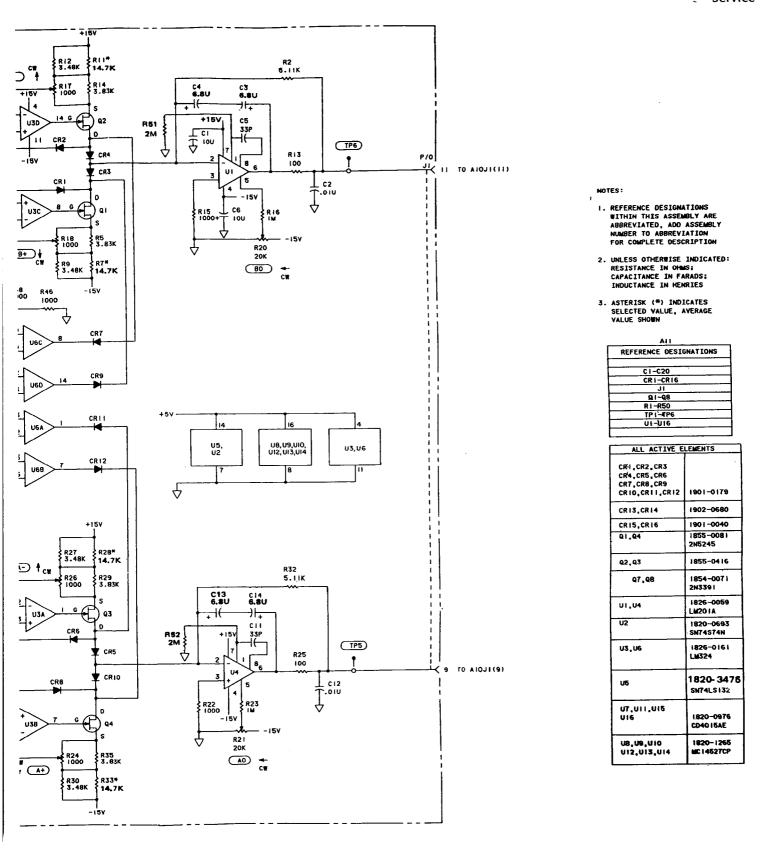


Figure 8-BA-11. A11 DAC (Digital-to-Analog Converter) Assembly Schematic Diagram

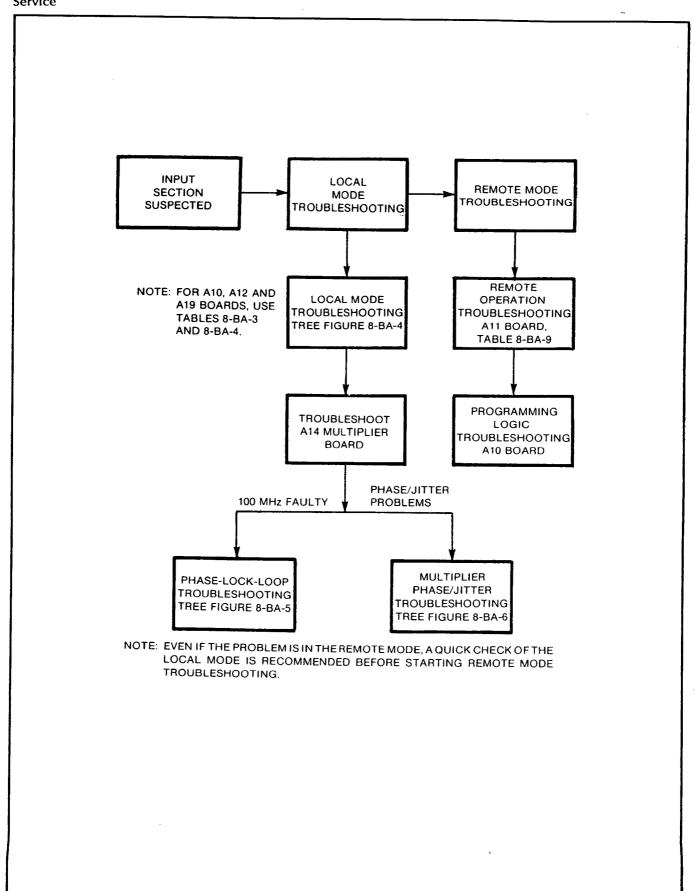


Figure 8-BA-12. Input Section Troubleshooting

8-BB-1. DIGITAL VOLTMETER (OPTION 020 OR PART OF OPTION 050)

The (optional) Digital Voltmeter (DVM) Option Module allows the counter to be used to measure dc voltages. It provides a high-performance unit that has:

- 10 μV sensitivity
- · automatic or manual range control to 1000V
- 10-Megohm floating inputs
- switchable filter
- 0.03% accuracy
- capability to read triggering levels of Channel "A" and "B" inputs
- · variable integration time
- high-speed acquisition (up to 300 readings per second with two-digit resolution).

The Model 5328B's (optional) Digital Voltmeter (DVM) Module uses the voltage-to-frequency conversion technique to provide an output suited to the units basic frequency measuring capabilities.

The DVM includes front-panel switches that allow it to be used to measure the triggering level of Channel "A" or Channel "B"; when either of these measurements is selected:

- the DVM's input circuitry is disconnected from its external-input banana-plug connectors, and is connected to the selected trigger-level voltage,
- the Model 5328B's front-panel FUNCTION and TIME BASE switches are disengaged
- the code "DVM" is placed on the Function Code Bus
- a 0.1 s gate time (1 mV sensitivity) is placed on the Time Base Bus

When the "READ" switch is released (i.e., in its "out" position), the Model 5328B returns to its previous state. This allows you to check trigger levels without having to change (and later change back) the FUNCTION and TIME BASE settings.

The Model 5328B's DVM provides the capability of automatic or manual selection of the DVM's range (10 μ V to 1000 V, with a basic accuracy of 0.03%). For particularly noisy environments, a switchable filter can be engaged to increase normal mode rejection to 50 dB at 50 or 60 Hz. Isolation for the DVM is accomplished through special high-speed transformers, optical isolators, and a switching dc-to-ac power supply that is part of the DVM's circuitry.

Although remote programming of the DVM's front-panel controls is not possible, remotely controlled voltage measurements can easily be done. Through the use of special range controls in the voltage-to-frequency converter, a conversion factor of 10 kHz-per-volt is maintained, regardless of the DVM's range. If the DVM has been (manually) placed in its Autorange mode, you only need to select the Model 5328B's "DVM" FUNCTION via the HP-IB to allow the DVM to measure any voltage from 10 μ V to 1000V.

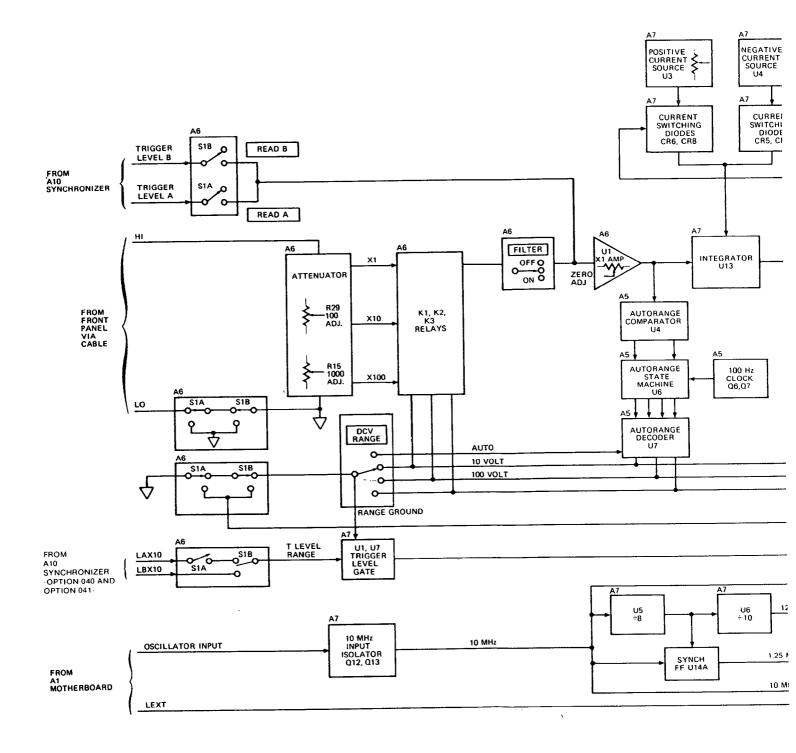
The voltage-to-frequency conversion technique does result in a small problem. Suppose, for example, the voltage at the DVM's input terminals is 900V. At DVM's 10 kHz-per-volt conversion ratio, its output frequency is 900×10 kHz, or 9 MHz. In a measurement time of one second (1s), this would provide a resolution of one part in 9×10^6 , which is far beyond the resolution limit of the voltage-to-frequency converter. In order to prevent mis-interpretation of DVM measurement results, the DVM blanks the meaningless data, thus providing a display that contains only accurate data.

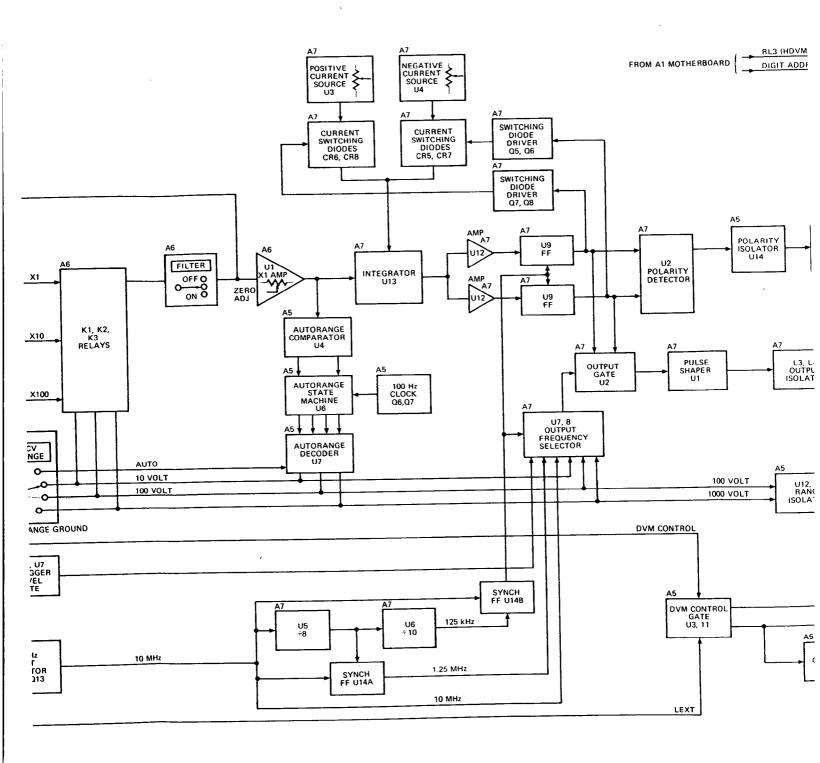
8-BB-2. Principles of Operation

The voltage at the Digital Voltmeter Module's "HI" and "LO" inputs is fed to the A6 Buffer Amplifier board through the attenuator. The attenuator's value is selected by relay K1, K2, or K3, to send the proper signal level to Buffer A6U1. The trigger level voltages from the Synchronizer (A10) enter buffer A6U1 through the "READ A" (S1A) or "READ B" (S1B) switch. When a trigger level measurement is being made, the floating ground is connected to the counter's chassis, and all of the attenuator relays are de-energized. A "zero" adjustment is connected to buffer amplifier A6U1 to provide an adjustment for 0V out when the input is zero volts.

The VF ("V-to-F" or "voltage-to-frequency") Converter Board (A7) receives the output of the Buffer Amplifier (A6) and generates a train of pulses whose frequency is proportional to the voltage at its input. The polarity of the voltage is detected by a polarity detector. Output frequency gating is provided, to change the output frequency as a function of range. An input isolator circuit and an output isolator circuit are used to couple signals between chassis ground and floating ground.

The Power Supply portion of the Auto Range Board (A5) receives $\pm 15V$ dc via the bus, and switches (alternates) it through a transformer. The transformer's outputs are rectified to supply all power for the "floating" portion of the DVM. The autoranging portion of the board contains four comparators (A5U4) that receive the output of Buffer Amplifier A6U1 and determine whether the DVM should range up, range down, or remain as is. The present DVM range is stored in a state machine which is decoded into one of three states. The clock for the state machine is supplied by a unijunction transistor oscillator that produces 100 μ s positive pulses at a 100 Hz rate. The remaining logic circuits on the A5 provide digit blanking and negative-sign information to the proper digits of the display.





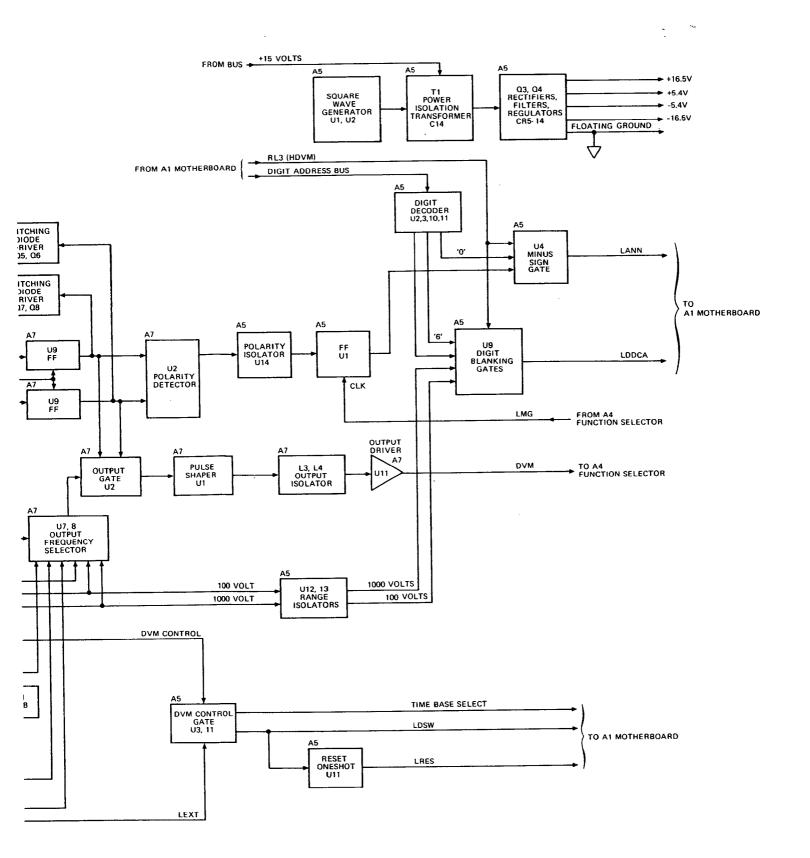


Figure 8-BB-1. Digital Voltmeter Module Block Diagram

8-BB-3. BUFFER AMPLIFIER BOARD — A6

The dc voltage to be measured is received from the DVM's two front-panel floating-inputs via cables W1 and W2. The input attenuator conditions the dc voltage to allow a proper level to be fed through one of three relays (K1, K2, K3) to the Buffer Amplifier (U1). The Buffer Amplifier converts the high-impedance dc level at its input to a low-impedance dc level at its output. Variable resistor R28, accessible through the front panel, allows the output to be set to "zero" for a 0V input. Switchable capacitor C1 is a low-pass filter. Diodes CR1 and CR2 protect the amplifier from overvoltage.

The READ A and READ B switches (\$1A,B):

- convert the DVM's "LO" terminal to chassis ground,
- allow "RANGE GROUND" to go "high",

This de-energizes all attenuator relays.

ground "DVM CONTROL",

This resets the counter and selects the time base code and FUNCTION code.

- select the proper range information from the Programmable Input Module,
- apply the trigger level voltage to the Buffer Amplifier's input.

8-BB-4. VF CONVERTER — A7

The output of the Buffer Amplifier (A6) is applied to integrator U13. The integrator slews (ramps) in the positive direction when the input is negative, and in the negative direction when the input is positive. Circuit U12 and its associated components make up two voltage comparators. When the voltage input to the integrator is positive, U12(4) goes to an ECL "low"; when the input is negative, U12(3) will go to an ECL "high". These levels are clocked into ECL "D"-type flip-flop U9. The clock is derived from a 10 MHz clock, which is divided by eight and then by ten to obtain the 125 kHz clock.

When flip-flop U9 changes state, its output is sent to one of two differential pairs of transistors — Q5 and Q6 for a negative input voltage, or Q7 an Q8 for a positive input voltage. The transistors activate and deactivate their respective switching diodes (CR7 and CR8). The polarity of the current is such that it tends to drive the output of the integrator in the opposite direction. The RS flip-flop (U2C and D) detects the polarity of the input voltage. Pulses are gated out through U2A,B during the time the current sources are "on".

The pulse rate at U2(1) is chosen by the range information; this rate is 125 kHz for the 10V range, 1.25 MHz for the 100V range, and 10 MHz for the 1000V range. When no range information is selected, as when trigger level measurements are made, the Trigger Level Range information is selected and it controls the gated output frequency.

8-BB-5. PS AUTORANGE BOARD — A5

Power for all floating circuitry in the DVM comes from the dc-to-dc converter on this board.

Multivibrator U2D clocks flip-flop U1B. The symmetrical output drives switching transistors Q1 and Q2 which are powered from the +15V line. Four output voltages are available at the secondary terminals of T1. Voltages supplied are rectified, filtered, and regulated +16.5V, -16.5V, +5.4V, and -1.4V.

The autorange portion of this board applies the output from the Buffer Amplifier (A6) to four comparators (U4). From the comparator's output, the "range up" and "range down" signals are generated. This range information goes to a clocked state machine (U6). The decoded "state" information energizes the proper relay on the Buffer Amplifier (A6) and selects the proper output frequency for the VF Converter (A7) when the "AUTO" RANGE is selected. Manual range selection (10V, 100V, or 1000V RANGE setting) disables the state decoder and pulls the appropriate "enable" line "low".

In order to blank non-significant digit(s) when in the 100V or 1000V range, the "range" information is passed through optically-coupled isolators U12 and U13 to chassis common. This information disables the DCA output when the digit address is "7" on the 100V range and when the digit address is "6" or "7" on the 1000V range.

Polarity information passes through a similar isolator (U14), and is then latched into a flip-flop to enable the minus sign when digit "0" is decoded.

When trigger levels are measured, the front-panel switches must be disabled and the proper time base and FUNCTION codes must be set. Also, a reset pulse must be applied when going into or out of a trigger level measurement. These signals are generated by the DVM Control line through U3A and U11. The "LEXT" line disables this function when the counter is in "external" mode.

8-BB-6. MAINTENANCE

When handling the Option 021 circuit boards, hold by the edges only. Do not touch the edge connectors. High impedence circuitry on the boards could develop leakage paths caused by contamination from finger prints.

8-BB-7. IN-CABINET PERFORMANCE CHECK

The Performance Check and Operational Verification procedures for the Model 5328B are given in Section 4 of this manual.

8-BB-8. EXTENDER BOARD(S)

05328-62022

The Digital Voltmeter Extender Board kit (HP Part Number 05328-82020) consists of the three boards listed in Table 8-BB-1. It allows circuit board assemblies to be extended from their plug-in connectors to allow signals to be monitored with the appropriate test equipment.

HP Part Number	Description	Use
05328-62020	One 15-pin connector and one 18-pin connector.	Extends A5
05328-62021	One 15-pin connector	Extends A6 or 7. NOTE: A5 must be placed on its extender before A6 can be extended, due to space requirements.

Simulates Digital Voltmeter front panel for front of A5 and A6.

connectors of A5 and A6.

Table 8-BB-1. DVM Extender Board Kit (HP Part Number 05328-82020)

Cable connects this

assembly to front

WARNING

THE AC LINE (MAINS) (110V/120V/220V/240V) WIRES ARE EXPOSED WHEN EITHER THE TOP OR BOTTOM IS REMOVED. USE EXTREME CAUTION DURING TROUBLESHOOTING, ADJUSTMENT, OR REPAIR.

REDUCE THE CHANCE OF PERSONAL INJURY OR DAMAGE TO THE COUNTER BY ALWAYS DISCONNECTING THE AC POWER CORD BEFORE YOU REMOVE OR REPLACE THE COUNTER'S COVERS OR ANY ASSEMBLY OR COMPONENT WITHIN THE COUNTER.

8-BB-9. REMOVAL AND REPLACEMENT INSTRUCTIONS

Removal and replacement instructions are provided for the counter's cover and the Digital Voltmeter Module (assemblies A5, A6, and A7).

8-BB-10. Instrument Cover Removal

To remove the Model 5328B's top or bottom cover:

- a. Disconnect the power cord from the rear of the instrument.
- b. Remove the screw at the rear of the cover that secures the cover to the counter's frame.
- c. Slide the cover toward the rear of the counter and lift it off.

To replace the Model 5328B's top or bottom cover, reverse the procedure above.

8-BB-11. Digital Voltmeter Installation

- a. Remove the cover as described above.
- b. Remove the plastic filler strip on the top of the front frame.
- c. Remove the 4 screws at the top and the 4 screws at the bottom of the front frame that secures the front panel section. Use a #2 posi-drive screw driver. Remove the front panel by gently pressing the front panel from the rear.
- d. Remove the Channel A and B trigger level adjust knobs and the C Channel attenuator control knob (if the C Channel is present). Use a .050 inch allen wrench, if the Option 021 is being retrofitted, remove the hex nuts from the A and B channel input controls and the Marker Output connectors so the new front panel can be installed. Use a 9/16 inch wrench or nut driver.
- e. Disconnect the coax cable from the rear of the C Channel input connector (if present). The cable connector fits a 1/4 inch wrench. Disconnect the 3 "Probe Power" wires from the A8 board with needle nose pliers.
- f. If the Option 021 is being retro-fitted, remove the hex nuts securing the C Channel input and "Probe Power" connectors to the front panel. Use 5/8 inch and 1/2 inch wrenches respectively.
- g. Install the banana plug connectors, C Channel BNC input and "Probe Power" connectors (if present) into the new front panel. Tighten the banana plug connector nuts with a 1/2 inch wrench.
- h. Install the A6 Buffer Amp board from the top of the 5328B. Insert the A6 board into the motherboard connector XA6.

- i. Route the red wire on the left side of the A6 board (as viewed from the front of the counter). Connect to the rear of the left banana plug connector labeled "HI" on the front panel. Tighten with an 8 mm wrench. (A 5/16 inch wrench, though over-size, functions satisfactorily in this low torque situation without damaging the nut.)
- j. Run the black wire on the side of the A6 board and to the banana plug connector labeled "LO". Tighten as with the red wire.
- k. From the top of the 5328B, insert the A7 VF converter board into connector XA7. Insert the A5 PS Autorange board into connector XA5.

NOTE

In the following steps, observe the connectors from the top (inside the front panel) to facilitate connection.

- 1. From the front panel of the 5328B, carefully insert the A17 Front Board so that the small connectors mate with the small pins that extend from the front of the A5 and A6 board.
- m. Connect the C Channel coax cable and "Probe Power" wires, if they were disconnected. Use a 1/4 inch wrench to gently tighten the coax connector. The color codes for the 3 wire connections are etched on the solder side of the 3 connector pins (top, blue; middle, red; bottom, black).
- n. Position all wires and cables for clearance and insert the front panel. Secure with 4 posidrive screws at the top and 4 at the bottom of the front frame.
- o. Perform the procedures listed in the In Cabinet Performance Check in section IV of this manual.

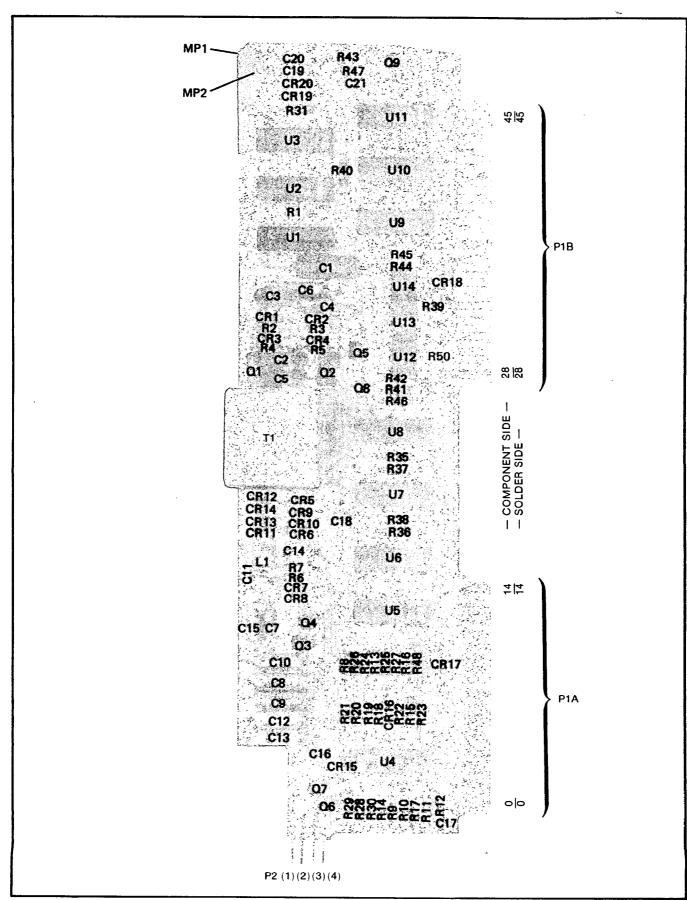
8-BB-12. Digital Voltmeter Removal

To remove the Option 021 Digital Voltmeter, proceed as follows:

- a. Disconnect the power cable from the 5328B (A safety precaution)
- b. Remove the top cover from the 5328B (Use a #2 posidrive screwdriver.)
- c. Remove the plastic filler strip on the top of the front frame.
- d. Remove the 4 screws at the top and the 4 screws at the bottom of the front frame that secures the front panel section. Use a #2 posidrive screw driver. Remove the front panel by gently pressing on the panel from the rear.
- e. Disconnect the red wire and the black wire from the rear of the banana plug connectors. Loosen the nuts with an 8 mm wrench. (A 5/16 inch wrench, though over size, functions satisfactorily in this low torque situation without damaging the nut).
- f. Remove the A17 board by gently pressingforward on the rear of the board.
- g. Remove the A5 PS Autorange board.
- h. Remove the remaining A6 and A7 boards.

8-BB-13. Digital Voltmeter (Option 021): Troubleshooting

Problems in this section are generally traceable through symptoms on the instrument display. A general troubleshooting flow chart, which branches to different boards in this section, is shown in Figure 8-BB-5.



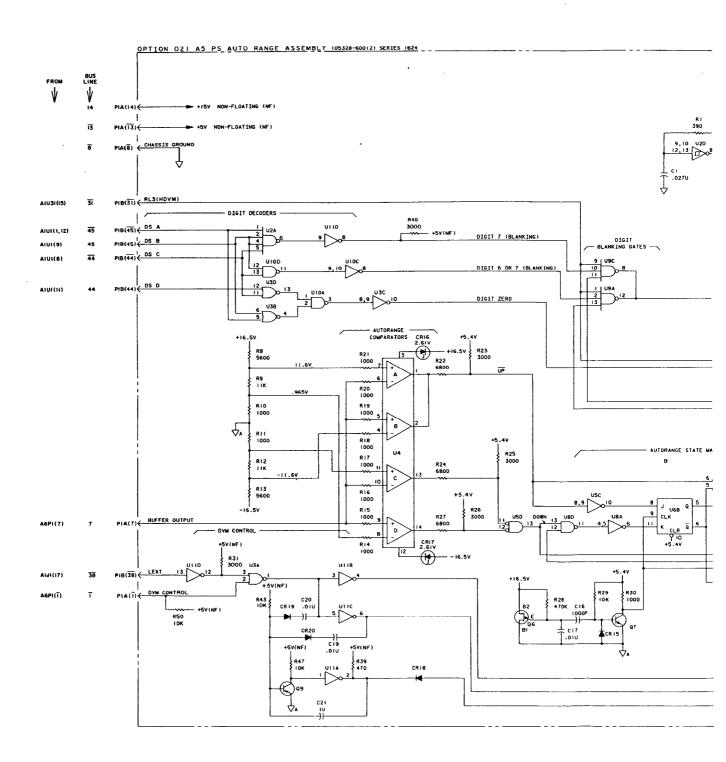
P/O Figure 8-BB-2. A5 PS Autorange Assembly

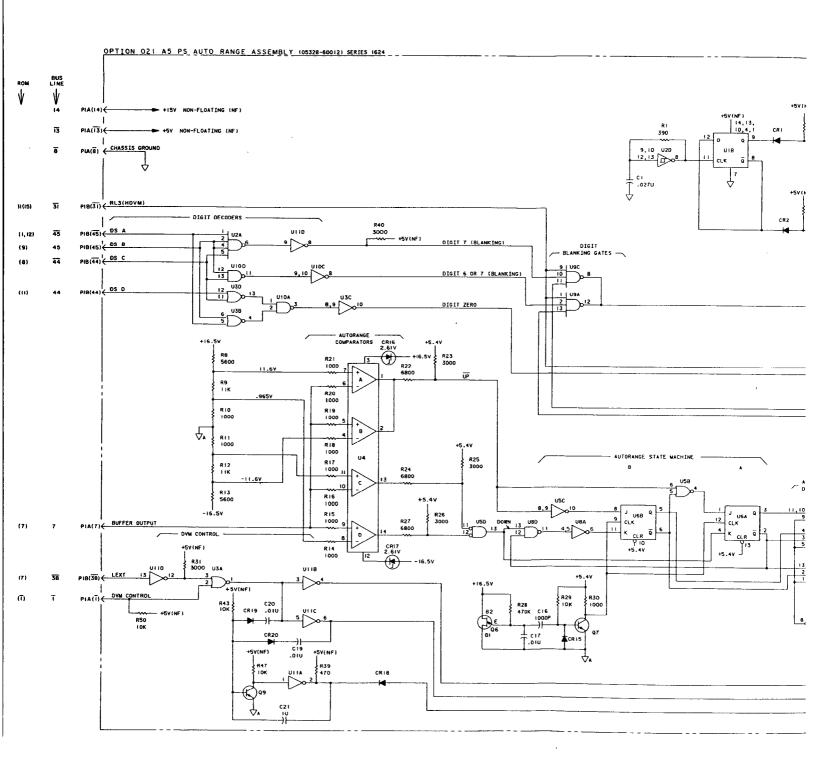
CONNECTOR P1A

0 1 2 3 4 5 6	Floating Ground +5.4V (floating) +16.5V (floating) NEGATIVE Sign	0 T 2 3 4 5 6 7	DVM Control 100V Enable 1000V Enable 10V Enable -16.5V (floating) -5.4V (floating)
7 8	Buffer Output	/ 2	Chassis Ground
9	DVM OSC	<u>8</u> 9	Chassis Glound
10	5 · 5 5 5	10	
11		11	•
12		<u>12</u> 13	
13			+5V (Non-Floating)
14	+ 15V (Non-Floating)	14	
15	_	15	

CONNECTOR P1B

	28 29	OSC
	30	
	31	RL3(HDVM)
	<u>32</u>	LRES
	<u>33</u>	
	34	
TBSB (TB code)	35	(TB code)
	38	LEXT
	<u>39</u>	
LD DCA	<u>40</u>	
	41	LÀNN ~
	42	
	₹ <u>43</u>	
DS D	44	DS C
DS B	45	DS A
	TBSB (TB code) LMG LD SW LD DCA	29 30 31 32 33 33 34 TBSB (TB code) 35 36 37 LMG 38 LD SW 39 LD DCA 40 41 42 43 DS D 44





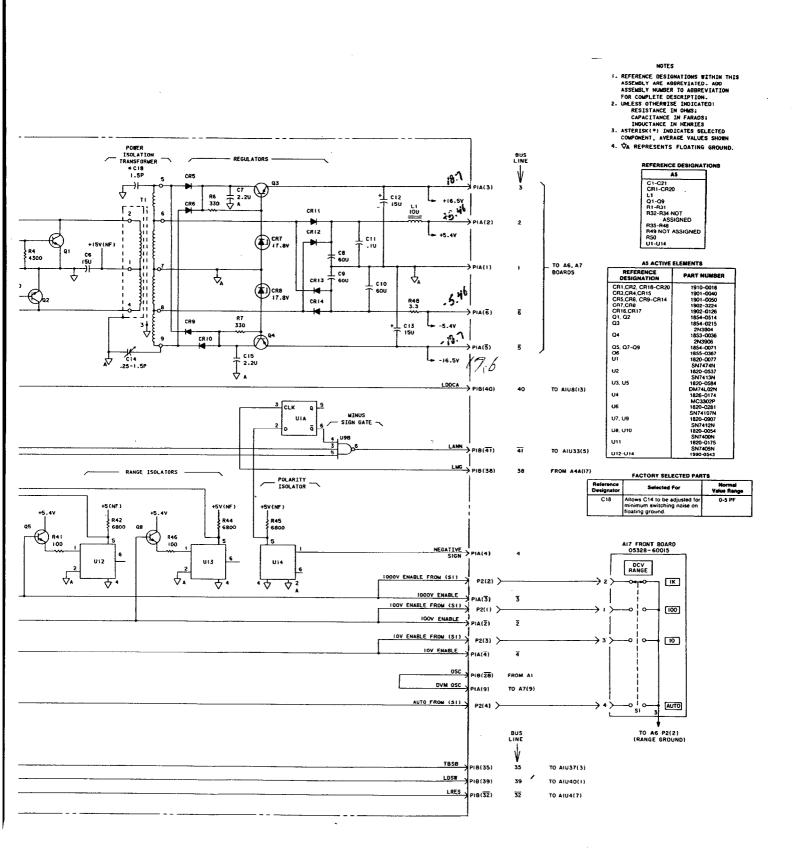
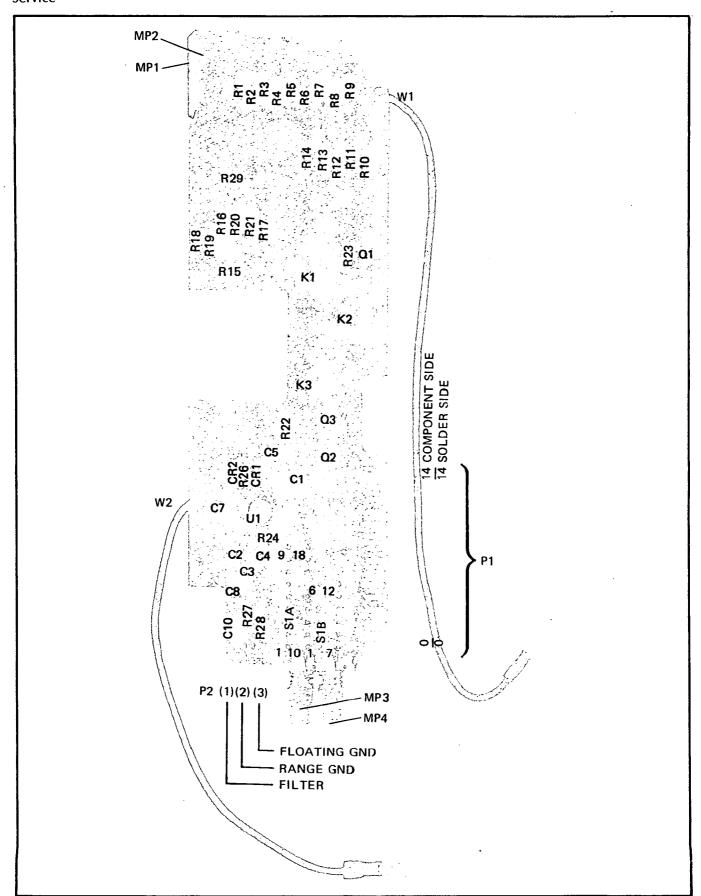


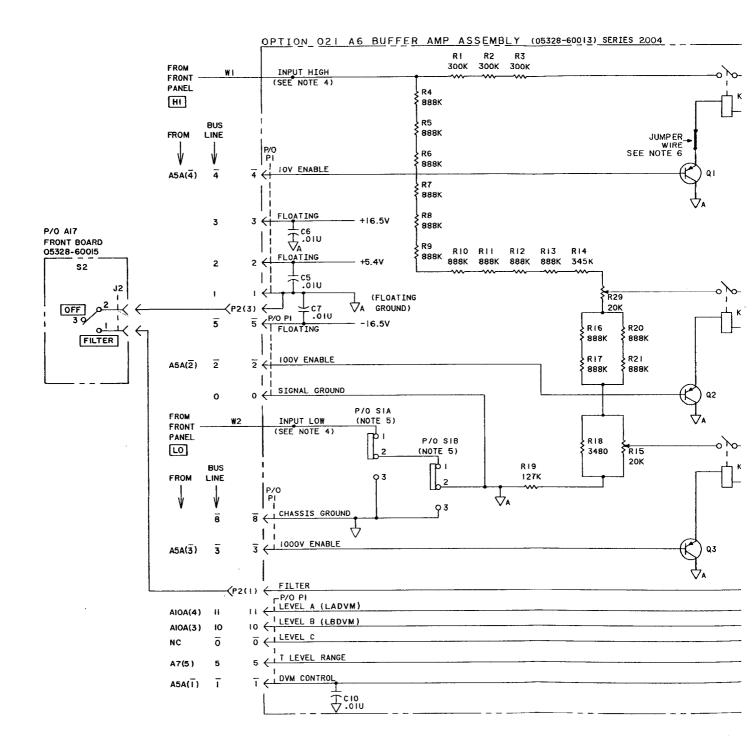
Figure 8-BB-2. A5 PS Autorange Assembly Schematic Diagram

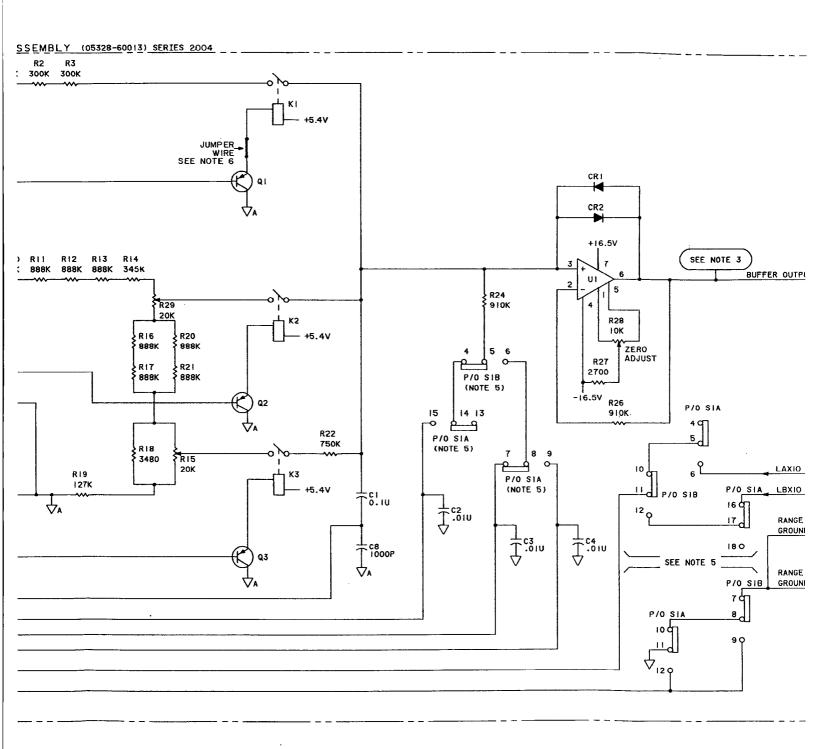


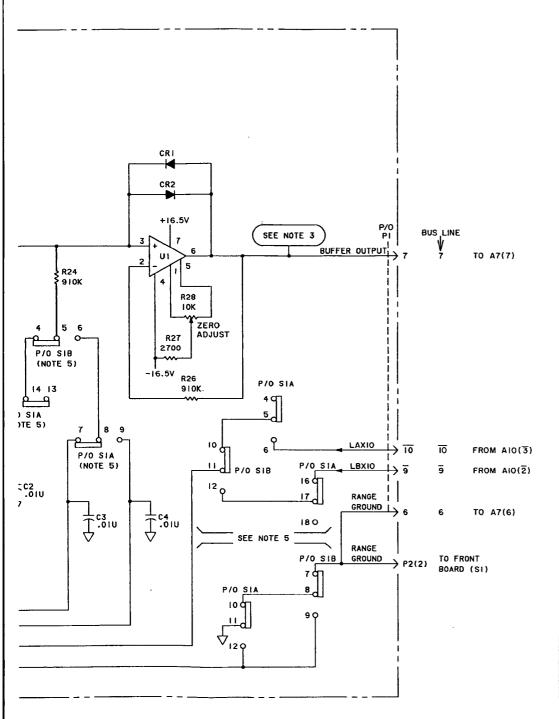
P/O Figure 8-BB-3. A6 Buffer Amplifier Assembly

CONNECTOR P1

0	Signal Ground	<u></u> 0	Level C
1	Floating Ground	1	DVM Control
2	Floating (+5.4V)	<u>2</u>	100V Enable
3	Floating (+16.5V)	<u>3</u>	1000V Enable
4		4	10V Enable
5	T Level Range	5	Floating (-16.5V)
6	Range Ground	· <u>6</u>	
7	Buffer Output	7	
8		8	Chassis Ground
9		<u> </u>	Level B RANGE (LBX10)
10	Level B (LBDVM)	10	Level A RANGE (LAX10)
11	Level A (LADVM)	11	·

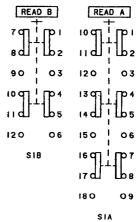






NOTES

- I. RÉFERENCE DESIGNATIONS WITHIN THIS ASSEMBLY ARE ABBREVIATED. ADD ASSEMBLY NUMBER TO ABBREVIATION FOR COMPLETE DESCRIPTION
- 2. UNLESS OTHERWISE INDICATED: RESISTANCE IN OHMS; CAPACITANCE IN FARADS.
- 3. GUARD TRACE CONNECTED TO THIS POINT REDUCES HIGH VOLTAGE LEAKAGE CURRENTS.
- 4. INPUT HIGH AND INPUT LOW CONNECT TO THE BOARD VIA CABLES WI AND W2.
- 5. SWITCH SIA & SIB DIAGRAM:



 JUMPER WIRE PART NUMBER IS 8159-0005.

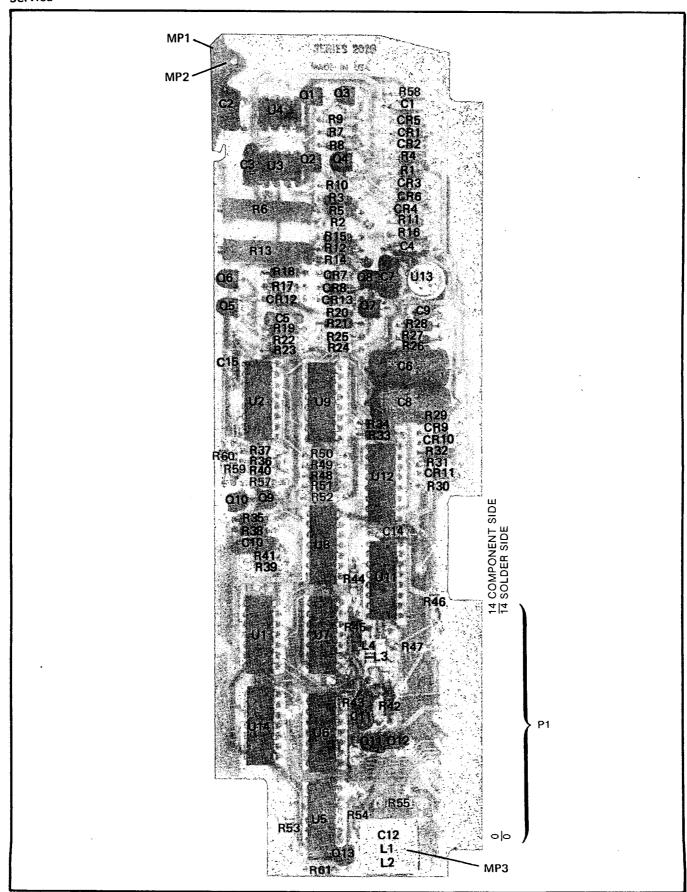
REFERENCE DESIGNATIONS

	A6
Г	C1-C8, C10
1	CR1, CR2
L	K1-K3
1	Q1-Q3
1	R1-R22, R24,
ı	R26-R29
ı	S1
ı	U1 `
t	(R23, R25 NOT
I	ASSIGNED)

A6 ACTIVE ELEMENTS

REFERENCE DESIGNATION	PART NUMBER	
CR1, CR2	1901-0376	
Q1-Q3	1853-0020	
U1	1826-0250	
	Special LH0042CH	

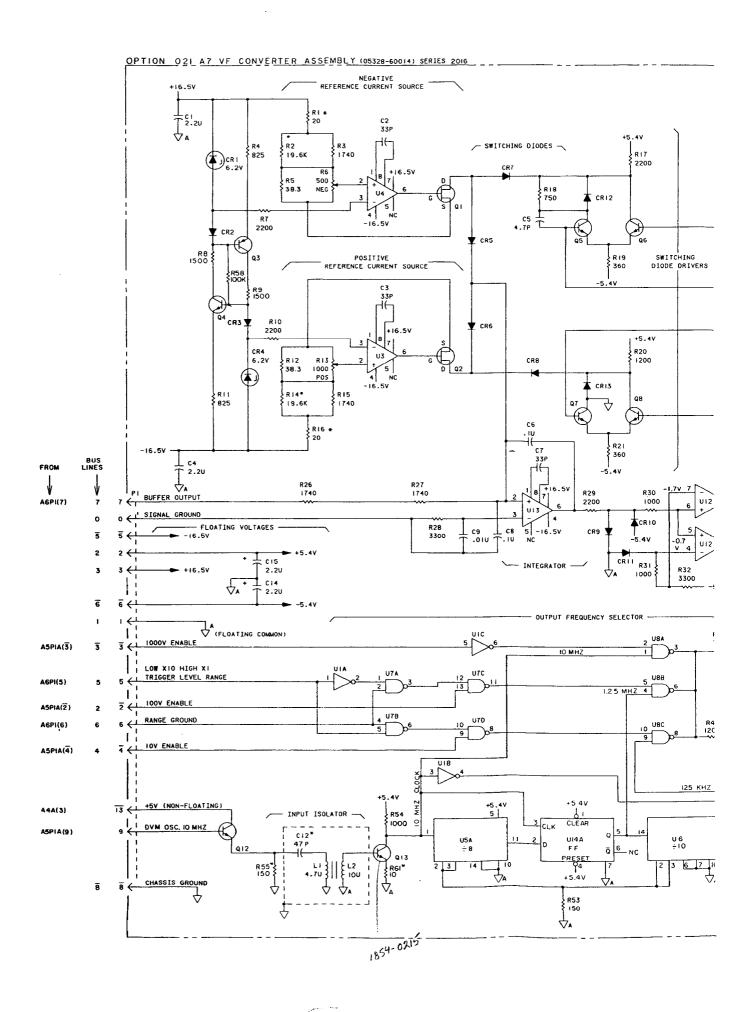
Figure 8-BB-3. A6 Buffer Amplifier Assembly Schematic Diagram

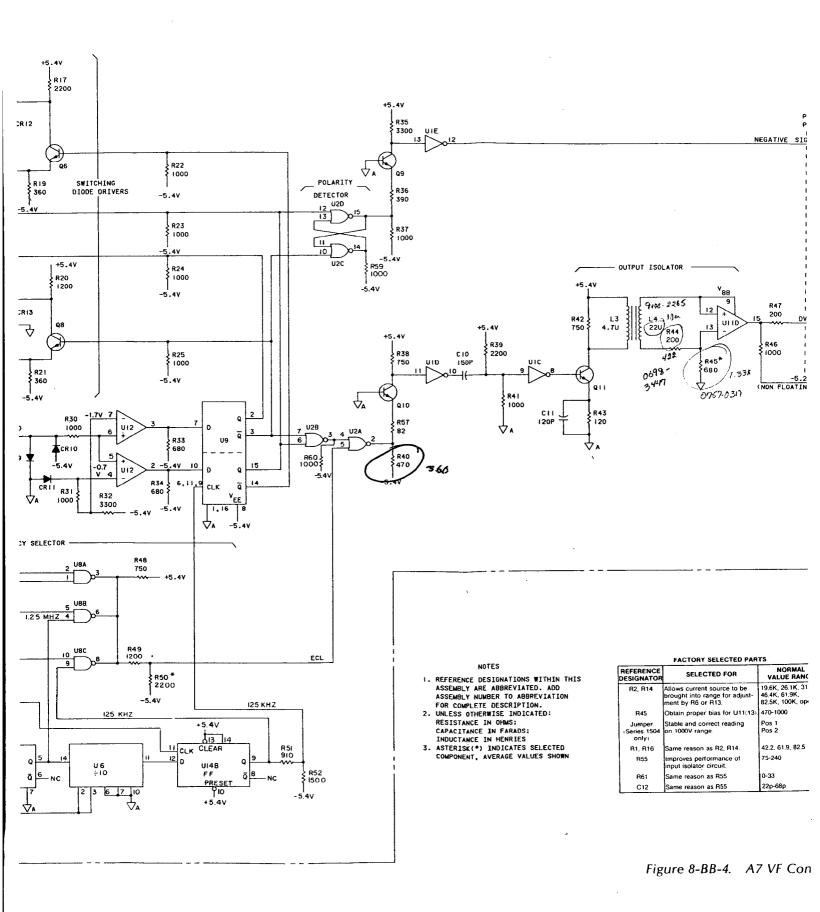


P/O Figure 8-BB-4. A7 VF Converter Assembly

CONNECTOR P1

0 1 2 3	Signal Ground Floating Ground +5.4V (from A5) +16.5V (from A5)	0 1 2 3	100V Enable 1000V Enable
4	NEG Sign	4	10V Enable
5	T Level Range	5	-16.5V (from A5)
6	Range Ground	<u> </u>	-5.4V (from A5)
7	Buffer Output	7	` ,
8		8	· Chassis Ground
9	DVM OSC	<u>9</u>	
10		10	
11		11	
12	DVM	12	
13	-5.2V (Non-Floating)	13	+5V (Non-Floating)





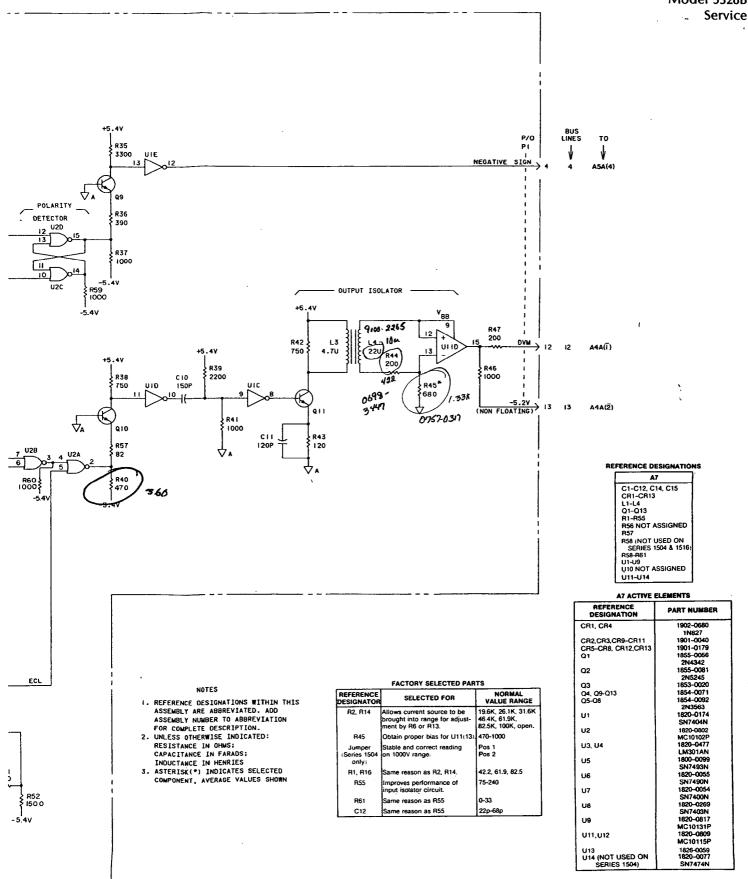


Figure 8-BB-4. A7 VF Converter Assembly Schematic Diagram

OPTION OZ! DVM TROUBLESHOOTING

8-119

Figure 8-B8-5. Digital Voltmeter Troubleshooting Flowchart

SUSPECT CUTPUT OECODER UT

AS TOOK GENERATOR

SUSPECT U4

REPAIR YOLTAGE DIVIDER METBORK

SUSPECT

8-BC-1. CHANNEL "C" INPUT (OPTION 031 OR PART OF OPTION 050)

- The Channel "C" Option (Option 031) gives the Model 5328B a wider frequency range (90 MHz to 1300 MHz). It also offers
- 20-mV sensitivity
- 50-ohm fuse-protected input
- a ninth digit in the Model 5328B's numeric display

The (optional) Channel "C" circuitry provides the Model 5328B with an input channel whose frequency range is 90 MHz to 1300 MHz. A 50-ohm fuse-protected amplifier and Schmitt trigger feed the decade divider. Latches in this circuitry strobe the ninth (least-significant) digit from the module onto the Data Bus and into the numeric Display. In any FUNCTION that does not require an input from this module, the main-controller ROM lines turn off the module's output strobing circuitry, which turns off the display's ninth digit.

8-BC-2. PRINCIPLES OF OPERATION

The "C" Channel circuitry is on two printed circuit boards.

The main board is installed in the Model 5328B's "A8" position. The second board, identified by reference designator "A8A1", plugs into the "A8" board and is held in place with two screws. For the discussion that follows, refer to the block diagram, Figure 8-BC-1.

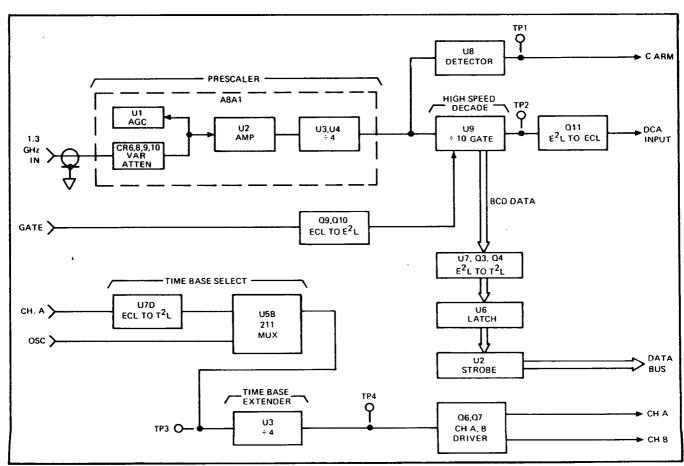


Figure 8-BC-1. "C" Channel Block Diagram

The 1300 MHz Front End Assembly (A8A1) contains circuitry that limits, amplifies, and prescales (divides) by four the input signal (which must be in the 90 to 1300 MHz range). This assembly contains an amplifier (A8A1U2) which provides approximately 20-24 dB of gain. Circuits A8A1U3 and A8A1U4 are binaries that divide the input signal frequency by four. An automatic gain control (AGC) circuit is also included in the A8A1 assembly; this circuit provides additional protection under overload conditions.

The signals sent from A8A1 to the main board (A8) have a maximum frequency of 325 MHz (1300 MHz/4). The A8 board contains a high-speed decade divider with output level conversion, latch, and strobe circuits. The board also has circuits for arming, time base select, extender, and drive. A high-speed gate, contained in U9, is driven by an ECL level from the Function Selector Board (A4).

8-BC-3. A8A1 1300 MHz Front End

Input signals are routed through Fuse J310F1 to the 1300 MHz Front End limiter circuitry. Diodes CR1, CR2, CR4, and CR7 form the limiter circuitry and limit the input to approximately 5V rms. A voltage-controlled attenuator composed of PIN diodes CR5, CR6, and CR8 provides variable attenuation as determined by an AGC circuit.

Signal output from the variable attenuator is supplied to U2, which provides 20-24 dB of gain. The signal from U2 is coupled by C18 to U3(3). Circuit U3 divides the signal frequency by two and routes it to U4, where it is again divided by two. Since U3 has no Schmitt trigger, it is biased for maximum sensitivity. With U3 biased for maximum sensitivity, it has a tendency to oscillate with no input signal applied. To maintain high sensitivity and prevent oscillations, R22 is adjusted to desensitize U3 when the ARM line is "high" and produce high sensitivity when the ARM line is low. This allows the counter's readings to "snap-on" from no input signal to the exact reading when a signal is applied. The amount of "snap-on" feedback is controlled by A8R12. The greater the "snap-on" feedback, the less the possibility of partial counts, but also the lower the sensitivity.

As the input level increases, the level at U2(1) increases. This level is also connected to a bridge circuit consisting of CR11, CR12, and associated resistors including R13 which balances the bridge. Resistor R13 is adjusted to balance the bridge with a -5 dBm input signal. With small input levels, the bridge allows little dc to flow, resulting in U1(6) remaining "high". As the input exceeds -5 dBm, the rectified bridge outputs drive U1(2) positive with respect to U1(3). With the inverting input of U1 positive with respect to the non-inverting input, the output of U1 goes "low". When U1(6) goes "low", it reduces the amount of current through R9, A8R43, A8R44, L4, L1, CR6, CR8, and R11. As the current decreases, the resistance of the two PIN diodes (CR6, CR8) increases, attenuating the incoming signal. A decrease in current causes the junction of R11 and CR8 to become less positive. This allows more dc current to flow through CR9, R10, R1, R6, R7, CR5, and R2. The increased current through CR5 and CR9 decreases their resistance and allows more input signal to be routed to ground through C5 and C9.

8-BC-4. A8 1300 MHz "C" Channel

The output signal at A8A1(9) has a maximum frequency of 325 MHz. This signal is rectified by Hot Carrier Diode CR8 and is sent to the Arming circuit, U8. When the voltage across C14 goes above ground, U8(6) goes "high", arming the counter. The counter responds by pulling the "HDS" line (A8P1(7)) "low", thus allowing the "snap-on" effect to take place on A8A1 (see A8A1 description above). Variable resistor R12 controls the amount of "snap-on" feedback. Increasing the voltage at Q8's base causes lower sensitivity, but also provides less susceptibility to partial counts.

The signal at A8A1(9) is also sent to the input of decade U9. Circuit U8 is the least-significant-digit (LSD) decade in FREQ C and RATIO C/A functions. The signal supplied to U9 is gated, divided by ten, and differentially outputted to the A4 board on the "C" and " \overline{C} " lines. The "MG" and " \overline{MG} " outputs at A8P1(12) and A8P1($\overline{11}$) arrive from the A4 board and drive Q9 and Q10 to develop a single gate signal connected to U9(7). This signal causes gating of the input signal in U9.

LSD data from U9, converted from E²CL to TTL levels by U7A, U7B, U7C, Q3 and Q4, is supplied to the "D" input of latch U6. During transfer, LGR at A8P2(4) goes "low", causing U1(10) to go "high". This "high" latches the information at U6's inputs to its outputs.

When the inputs of U4B ("LDDCA", "HEC", and "DSD") are all "high", indicating strobing of the LSD data, U4(8) is "low", causing a "high" at U1(13). The "high" at U1(13) enables U2 and allows data latched into U6 to be transferred on the "DA" through "DD" lines.

When the Model 5328B is in FREQ C mode, "FB" is "high". This "high", at U5(1), connects oscillator pulses at U5(6) to U5(7). Since the input frequency is prescaled by four, oscillator pulses are routed through divide-by-four circuitry composed of U3B and U3A. With U5(1) "high", the oscillator-pulses-divided-by-four are routed from U5(3) to U5(4) and the "high" at U5(10) is supplied to U5(9). On the negative-going edge of TTL pulses at U5(4), C4 acts as a differentiator — causing emitter follower Q6 to output negative pulses on the " \overline{A} " line. These pulses (on the " \overline{A} " line) are sent to the A4 board, where they are used as the time base input. The "high" at U5(9) causes the " \overline{B} " output to be "high". Also, with U5(1) "high", U1(4) is driven "low", causing Q1 to drive "B ARM" "low". With "A ARM" "high", any "A" channel input is disabled. A "low" on "B ARM" allows the "B" channel to function normally and arm the counter in the ARM ON mode.

In FREQ C and RATIO C/A, both "HEC" (A8P2(7)) and "FC" (A8P2(9)) lines will be "high", giving a "low" at U4(12). The "low" at U4(12) enables U5A, U5B, U5C, and U1A.

In a RATIO C/A function, the "FB" line is "low". This "low", at U5(1), switches the "low" at U5(14) to U5(12). With U5(12) "low", Q2 drives the "A ARM" line "low", enabling "A" channel input. the "low" at U5(12) is also coupled via CR1 to U1(5), causing U1(4) to be "high". A "high" at U1(4) causes Q1 to drive "B ARM" "high". "B ARM" being "high" diaables any "B" channel input. The " \overline{A} " signal, at A8P1($\overline{13}$), arrives from the A10 board and is routed through one-shot U7D to appear at U5(5). With "FB" "low", the " \overline{A} " signal at U5(5) is connected to U5(7). This signal, coupled to U3(11), is divided by four by U3B and U3A and applied to U5(3,11). With U5(1) "low", the " \overline{A} -divided-by-four" signal is connected from U5(11) to U5(9), and the "high" at U5(2) is connected to U5(4). On the negative-going edge of TTL pulses at U5(9), C5 acts as a differentiator, causing emitter follower Q7 to output negative spikes. The output of Q7 is connected to the " \overline{B} " line and sent to the A4 board where it is used as the time base input. With U5(1) "low", the "high" at U5(2) is connected to U5(4), producing a "high" at the " \overline{A} " output via Q6.

When the Model 5328B includes the "C" Channel, the FUNCTION "EVENTS C, $A \rightarrow B$ " is an invalid one. Using this FUNCTION will cause a display of all zeroes and decimal points and the the counter will not gate.

In summary, for FREQ C and RATIO C/A functions, input signals at the "C" input are processed identically. The operational difference between the two functions is which signal is routed to the time base and how it arrives there. In FREQ C, the oscillator signal is divided by four and routed via the " \overline{A} " (A8P1($\overline{13}$)) line to the time base multiplexer on the A4 board. In RATIO C/A, " \overline{A} " pulses, from the A10 board, are divided by four and routed via the " \overline{B} " line to the time base multiplexer on the A4 board

WARNING

110V/120V/220V/240V AC SUPPLY WIRES ARE EXPOSED WHEN EITHER THE TOP OR BOTTOM COVER IS REMOVED. USE EXTREME CAUTION DURING TROUBLESHOOTING, ADJUSTMENT, OR REPAIR. TO REDUCE THE CHANCE OF PERSONAL INJURY OR DAMAGE TO EQUIPMENT, DISCONNECT THE MODEL 5328B FROM ITS OPERATING AC POWER LINE BEFORE YOU REMOVE OR REPLACE ITS COVERS OR ANY OF ITS INTERNAL ASSEMBLIES OR COMPONENTS.

8-BC-5. IN-CABINET PERFORMANCE CHECK

The Performance Check and Operational Verification procedures for the Model 5328B are given in Section 4 of this manual.

8-BC-6. EXTENDER BOARD(S)

A special extender board (HP Part Number 05328-62016) is available to make testing of certain parts of the Model 5328B either easier.

- One of these boards is required to extend the A10 or A14 Assembly.
- Two of these boards are required to extend the (optional) Frequency "C" Assembly (A8).

8-BC-7. REMOVAL AND REPLACEMENT INSTRUCTIONS

Removal and replacement instructions are provided for the counter's cover and the C Channel Module (A8 and A8A1).

WARNING

THE AC LINE (MAINS) (110V/120V/220V/240V) WIRES ARE EXPOSED WHEN EITHER THE TOP OR BOTTOM IS REMOVED. USE EXTREME CAUTION DURING TROUBLESHOOTING, ADJUSTMENT, OR REPAIR.

REDUCE THE CHANCE OF PERSONAL INJURY OR DAMAGE TO THE COUNTER BY ALWAYS DISCONNECTING THE AC POWER CORD BEFORE YOU REMOVE OR REPLACE THE COUNTER'S COVERS OR ANY ASSEMBLY OR COMPONENT WITHIN THE COUNTER.

8-BC-8. Instrument Cover Removal

To remove the Model 5328B's top or bottom cover:

- a. Remove the power cord from the counter.
- b. Remove the screw at the rear of the cover that secures the cover to the counter's frame.
- c. Slide the cover toward the rear of the counter and lift it off.

To replace the Model 5328B's top or bottom cover, reverse the procedure above.

8-BC-9. C Channel Installation

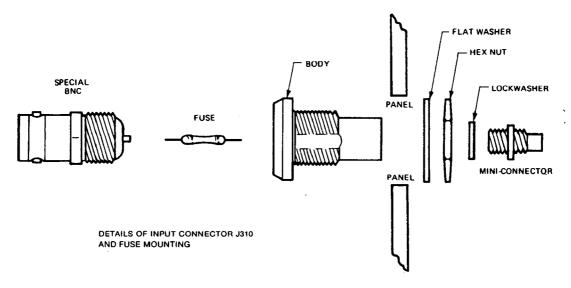
- a. Remove the cover as described above.
- b. Remove the plastic filler strip on the top of the front.
- c. Remove the 4 screws at the top and the 4 screws at the bottom of the front frame that secures the front panel section. Use a #2 posi-drive screw driver. Remove the front panel by gently pressing on the front panel from the rear.
- d. If the Option 031 is being retrofitted, remove the Channel A and B trigger level adjust knobs. Use a .050 inch wrench. Remove the hex nuts from the A and B channel input trigger level controls and the Marker Output connectors so the new front panel can be installed. Use a 9/16 inch wrench or nut driver.







- e. Disconnect the red wire and the black wire from the rear of the banana plug connectors for the Digital Voltmeter input. (If the DVM is present.) Use an 8 mm wrench. (A 5/16 inch wrench, though oversize, functions satisfactorily in this low torque situation without damaging the nut.)
- f. If the Option 031 is being retrofitted, and the DVM is present, remove the hex nuts securing the DVM banana plug connectors so the new front panel can be installed. Use a 1/2 inch wrench.
- g. Solder 2 terminal pins in XA9 plated through holes 16 and $\overline{16}$.
- h. Install Option 031 C Channel board into XA8.
- i. Install the fuse holder and "Probe Power" connector assembly into the new front panel. Details of the connector assembly are shown below. Install the the SMC connector into the rear of the fuse holder. Insert the fuse and install the BNC connector into the front of the fuse holder.



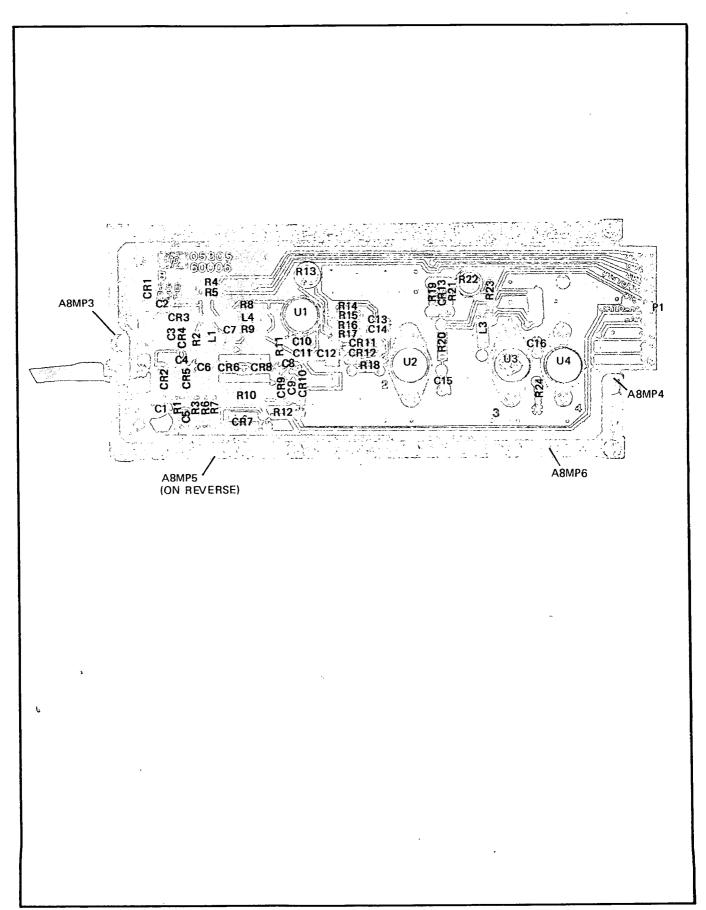
- j. Connect the cable from the C Channel board to the SMC connector of the fuse holder. Connect the wires from the "Probe Power" cable to pins on the front of the A8 board (the wire colors are marked on the board).
- k. Connect the wire from the A8 board marked "A" to the terminal in XA9(16) and the wire marked "B" to the terminal in XA9(16°).
- I. Connect the banana plug connectors to the new front panel and attach the red wire and the black wire of the DVM to the banana connectors. Insert the front panel into the front frame. Secure with 4 posidrive screws at the top and bottom of the front frame.

8-BC-10. C Channel Removal

Removal of the Option 031 C Channel is accomplished in essentially the reverse order as described above.

8-BC-11. Frequency C (Option 031): Troubleshooting

Problems in this section are easily distinguishable because they would occur only in measurements involving frequency C. If the problem occurs in other "non Frequency C" functions then the fault probably resides in some other section of the counter and should be tested before troubleshooting this section. The troubleshooting chart in Figure 8-BC-4 describes failure symptoms to faulty components on the A8 board.



P/O Figure 8-BC-2. A8A1 1300 MHz Front End Assembly

The waveforms below are included for ease of troubleshooting. Use procedure below and connect an HP 1725 Oscilloscope to location next to waveform.

PROCEDURE:

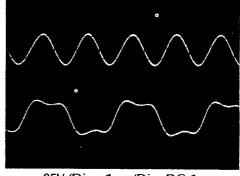
Connect HP 8660 OUTPUT, using a BNC cable to 5328B C channel INPUT.

U3(1) 0V -

Set 8660B to 100 MHz at 20 mV rms.

Set 5328B channel C ATTENUATOR to Min (cw).

U4(1) 0V -



.05V/Div, .1 μs/Div, DC 1 ALT MAG-X10

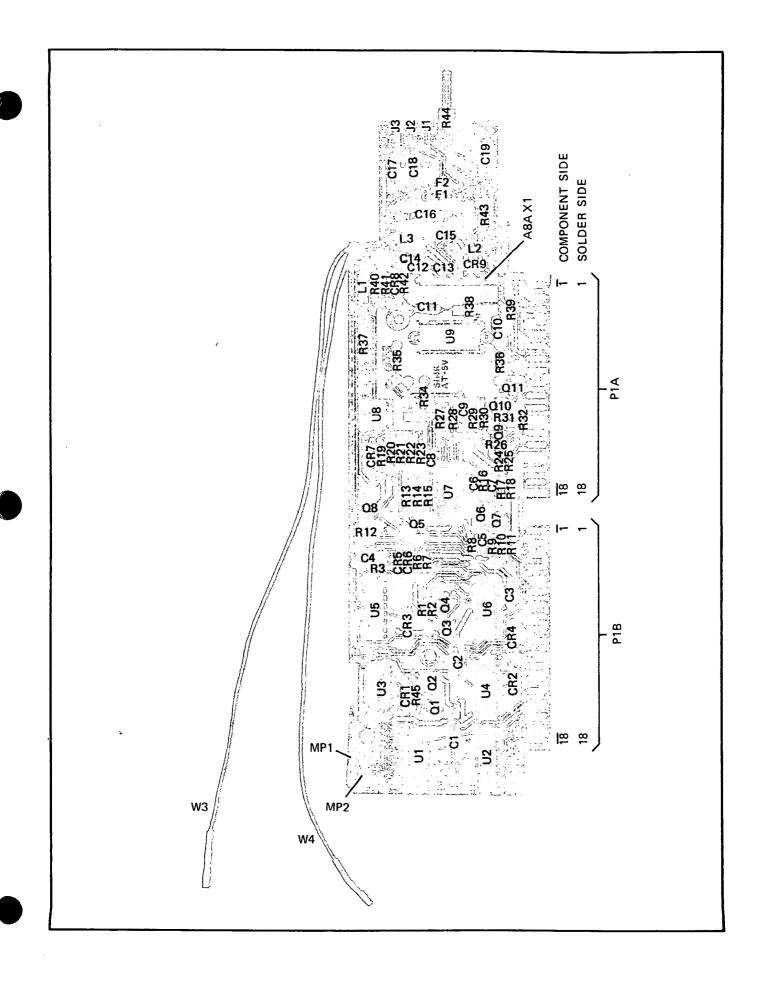
The following table contains dc voltages for various points on the A8A1 assembly with no input signal applied.

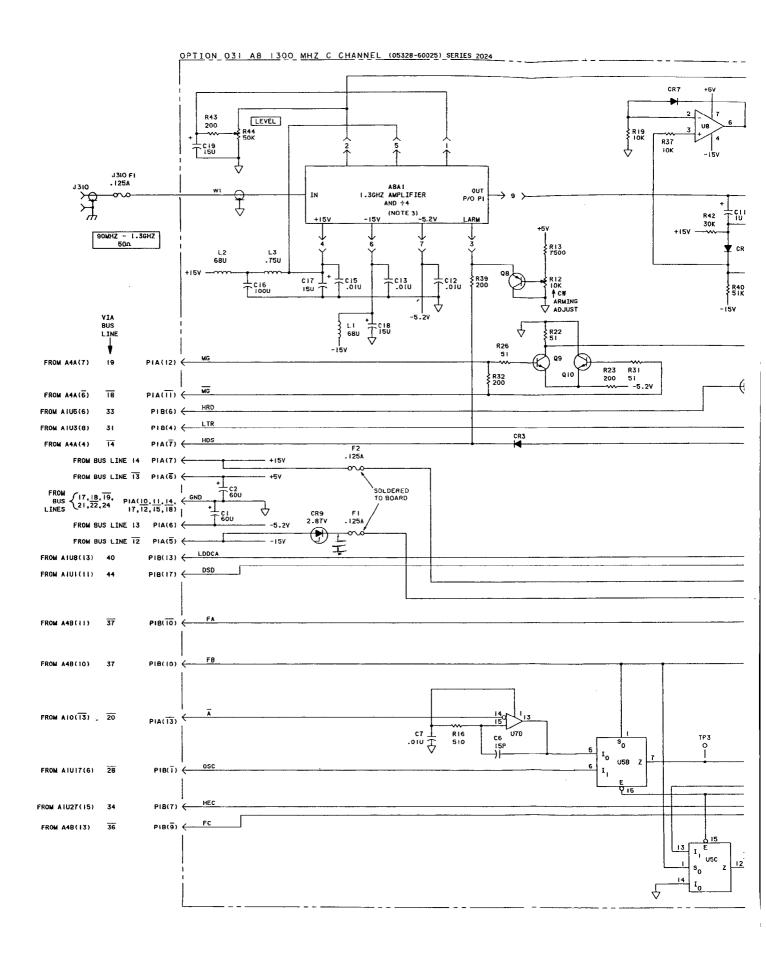
NOTE

Voltages may vary depending on the adjustment of R13.

LOCATION	CHANNEL C ATTENUATOR	
	MAX (CCW)	MIN (CW)
ANODE CR5	3.1V	10.5V
CATHODE CR5	2.5V	10.2V
ANODE CR6	0V	12V
CATHODE CR6	0V	11.2V
CATHODE CR8	1.4V	10.5V
ANODE CR9	2.0V	10.2V
CATHODE CR10	.82V	.8V
CATHODE CR12	8.0V	8.0V
CATHODE CR11	8.0V	8.0V

Figure 8-BC-2. A8A1 1300 MHz Front-end Assembly Schematic Diagram





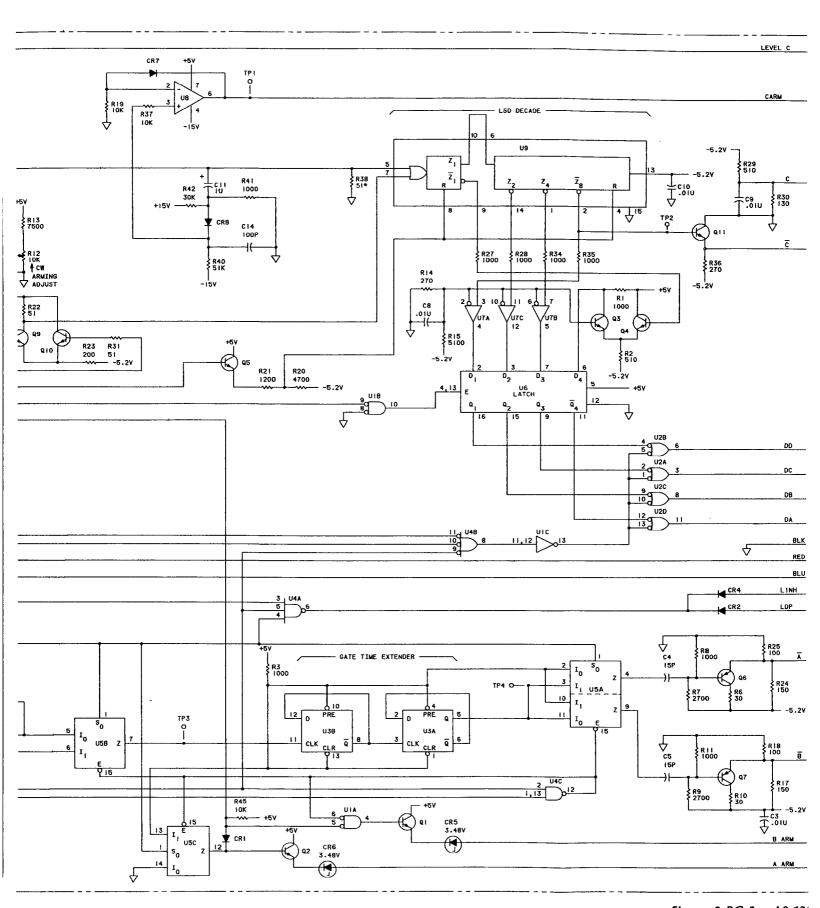


Figure 8-BC-3. A8 13(

NOTES

- REFERENCE DESIGNATIONS WITHIN THIS
 ASSEMBLY ARE ABBREVIATED. ADD
 ASSEMBLY NUMBER TO ABBREVIATION
 FOR COMPLETE DESCRIPTION.
- 2. UNLESS OTHERWISE INDICATED: RESISTANCE IN OHMS; CAPACITANCE IN FARADS INDUCTANCE IN HENRIES
- 3. ABAI ALSO HAS AGC AND ATTENATOR

TABLE OF ACTIVE COMPONENTS

REFERENCE DESIGNATIONS	HP PART NUMBERS
CR1,CR2,CR3,CR4	1910-0016
CR5,CR6	1902-3059
CR7	1901-0040
CR8	1901-0535
CR9	1902-3024
Q1,Q2,Q3,Q4,Q5, Q9,Q10,Q11 Q6,Q7,Q8	1854-0215 1853-0036
101	1820-0328
112	1820-0269
U3	1820-0277
U4	1820-0068
U5	1620-0616
U6	1820-0301
1 07	1820-1052
U8	1820-0493
U9	1620-1019

REFERENCE DESIGNATIONS A8 A1 C1-C19 CR1-CR9 F1-F2 J1-J4 L1-L3 O1-O11 R1-R3 R4-R6 Not Assigned R6-R32 R33 Not Assigned R34-R45 TP1-TP4 U1-U9 XU9

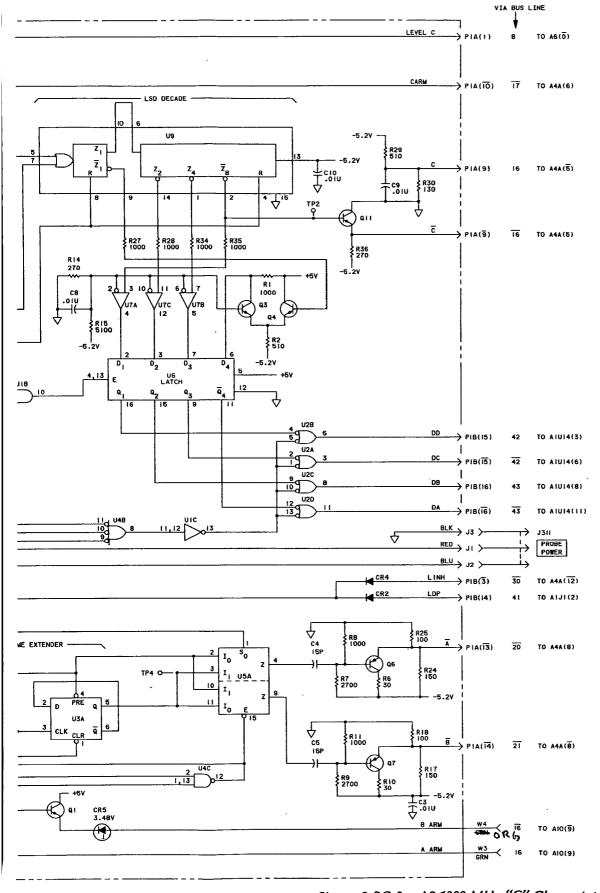
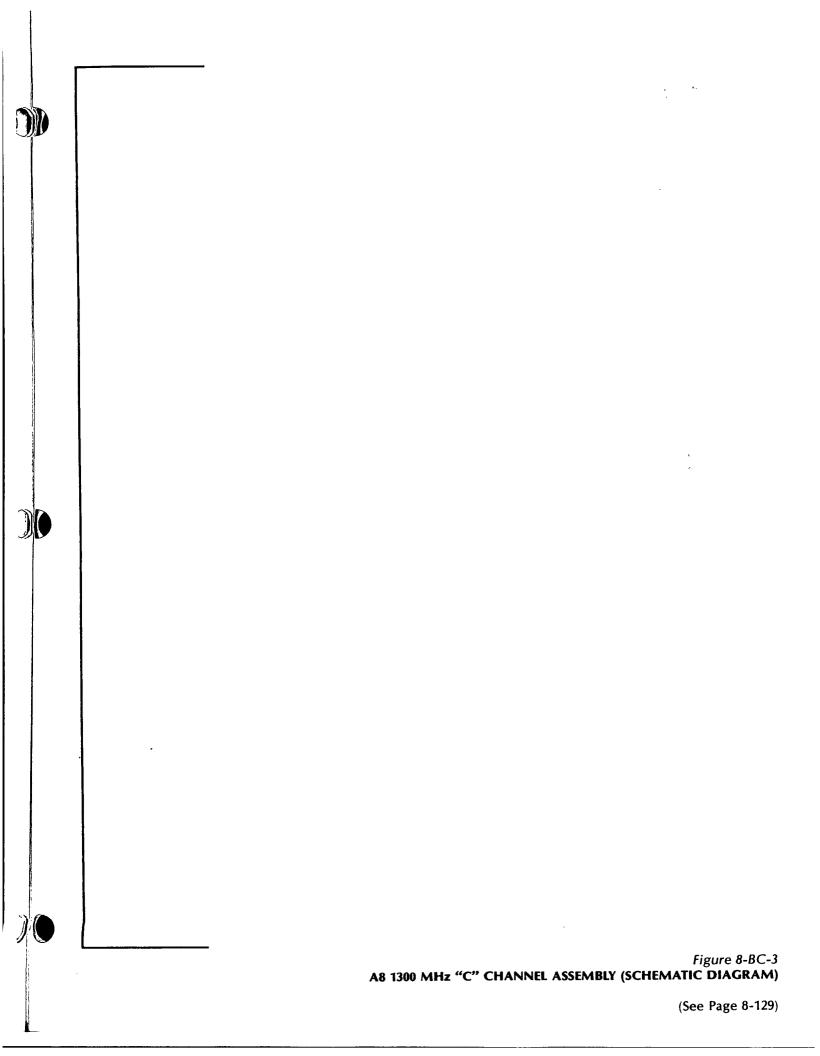
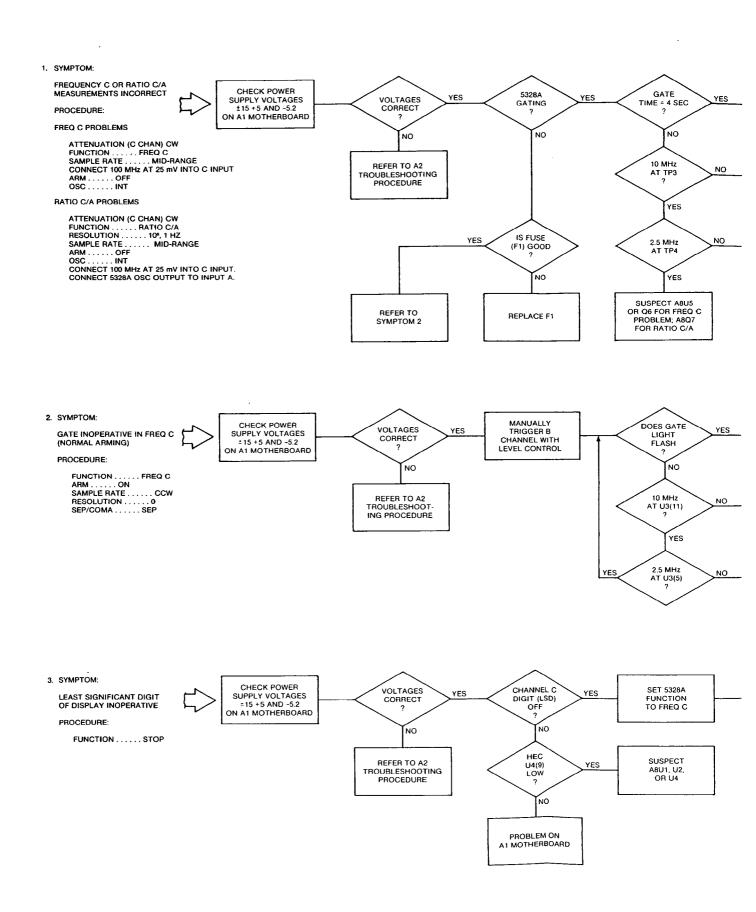
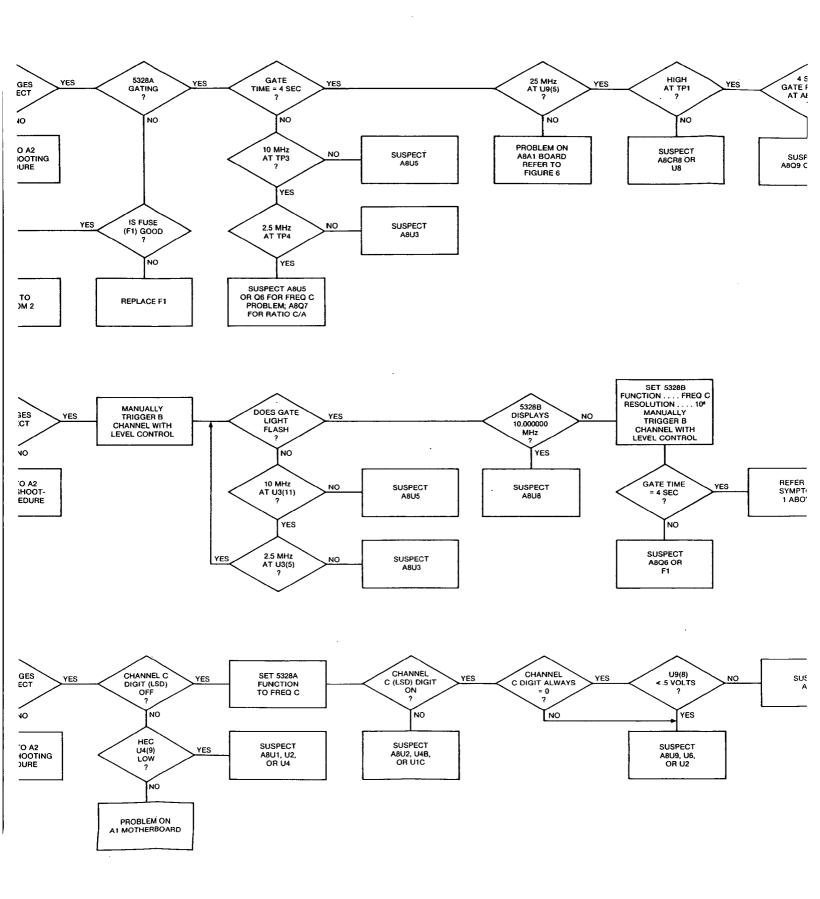
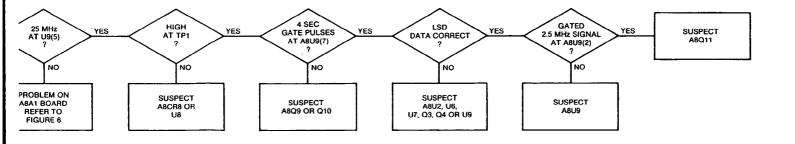


Figure 8-BC-3. A8 1300 MHz "C" Channel Assembly Schematic Diagram









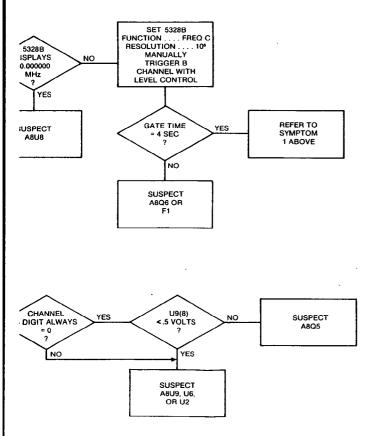


Figure 8-BC-4. "C" Channel Assembly Troubleshooting

8-C-1. HEWLETT-PACKARD INTERFACE BUS (HP-IB) SECTION

The HP-IB Assembly provides for control of the Model 5328B via the HP-IB. Plugging into the main instrument bus through a ribbon cable, the internally-mounted HP-IB board controls:

- FUNCTION
- TIME BASE
- cycle rate
- arming
- virtually all of the Model 5328B's controls, except for the DVM and the Programmable Input Module front-panel controls.

A special programming system in the HP-IB board allows any future module to be programmed through the present HP-IB system.

8-C-2. Principles of Operation

Operation of the Hewlett-Packard Interface Bus (HP-IB) is described in the following paragraphs.

8-C-3. STATE COUNTERS

Refer to the schematic diagram, Figure 8-C-2, as you read the material below.

The state of the ASM (Arithmetic State Machine) ROM (Read-Only Memory) is determined by State Counters A15U14 and A15U23, which form an eight-bit presettable binary counter.

When A15U25(1) is "low", the counters will always increment. When A15U25(1) is "high", the counters will "preset" ("jump" to another state in the program), if the output of A15U30C is "high". The preset address is supplied to the State Counter's input from the ROM.

The output of A15U30C is determined by the "not" bit from the ROM (through A15U21E) and the output of the Qualifier Flip-flop (A15U11A). The preprogrammed state of the "not" bit determines whether a "high" or "low" output of the Qualifier Flip-flop will result in a jump in the program. (This is shown in the ASM Operational Flowchart, by the of the letter "N" in a "decision diamond" symbol.) The preset (jump) is synchronous, and occurs only when:

- A15U14(9) and A15U23(9) are "low" and:
- there is a rising edge at A15U14(2) and A15U23(2).

Flip-flop A15U31A synchronizes the resetting of the State Counters to occur at the proper time.

8-C-4. ARITHMETIC STATE MACHINE (ASM) OSCILLATOR

Refer to the ASM Oscillator Timing Diagram, Figure 8-C-1, as you read the material below.

The ASM oscillator circuit provides three separate phases of clock outputs.

Schmitt trigger A15U18A is the fundamental oscillator element, using hysteresis to develop oscillation.

The output of A15U18A (through A15U13) strobes storage latches A15U11A and A15U11B, A15U15, A15U19, A15U24, A15U26, A15U28, A15U33, A15U31B and A15U34.

The output of A15U18A is also sent through a delay circuit (consisting of resistor A15R14 and capacitor A15C4) into A15U18B to provide another phase of the clock output that determines the state of the ASM.

In addition, the output of A15U18A is sent through A15U30A to provide a third clock phase which is applied to A15U31A. The output of A15U31A resets the 8-bit State Counter synchronously at power-up or when the IFC signal occurs. (Synchronous reset prevents loading the storage latches with erroneous data.) The IFC signal also resets A15U26 (ASM storage).

The power-up reset circuit consisting of A15U18C and A15U18D clears all storage elements.

8-C-5. BUS INTERFACE

The bus interface circuit consists of bus line termination resistors, data output drivers, and data input buffers.

Resistors A15R29 and A15R30 form the line termination networks. Integrated circuit A15U4 buffers the bus line inputs.

Integrated circuits A15U5, A15U10, and A15U16 are high-current drivers that drive the bus line output.

The "ATN" signal is sent through A15U9A and A15U29D to ensure that the gates connected to bus lines "DIO1" through "DIO7" and "DAV" do not output when "ATN" goes "true".

The DAO signal from A15U24(9) arms the DAC signal through A15U17B to ensure that DAC goes "false" within a few gate delays after "ATN" goes "true". (In some cases, the DAC response from the ROM may be too slow.) After "ATN" is "true", "DAO" is set to a "0" to allow normal operation of the DAC line.

8-C-6. END OF MEASUREMENT

When a measurement has been completed, flip-flop A15U11B is set. This flip-flop is clocked by the closing edge of the "LMG" signal.

Diode A15CR2 and transistor A15Q3 keep A15U11B from going to its "1" state when "LRES" is "low" or "HRD" is "high". (During these times, the counter is being reset, and the noise which appears on the "LMG" line, could trigger A15U11B).

8-C-7. QUALIFIER MULTIPLEXERS

Five 8-to-1 multiplexers are connected to allow 36 lines to be multiplexed into one line.

ASM ROM A15U22 controls:

- multiplexers A15U3, A15U6, A15U8, and A15U32, to select individual line qualifiers and:
- multiplexer A15U12, to select one of these multiplexers.

Additionally, A15U12 checks the output of the Auxiliary State Counter, A15U7. This counter is a four-bit binary counter that allows the same sequence of states to be repeated up to 16 times. In the output algorithm, each state represents an output character.

Qualifier flip-flop A15U11A eliminates erroneous results by ensuring that the Counters A15U14 and A15U23 are not clocked when a qualifier is changing states. This would cause a partial preset and partial increment of the State Counters.

8-C-8. ADDRESSING

Address Comparator A15U2 monitors the Data Input/Output (DIO) lines "2" through "5" and the address switch (S1) settings.

When the number on the "DIO" lines is the same as that set into switch S1, A15U2 sends the qualifier "ADDR" to multiplexer A15U8. The address switch's "TALK ALWAYS" section provides a way of setting A15U6 so the interface is always addressed to talk.

8-C-9. DATA OUTPUT

The Data Output circuit outputs characters on the bus data lines.

Storage circuit A15U24 transfers outputs from the ROM to DIO lines "5" through "7".

Integrated Circuit A15U15 selects data from either the ROM or the Model 5328B's data bus and transfers it to DIO1-DIO4. The state of the "not" bit from ROM A15U22(13) through A15U21E determines the selection made by A15U15.

A displayed digit is selected from the Model 5328B, any other character (decimal point, "E", carriage return, exponent, line feed, etc.) is selected from the ROM.

8-C-10. ASM STORAGE

The internal memory for the ASM is in the ASM Storage circuits (A15U19, A15U26, and A15U31B), which can set or clear 17 information bits.

One-shot A15U1, which is part of this section, outputs a 1 ms pulse ("LRST") to ensure reliable operation of the state control circuit A1U4 on the Motherboard.

Diode A15CR3 ensures that "LINH" is "low" to inhibit the counter during the time that "LRST" is "low".

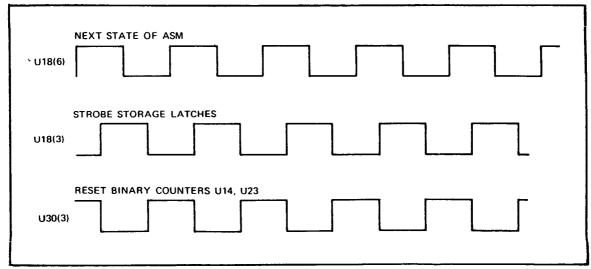


Figure 8-C-1. HP-IB Interface ASM Oscillator Timing Diagram

8-C-11. STROBE ENABLE DECODER

Decoder A15U13 is a 4-to-10-line decoder that strobes the various storage latches.

Pins "1", "14", and "15" are used to select the device to be strobed.

Pin "2" is an "enable" which determines the width of the strobe pulse.

The decoder (A15U13) is disabled when the output of A15U25C goes "high", indicating that the ASM is in its "decision state" mode.

8-C-12. REMOTE PROGRAM STORAGE

Storage circuits A15U28, A15U33, and A15U34 program instrument functions.

Integrated circuit A15U28 stores Time Base codes in three-bit bytes.

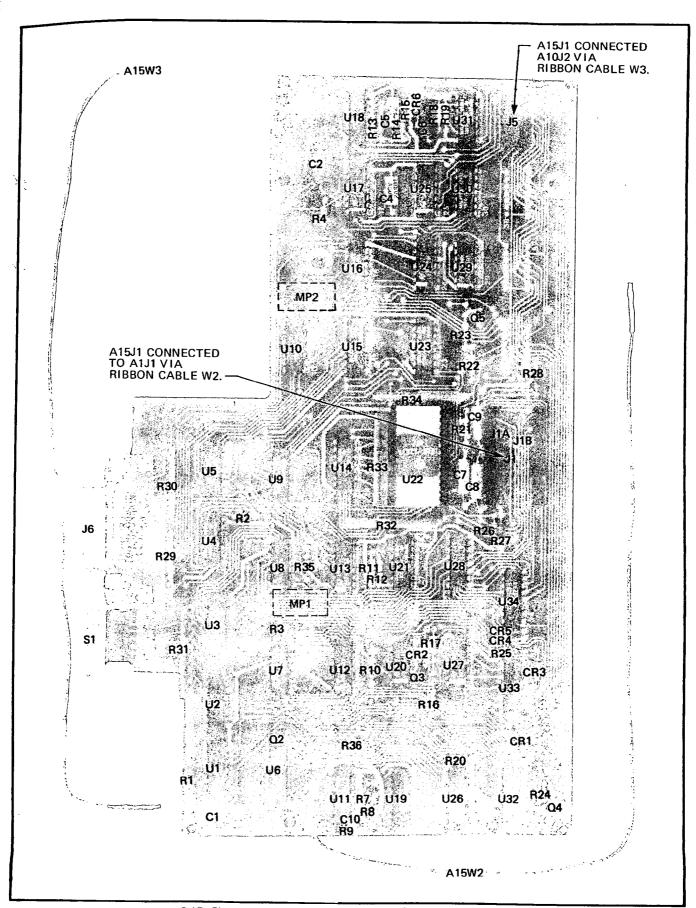
Integrated Circuit A15U34 stores Function codes in four-bit bytes.

Integrated circuit A15U33 stores eight bits of information, one bit at a time. The Sample Rate, Arming, Storage Off, and Decade Reset can be programmed by A15U33. In addition, A15U33(4, 5, and 6) control the manner in which measurements are made and output to the bus. The inputs to the remote program storage circuit are Module Data "A", "B", "C", and "D" lines from the DIO lines —"1", "2", "3", and "4", respectively.

8-C-13. Troubleshooting The HP-IB Board A15

The HP-IB board in the 5328B is a complex board with a large number of ICs and control lines. A troubleshooting kit has been set up for this board that uses the 55005A Logic Troubleshooting System. Signatures of a correctly operating HP-IB board have been characterized and provided along with a program that guides the user, pin by pin through the board. The kit also contains the 2 test cards described earlier in the Main Counter Troubleshooting. When ordering the kit use Part Number 05328-60131.

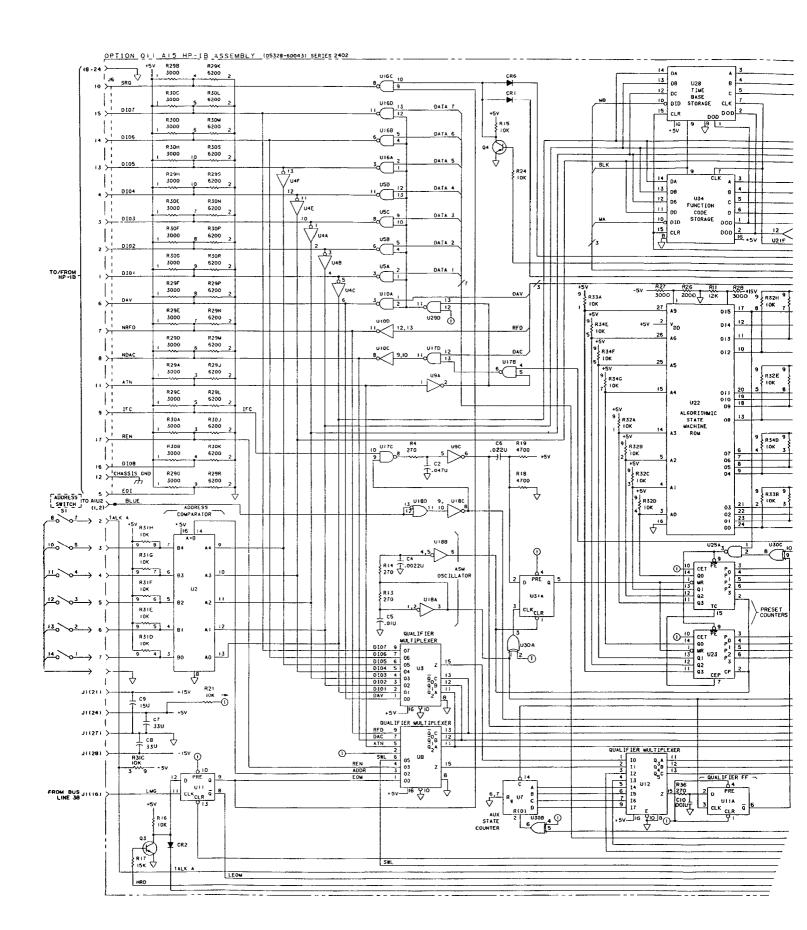
This kit is used in conjunction with a controller such as the HP 85 and the 5005B Signature Multimeter.



P/O Figure 8-C-2. A15 HP-IB Interface Assembly

Signals from A15J1 through Cable to A1J1 (Motherboard)

- 1 LDDIS → BUS (39)
- 2 LDP → BUS (41)
- 3 LOVFL → BUS (28)
- 4 FC \rightarrow BUS (36) \rightarrow XA16(5)
- 5 FD → BUS (36) → XA16(5)
- 6 HRD → BUS (33)
- 7 LRES → BUS (32)
- 8 LTR \rightarrow BUS (31) \rightarrow XA4A(12)
- 9 LINH → BUS (30)
- 10 TBSB → BUS (35) → XA16(24)
- 11 TBSA \rightarrow BUS (35) \rightarrow XA16(23)
- 12 FB \rightarrow BUS (37) \rightarrow XA16(7)
- 13 LARM \rightarrow XA4B(1)
- 14 TBSC \rightarrow BUS (34) \rightarrow XA16(22)
- 15 FA \rightarrow BUS (37) \rightarrow XA16(6)
- 16 LMG \rightarrow XA4(A17) \rightarrow BUS (38)
- 17 LEXT BUS (38)
- 18 LANN → BUS (41)
- 19 SRT \to A1(U4) \to XA16(24)
- 20 HDS → XA4A(4) → BUS (14)
- 21 +15V
- 22 DC → XA4(B15) → BUS (42)
- 23 DD → XA4(B16) → BUS (42)
- 24 +5V
- 25 DA → XA4(B17) → BUS (43)
- 26 DB \rightarrow XA4(B18) \rightarrow BUS (43)
- 27 GND
- 28 -5V



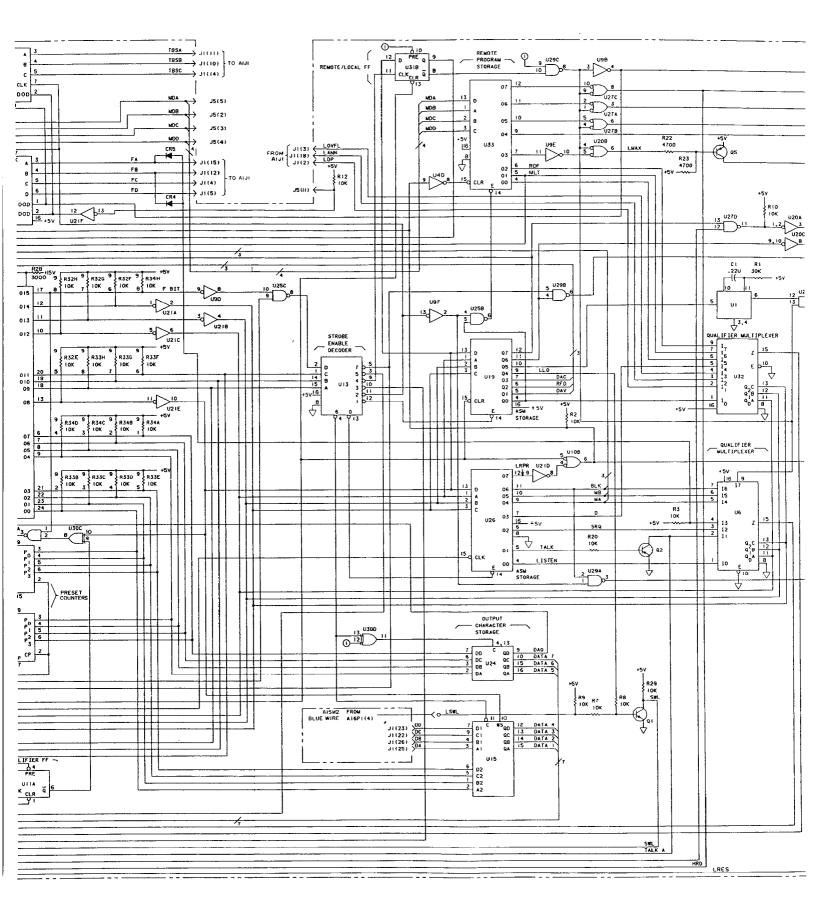


Figure 8-C-2. A15 HP-IB In

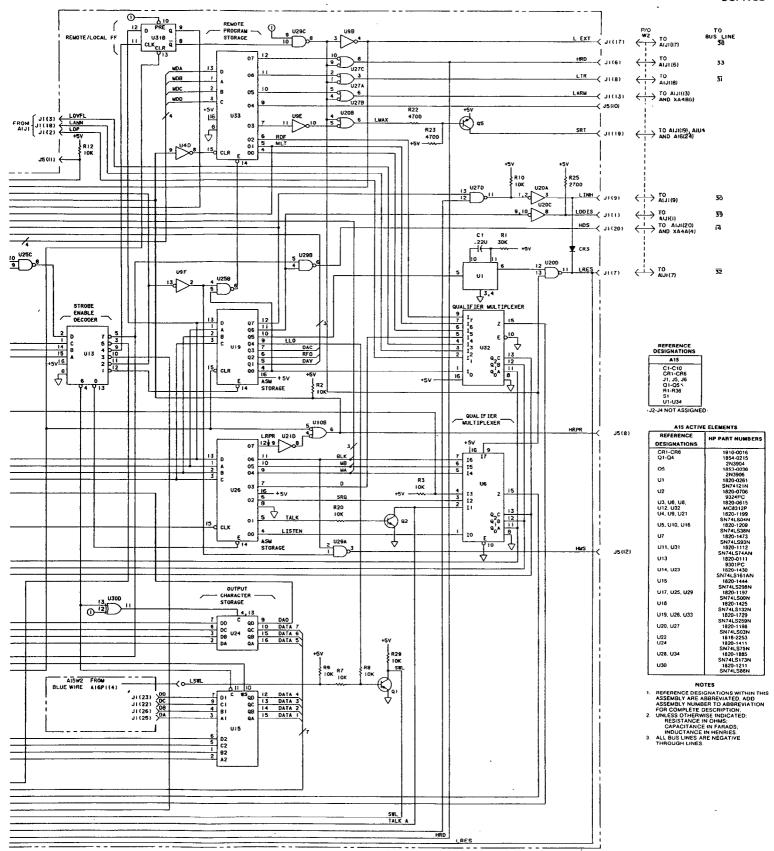


Figure 8-C-2. A15 HP-IB Interface Assembly Schematic Diagram

8-D-1. POWER SUPPLY (A2)

The Model 5328B's Power Supply, shown in Figure 8-D-2, has five output voltages (all are dc): +5V, -5.2V, +15V, -15V, and +3.5V.

The +5V and -5.2V circuits are essentially the same, as are the +15V and -15V sections; therefore, only the supplies for the positive voltages will be described below.

8-D-2. Principles of Operation

8-D-3. GENERAL

The Power Supply provides regulated dc voltages for the Model 5328B's other operating sections. It has enough capacity to operate any combination of the Model 5328B's options.

The main ON/OFF switch operates only the central power supply regulator; the main ac power is not controlled by this switch, and is always "on" as long as the Model 5328B is connected to an operating ac power line. Unregulated dc is constantly fed to the (optional) oven oscillator (if installed), eliminating the need for time base warm-up. The cooling fan gets its power from the ac power line via a triac, which is controlled by an optical isolator when the Model 5328B is turned off.

8-D-4. +5V SUPPLY

The +5V supply is a switching regulator that is more efficient than a linear regulator having the same output.

When the "+5V" output voltage is below its nominal level, comparator U1's "+" input becomes more positive than its "-" input; this makes U1's output go positive, which turns on transistor Q5, which, in turn, turns on Q3 and Q1. The voltage at Q1's collector now goes "high" (more than +17V) and current starts to build up through L1, charging the output capacitor and increasing the output voltage. At the same time, positive feedback is provided via resistor R11 to maintain the condition until the output goes slightly above +5V. When the voltage reaches this point, the comparator's output voltage starts to fall, turning off transistors Q5, Q3, and Q1, causing the voltage at Q1's collector to fall. This provides positive feedback via resistor R11 to reinforce the charge. As a result, transistors Q5, Q3, and Q1 are turned off "hard", and the voltage at the collector of Q1 goes negative, except for diode CR3, which clamps the voltage to ground. During this part of the cycle, current flows through diode CR3 and coil L1, allowing the energy which has been stored in the field of L1 to go into the load. This goes on until the output voltage again goes "low" enough to overcome the offset at the input of Comparator U1 and turn transistor Q1 on again.

8-D-5. +15V SUPPLY

The +15V Supply is a linear regulator, using transistor Q7 as the pass transistor. Transistor Q9 provides level-shifting and current gain, while U3 is used as a Comparator and Gain Block.

8-D-6. +3.5V SUPPLY

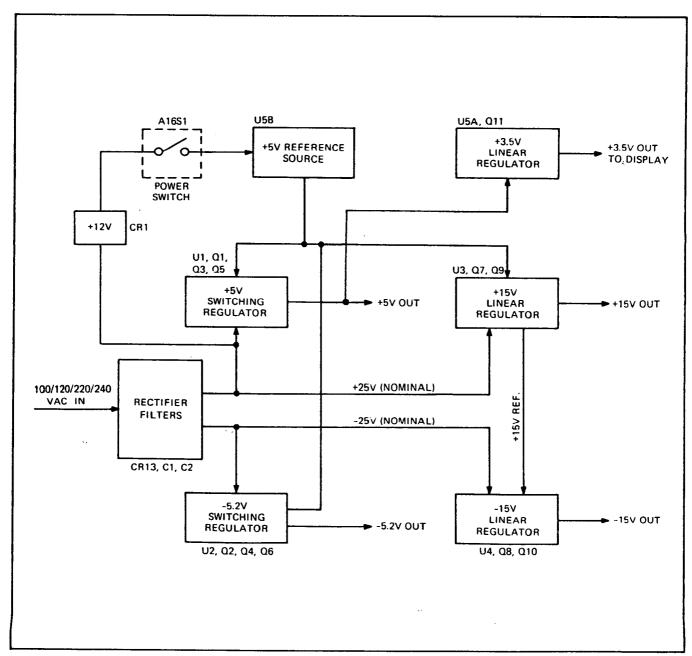
The +3.5V Supply is a linear regulator with the Operational Amplifier section of U5 being used as a Comparator and Gain Block. Resistor R32 provides overcurrent limiting to protect against short circuits.

8-D-7. Troubleshooting

Figures 8-D-1 and 8-D-2 are provided to aid in troubleshooting the A2 Power Supply Board.

WARNING

110V/120V/220V/240V AC SUPPLY WIRES ARE EXPOSED WHEN EITHER THE TOP OR BOTTOM COVER IS REMOVED. USE EXTREME CAUTION DURING TROUBLESHOOTING, ADJUSTMENT, OR REPAIR. TO REDUCE THE CHANCE OF PERSONAL INJURY OR DAMAGE TO EQUIPMENT, DISCONNECT THE MODEL 5328B FROM ITS OPERATING AC POWER LINE BEFORE YOU REMOVE OR REPLACE ITS COVERS OR ANY OF ITS INTERNAL ASSEMBLIES OR COMPONENTS.

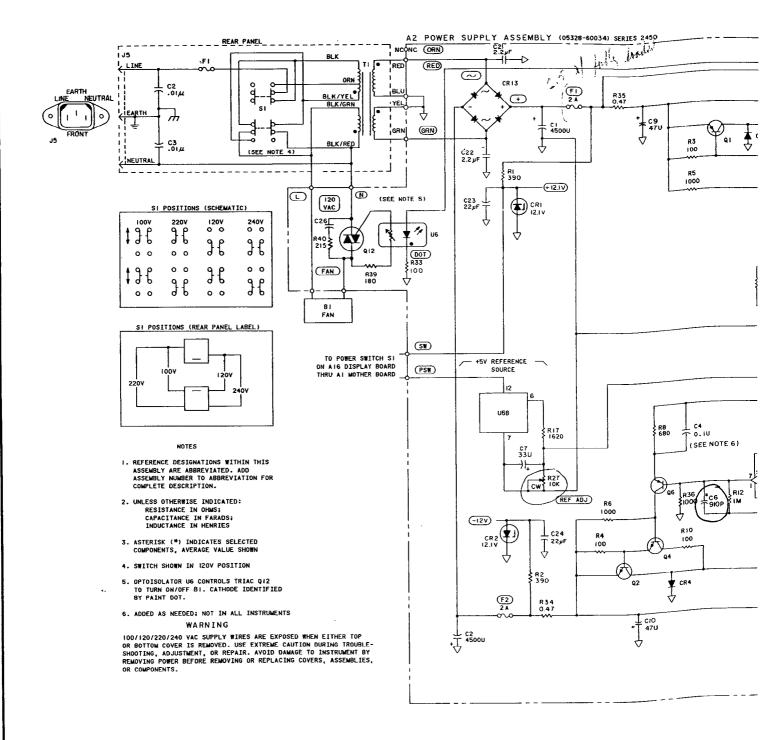


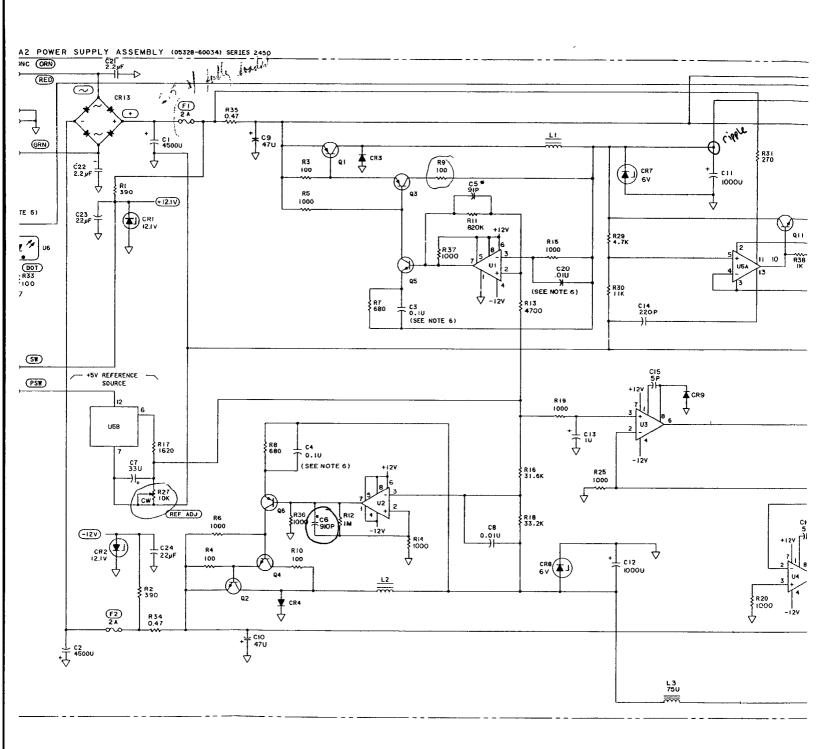
P/O Figure 8-D-2. A2 Power Supply Assembly Troubleshooting











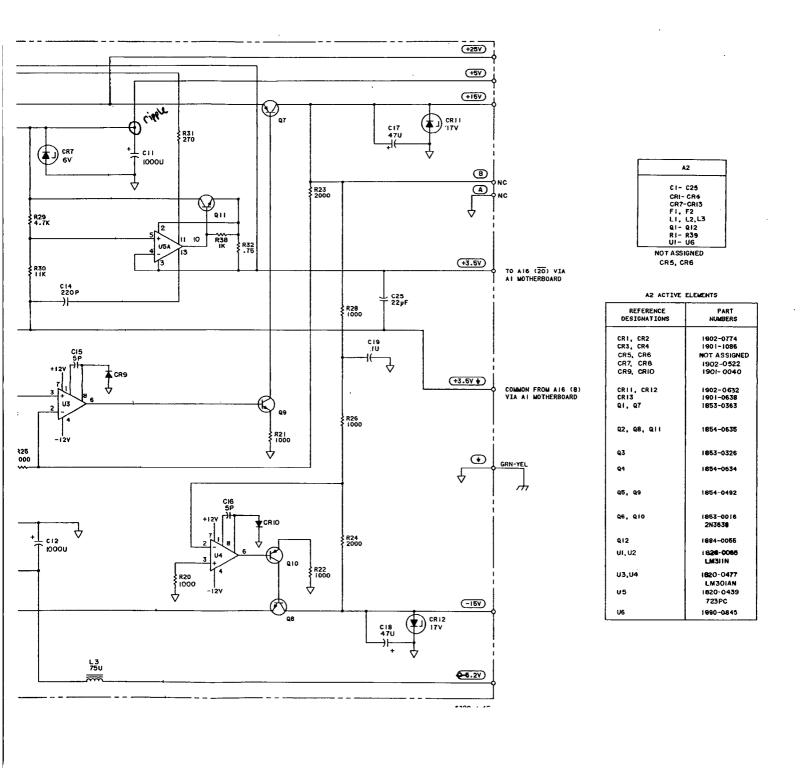


Figure 8-D-2. A2 Power Supply Assembly Schematic