3048 MS

HP 8663A SYNTHESIZED SIGNAL GENERATOR (Including Options 001, 002, & 003)

Service Manual

Volume 4

SERIAL NUMBERS

This manual applies directly to instruments with serial numbers prefixed:

2234A to 2927A and all MAJOR changes that apply to your instrument.

rev.01JUL91

For additional important information about serial numbers, refer to "INSTRUMENTS COVERED BY THIS MANUAL" in Section 1.

Third Edition

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Service Manual (Volume 1, 2, 3, 4) HP Part 08663-90071

Other Documents Available: Operation and Calibration Manual HP Part 08663-90069 Microfiche Service Manual HP Part 08663-90072 Microfiche Operation and Calibration Manual HP Part 08663-90070

Printed in U.S.A. : November 1990





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Model 8663A

SERVICE SHEET 38 A11A4 FM PHASE DETECTOR

REFERENCE BLOCK DIAGRAM 7 Table 4-1. Recommended Performance Tests After Adjustments or Repairs Table 5-2. Post-Repair Adjustment Procedures

PRINCIPLES OF OPERATION

General

The purpose of the FM Phase Detector (A11A4) is to develop the FM Loop Error Voltage. This is accomplished by comparing the VCO output with a 10 MHz reference signal.

Phase Detector Circuitry

The Phase Detector Circuitry compares the phase difference between the reference signal (FM 10 MHz Reference Signal) and the output of the VCO (FM Loop N-Divider Drive), then generates a voltage proportional to the phase error. Both input signals are first divided down to 100 kHz before they are compared by the FM Loop Phase Detector, U4. The 10 MHz signal from the Reference Section is divided by a total of 100. This signal becomes the reference input to the phase detector. The 140 MHz VCO output is divided by a total of 1400. This signal becomes the variable input to the phase detector.

The Phase Detector, U4, is a digital phase detector that generates output pulse widths proportional to the phase difference between the two input signals. If the variable 100 kHz signal is lower in frequency or lags the reference input in phase, the output on pin 13 generates a pulse (See Figure 8-601). When the variable input is higher in frequency or leads the input in phase the output on pin 2 generates a pulse (see Figure 8-602).



Figure 8-601. Phase Detector Operation (Variable Input is Lower in Frequency than Reference Signal)



Figure 8-602. Phase Detector Operation (Variable Input is Higher in Frequency than Reference Signal)

Following the FM Loop Phase Detector are the Switch, Low-Pass Filter, and Integrator circuits. In a phase-locked condition the switch circuit (gates U3A and U3D) is enabled, allowing the output pulses from the phase detector to pass through. The Low-Pass Filter and the Integrator convert the pulses into a voltage. This becomes the FM Loop Error Voltage. Each pulse causes a given amount of charge to be stored in the integrating capacitors, C11 and C12.

When the FM Loop is disabled the switch circuit prevents the phase detector output pulses from reaching the Integrator. To prevent the Integrator's output from slewing against the power supply rails when the FM Loop is disabled, R20 is shunted across the integrating capacitors.

When no modulation signal is applied, the error voltage developed by the Phase Detector Circuit is dependent on the frequency and phase of the 140 MHz VCO. In the phase-locked FM mode the error voltage is also dependent on the instantaneous frequency deviation. The FM Loop's bandwidth is about 6 Hz, however, phase-locked FM is only possible down to modulation rates of 20 Hz. Modulation rates below 20 Hz result in the gradual cancellation of the modulating signal because the phase-locked loop can now start responding to the modulation signal.

An overmodulation condition exists when the peak deviation of the 140 MHz VCO exceeds the limit or range of the phase detector, U4. This overmodulation condition is detected by monitoring the output of the phase detector. The output is first filtered by a Low Pass Filter consisting of R11 and C7. The output waveform from the low pass filter is similar to a half wave rectified sine wave. This signal is monitored by the Overmodulation Detector (located on the Modulation Drive) which is a peak detector circuit. If the output waveform from the low pass filter exceeds 1 volt peak, the Overmodulation Detector is triggered.





Figure 8-603. A11A4 FM Phase Detector Block Diagrams



Figure 8-604. A11A4 FM Phase Detector Component Locator

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CHANGES

On the schematic:

Bullet "I" FM LOOP ENABLE (FM-AC) FROM MODULAð TION DRIVE - In the lower left portion of the schematic, change the line label of bullet "I" from "TTL HIGH=ENABLE" to "Special Levels, +9V=Enable, -19V=Disable".









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NOTES

1. REFER TO TABLE 8-102 FOR SCHEMATIC DIAGRAM NOTES.

- 2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR WEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.
- * BACKDATING INFORMATION IN SECTION VII.



LOGIC	LEVELS
	TTL
HIGH	>+2V
LOW	<+0.8V
< IS MORE > IS MORE	NEG.THAN POS.THAN
OPEN	HIGH
GROUND	LOW

INTEGRATED CIRCUIT VOLTAGE AND GROUND CONNECTIONS

PIN NUMBERS

+5V(F3)- 4,5

+5V(F1)- 5 ☆ - 10

+5V(FI)- 14 +5V(F2)- 14

7

16

8

Ý -

+5Y(FI)-

 \Diamond

+5V(F2)- 5

´-

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U2

U3

14

105

U6

REFERENCE DESIGNATIONS

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TRANSISTOR AND INTEGRATED CIRCUIT PART NUMBERS				
	REFERENCE DESIGNATIONS	PART NUMBERS		
	Q1.3 Q2	1854-0404 1854-0019		
	U1 U2	1820-1780 1820-1478		
	U3 U4 U5	1820-1201 1820-0630 1820-1490		
	na ne	1820-1463 1826-0013		
8		CIRCUIT BOARD TOP VIEW IC 016 KEY-O 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
4		0 C 40 05		



SERVICE SHEET 38

Figure 8-603. A11A4 FM Phase Detector Schematic 8-605/606



Figure 8-606. P/O A11A1 Variable Modulation Oscillator Block Diagrams

Service

Model 8663A



Figure 8-607. P/O A11A1 Variable Modulation Oscillator Component Locator

All serial prefixes

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On the schematic:

- A11A1U28 In the Table of Transistor and Integrated Circuit ٠ Part Numbers, change the part number of U28 to 1826-0785.
- A11A1Q1 Add Q1, 1854-0477 to the Table of Transistor ٠ and Integrated Circuit Part Numbers.

On the A11A1 schematic:

A11A1U26 - Move the low-level-active polarity indicator ٠ (triangle) from U26 pin 8 to U26 pin 6.

On the Component Locator:

A11A1C13 - On the component locator, change R13 to C13, ۰ between R15 and R10

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Fig 8-608 SH 20\$5



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Jig 8-608 Sht 40/5





8-609/610



Figure 8-609. P/O A11A1 Variable Modulation Oscillator Block Diagrams

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Model 8663A







SERIAL PREFIX: 2234A

_ATION OSCILLATOR (08663-60321) sht 2085









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Model 8663A





All serial prefixes 👘

2333A and Above

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On the A11A5 schematic:

- AllAIR1 - R1 is incorrectly shown connected between output pin 6 of Ul and ground. R1 should be connected between inverting input, U1 pin 2, and ground.
- AllAlR2 R2 is incorrectly shown connected between output ٠. pin 6 of U2 and ground. R2 should be connected between inverting input, U2 pin 2, and ground.
- AllAlR29, R30 Change the value of R29 to 90.9 ohms. Change the value of R30 to 10 ohms.

On the A11A5 component locator:

٠ AllASCR8 - Delete CR8.

On the AllA5 schematic:

A11A5CR8 - Delete CR8.

On the A2A2 component locator:

A2A2CR5 - Delete CR5.

On the A2A2 schematic:

A2A2CR5 - Delete CR5. •









LOGIC	LEVELS
	TTL
HIGH	>+2.0V
LOW	<+0.8V
< IS MORE > IS MORE	NEG.THAN POS.THAN
OPEN	HIGH
GROUND	LOW

TRANSISTOR AND INTEGRATED CIRCUIT PART NUMBERS			
REFERENCE	PART		

DESIGNATIONS	NUMBERS
Q2	1885-0020
UI,2 U3,7 U4,6 U8 U9 U10,12 U13	826-0783 826-0358 826-0950 820-1 99 826-0 6 826-0264 826-095

INTEGRATED CIRCUIT VOLTAGE AND

GROUND CONNECTIONS				
REFERENCE DESIGNATIONS	PIN NUMBERS			
U4 ,6	+10V - 11 +5V - 12 -20V - 14 -13			
US	+5V = 14 ↓ = 17			
UI3	+10V - 6 +5V - 7 -20V - 9 -20 - 8			

ALIASUL,2 TOP VIEW	C1R 80 TOP	CUIT ARD VIEW
	KEY-O	08 0 0

AIIA U8,9, T <u>OP V</u> 1	5 3 LEW	CIRCUIT BOARD TOP VIEW	
니티아	₽ I4		014
9	PK	EY-70	0
i q	Þ	0	0
Ľ	þ	0	0
d	Þ	0	0
đ	Þ	0	0
7 0	þ 8	70	08

SERVICE SHEET **41**

Figure 8-614. P/O A11A5 Modulation Drive Schematic 8-617/618

- I. REFER TO TABLE 8-102 FOR SCHEMATIC DIAGRAM NOTES.
- 2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.










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< $= \sum_{i=1}^{n} \sum_{j=1}^{n} \sum_$

On the A11A5 schematic:

٠ The output line U9B pin 7 is incorrectly labeled FM LOOP ENABLE. Change the label to AUXILIARY FM SWITCH DRIVE.

On the A11A5 schematic:

- A11A5R36 Change the value of R36 to 1.33k. ٠
- é, A11A5U9 - In the Table of Transistor and Integrated Circuit Part Numbers, change the part number of U9 to 1826-0753.





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3663-60323)



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I. REFER TO TABLE 8-102 FOR SCHEMATIC DIAGRAM NOTES.

2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.

REFERENCE	ESIGNATIONS
NO PREFIX	ALIA6
₩84,85	J8,9 XAIIA5
A I 1 A 5	
C18-27 CR6-8 L:-3 Q: R36-60 TP5 U5,6.9,11 VR:-3	

)		
5		
LOGIC LEVELS		

/
AN AN

INTEGRATED CIRCUIT VOLTAGE AND GROUND CONNECTIONS		
REFERENCE DESIGNATIONS	PIN NUMBERS	
U68	+10V - 11 +5V - 12 -20V - 14 $\sqrt{-13}$	
UIF	+5V - 16 ↓ - 8	



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SERVICE SHEET 43 A11A2 FM VCO

REFERENCE BLOCK DIAGRAM 7 Table 4-1. Recommended Performance Tests After Adjustments or Repairs Table 5-2. Post-Repair Adjustment Procedures

PRINCIPLES OF OPERATION

General

The FM VCO (A11A2) generates the frequency modulated 20 MHz signal. The FM signal is generated by applying the modulation signal to the varactor diode that forms part of the oscillator's tank circuit. The FM VCO has two primary outputs. One is the 140 MHz VCO output which is used to lock the loop and the other output is the frequency modulated 20 MHz signal. The 20 MHz FM signal is developed by heterodyning the 140 MHz VCO output with the 120 MHz signal from the Reference Section. The primary inputs are the modulation signals and the FM Loop Error Voltage which is used to lock the loop.

140 MHz Voltage Controlled Butler Oscillator (VCO)

Q1, Q2 and associated components form the 140 MHz Voltage Controlled Oscillator. Q1 is a common-gate FET amplifier with the tank circuit located in its drain circuit. The signal developed in the tank circuit is coupled to the gate of Q2 by C13. Q2 acts as a source follower in the feedback circuit, providing a high impedance at its gate and a low output impedance at its drain. Q2 amplifies the feedback signal and feeds the signal back to Q1 to sustain oscillation.

The 140 MHz VCO is both frequency modulated and phase locked by varying the reverse-bias voltage across the varactor diode. An increase in the reverse-bias voltage reduces the junction capacitance, which increases the resonant frequency of the tank circuit. This causes the VCO to oscillate at a higher frequency. When the FM Loop is phase locked the center frequency is always 140 MHz.

Gain Limiting and Mixer Circuit

The 140 MHz signal developed across the tank is coupled to Limiter, U1. The Limiter acts as a buffer amplifier for the VCO and provides a constant level output. One output from the Limiter is used to phase lock the loop. The other output is applied to the RF port of the double balanced mixer, U2. The Mixer heterodynes the 140 MHz VCO output with the 120 MHz signal to produce the 20 MHz FM signal.

Shaping Network and Shaping Network Bias

Due to the nonlinear tuning characteristic of the varactor diode and the large amount of shunt capacity used in the resonant circuit, a Shaping Network is required. The Shaping Network conditions the modulation signal applied to the varactor to ensure that the frequency change is linear with the applied voltage.

The Shaping Network consist of a ladder of diodes that are reverse biased, in sequence, at a voltage higher than the reverse bias on the diode previous to it. As the input signal level increases (negative direction) the diodes successively become forward biased, in sequence, and present a lower impedance to the input signal.

The Shaping Network Bias circuit acts as a voltage supply for the resistor-diode network and for Q3. The bias voltage is about +14.1 Vdc.

Voltage to Current Converter

The various input signals (the FM Loop Error Voltage, DC Offset and the modulating signals) are summed at the emitter of Q5. Q5 operates in a common-base mode with the inputs applied at its emitter. The base is biased one diode drop above ground, therefore the voltage at the emitter is approximately zero volts.

Loop and Aux FM Switch

Q6 and Q7 are JFETs used as analog switches. The FM Loop Enable and the Aux FM Switch Drive lines are the control lines that drive the JFET switches either ON or OFF. To maintain the JFET switches in the ON state, the control lines are biased to approximately +9 Vdc. This reverse biases both diodes, CR1 and CR2, and the gates are essentially left floating. To turn the JFET switches OFF, the control lines are biased to approximately -19 Vdc. This voltage forward biases both diodes, and clamps the gates at a negative voltage. This negative voltage at the gates effectively pinches off the JFETs (OFF state).

Loop switch, Q6, is used to disable the FM Loop Error Voltage. The Aux FM switch, Q7, isolates the modulation signal from the Aux FM Input.

Phase Lock Detector

The Phase Lock Detector consists of two comparators that form a window comparator circuit. This circuit determines if the FM Loop Error Voltage lies between two preset voltage limits (the window). R41, R42, and R43 form a voltage divider that establishes the upper and lower voltage limits. The upper and lower voltage limits, +1.39 Vdc and -1.34 Vdc respectively, are applied to the inverting input of comparator U3A and U3B. When the error voltage is within the voltage window, indicating the FM Loop is phase locked, the output from the 8-624

comparators will remain high. If the error voltage lies outside the voltage window, one of the comparators will go low, turning the Out of Lock Indicator on.

TROUBLESHOOTING

If any components in the 140 MHz Voltage Controlled Butler Oscillators fail, the A11A2 assembly may have to be replaced. Resistors R21-R24 are selected to match the characteristics of the oscillator. Changing a component in the oscillator could change the characteristics enough to require new selected resistors. The selection process requires a special test fixture and is very time consuming; therefore, it cannot be done in the field.

To determine whether or not a new assembly is needed, replace the defective component and measure FM distortion with 100 kHz deviation. If FM distortion is within the specifications listed in Table 1-1, the repair is successful. However, if FM is not within the specifications, the assembly will have to be replaced. The varactor diode, CR9, is the most critical component.





Service





CHANGES

All Serial Prefixes	On the A11A2 schematic:
	• <u>A11A2R4*, R7*</u> - Change the value of R4* to 5.62k. Change the value of R7* to 5.11k.
All Serial Prefixes	On the A11A2 Schematic:
	• <u>R7*</u> - Change the value of R7* to 4.22k.
•	

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Fig 8-620 Sht 294 60343) PHASE LOCK DETECTO +20V - SHAPING NETWORK BIAS -¢ VRI 5.IV +20V +207 R39 R | 9 2370 10 SHAPING AMPLIFIER 04 R41 21.5k ÅR16 §5620 R40 51.1k≸ U3A 3 2.7 +20V ₹ 3160 77 R35 R43 10⊮ 9 -20V U3B SHAPING NETWORK 8 P Q3 Ï CR4 5 -i'ov R17 16.2k 📥 CR5 🛣 CR6 🛣 CR7 G +20V R23* 38.3k R24* 56.2k R2|* 23.7k R22* 26.1k ş CR3 R30 46.4 R31 R33 681 31.6 उ R25 46.4 R28 46.4 R27 46.4 14 0 MHz VOLTAGE CONTROLLED BUTLER DSCILLATOR VOL TAGE-TO-CURRENT CONVERTER R38 10K ΤΡΙ R36 1470 4 R37 100 C13 10p R9 1000 TANK CIRCUIT --6.1 Vdc (DEPENDS ON RI8 SETTING) C15 470p DC DFFSET ⋧ Ą Q2 C18 R26 сіі 1000р —) |---TP2 R15 1330 QI R18 500 -141 ₹ R20 \$ 1000 7 + ↓ R32 R29 2160 ⊥ ci4 ↓ ↓ -101 G Ð ₹ CI2 6.8/4 -25V DUPLING -R]] |4.7k - - 4V (NOMINAL) R12 13.3k -25V(NOMINAL) R I 3 464 ?∔4 464





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-15dBm 0.1Vpp)

NOTES

1. REFER TO TABLE 8-102 FOR SCHEMATIC DIAGRAM NOTES.

- 2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.
- 3. ASTERISK (*) INDICATES THAT THESE PARTS ARE SELECTED IN TEST. THE VALUES SHOWN ARE TYPICAL ONLY. SEE SECTION X FOR PROCEDURE.

LEVELS
TTL
>+2V
<+0.8V
NEG.THAN POS.THAN
HIGH
LOW

TRANSISTOR AND INTEGRATED CIRCUIT PART NUMBERS

DESIGNATIONS	NUMBERS	
Q1.2 Q3-5 Q6,7	1855-0235 1854-0404 1855-0020	
U1 U3	1826-0372 1826-0191	

REFERENCE D	ESIGNATIONS
NO PREFIX	A I 1A2
₩ ₩41J:	CI-27 CRI-10 DSI JI-6 QI-7 RI-33,35-55 TPI_2 UI-3 VRI-3
	ATIA6
	J4-6,8 XA11A2 XA11A4



SERVICE SHEET 43

Figure 8-620. A11A2 FM Loop Voltage Controlled Oscillator Schematic

8-629/630







Figure 8-622. A11A3 Phase Modulation Input Component Locator

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CHANGES



On the A11A3 schematic:

4 A11A3U3 - On the U3 symbol, reverse the "+" and "-" symbols at the inputs. Pin 3 should be inverting (-) and pin 2 should be non-inverting (+).

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PHASE MODULATION SIGNAL TO PHASE MODULATOR

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NOTES

- I. REFER TO TABLE 8-102 FOR SCHEMATIC DIAGRAM NOTES.
- 2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.

LOGIC	LEVELS
	TTL
HIGH	>+2¥
LOW	<+0.8V
<pre>< IS MORE > IS MORE</pre>	NEG.THAN POS.THAN
OPEN	HIGH
GROUND	LOW













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ATTASK8-16 BOTTOM VIEW

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Figure 8-623. A11A3 Phase Modulation Input Schematic

8-633/634



Figure 8-624. A4A4 Phase Modulator Block Diagrams

Service



Ing 8-625 Sht 2 43



A4A4A5

J-ig 8-625 Stt 3 \$3



Fig 8-625a SLt 18/5 Service . •. din tabéné 0 C4]% **R1** []сз R3 **Q**2 0⁰−R7 −0 0-R6-0 Q1 **R**9 0—R4 ©Ei₀-R10-0 Q3 ∽ rr1 -0 ы В <u>0-R5</u> -R11 -0 A4A4A4 8-638



Figure 8-625. A4A4 Phase Modulator Component Locator (2

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A4A4A1

ator (2 of 2)

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Model 8663A

CHANGES

	On the enhanced in
All serial prefixes	On the schematic:
1	
	• W1 (W19) In the upper left portion of the schematic, change
	the w1 to w19.
	W2 - Label the -10V line connecting A4A4A3 and P/O
	A4A4A2 WZ.
	W_3 - Label the +20V line connecting A4A4A3 and P/O
	A4A4A2 W3.
	W4 – Label the $-10V$ line connecting P/O A4A4A2 and
	A4A4A4 W4 .
	W5 - Label the $+20V$ line connecting P/O A4A4A2 and
	A4A4A4 WD.
	W_6 - Label the coaxial cable connecting A4A4A3 and
	A4A4A1 (associated with $E1-4$) W6.
	W7 - Label the INPUT BYPASS SEL line connecting A4A4A3
	and A4A4A1 W7.
	W8 - Label the +20V line connecting A4A4A3 and A4A4A1
	W 8.
	W9 – Label the $+5V$ line connecting A4A4A3 and P/O
	A4A4A2 WY.
	W10 - Label the -10V line connecting A4A4A3 and A4A4A1
	W 10.
	W12 - Label the OUTPUT BYPASS SEL 1 line connecting
1 7 1 5 f s 6 6 s 6 S 7 S 7 S 7 S 7 S 7 S 8 S 8 S 8 S 8 S 8	A4A4A3 ang A4A4A0 - W 12-
Constant and a state of the second state of th	
1 / / / / / / / / / / / / / / / / / / /	
	$\underline{W13}$ - Label the OUTPUT BYPASS SEL 2 line connecting
eneral for production and it is a statement in the	AAAAA S and AAAAAA W12
The second second second states and second	АТАТАЈ ANU АТАТАŬ 1149.
T TO A THE THE REAL AND	
	W14 - Label +20V line connecting A4A4A3 and A4A4A6
	W1A
n a star a st	TT 14.
n - Carlon Carlon Carlon Carlon Carlon - Carlon Carlon Carlon Carlon Carlon Carlon Carlon Carlon Carlon Carlon La carlon Carl	TTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTT
	W_{15} - Label the coaxial cable connecting A4A4A5 and
	454546 (associated with $E5-8$) W15
	AJAJAO (associated with EJ=0) WIJ.
n na sana na sana na ang kana na sana n	
n an	w_{10} - Label the coasial cable connecting A4A4FL/ and
	$\overline{\mathbf{A4A}}$
a second a second s	ATATAY ILLY
	\mathbf{W}
් මොහා මොහා ඒ ඒ සිටි අන්තර අන්තර අන්තර සිටින් දී කිරීමට සිට කරන්නේ මොහා කරන්නේ මොහා කරන්නේ කරන්නේ මොහා කරන්නේ ක මොහො මොහො කරන්නේ කරන්නේ කරන්නේ කරන්නේ කරන්නේ සිටින්නේ සිටින්නේ සිටින්නේ සිටින්නේ සිටින්නේ සිටින්නේ සිටින්නේ සිට	$w_{1/}$ - Label the line connecting A4A4A2 and A4A4PL/
	W17
· · · · · · · · · · · · · · · · · · ·	
	W(2) = 1 and the line connecting $A / A / A / (2)$ to $A / A / A / (2)$
The second se	$\frac{W20}{W20}$ - Easer the fine connecting A#A#A1Z2 to A#A#A221
	W20.

Service

2323A and Above

• <u>A4A4A4</u> - Use the new component locator "P/O Figure 8-625. A4A4 Phase Modulator Component Locator (2 of 2) (2323A)" on page 8-638.3.

On the A4A4A4 schematic:

A4A4A4 component locator:

• <u>A4A4A4R10</u>, <u>R11</u> - Add an asterisk (*) to R10 and R11 to indicate a factory selected component.

A4A4A2, A4A4A3, and A4A4A5 component locators:

• <u>A4A4A2</u>, <u>A4A4A3</u>, <u>A4A4A5</u> – Use the component locators "A4A4A2, A4A4A3, and A4A4A5 Component Location Diagrams (2535A)" on page 8-638.4.

A4A4A2 schematic:

 <u>A4A4A2</u> - Use the A4A4A2 schematic "P/O Figure 8-626. A4A4A2 Schematic (2535A)" on page 8-638.5.

A4A4A3 schematic:

 <u>A4A4A3</u> - Use the A4A4A3 schematic "P/O Figure 8-626. A4A4A3 Schematic (2535A)" on page 8-638.5.

A4A4A5 schematic:

 <u>A4A4A5</u> - Use the A4A4A5 schematic "P/O Figure 8-626. A4A4A5 Schematic (2535A)" on page 8-638.6.

On the A4A4A6 component locator:

• <u>A4A4A6C7 (R5)</u> - Change R5 to C7.

On the A4A4A6 schematic:

- <u>A4A4A6C6</u> Change the value of C6 to 4.7p.
- A4A4C7 (R5) Replace R5 with a capacitor, C7 (4.7p)

SS45 8-638.2

2535A and Above 2545A and Above ******

A4A4A4

P/O Figure 8-625. A4A4 Phase Modulator Component Locator (2 of 2) (2323A)

60366

60367

60368

A4A4A2, A4A4A3, and A4A4A5 Component Location Diagrams (2535A)

4






P/O Figure 8-626. A4A4A3 Schematic (2535A)







Fig 8-626 Sht 20/5

A4A4 PHASE MODULATOR (08663-60011) (OPTION 002 ONLY)



.. PREFIX: 2234A



7 ig 8-626 Sht 4 85





Figure 8-626. A4A4 Phase Modulator Schematic 8-639/640



Figure 8-701. P/O A2A7 Microprocessor Block Diagrams

Service

Model 8663A



Figure 8-702. P/O A2A7 Microprocessor Component Locator

All serial prefixes •

2346A and Above

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SOUCH SIM ANON



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100 A

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CHANGES

On the A2A7 schematic:

- A2A7C9 Change the value of C9 to 2.2u. ٠
- A2A7R15 Change the value of R15 to 121k. ۰

On the A2A7 component locator:

•

A2A7C12 - Add chip capacitor C12. C12 is added to the ٠ circuit side of the board, soldered in parallel with pins 4 and 5 of U4. (Use a dotted outline to indicate that this component is on the circuit side of the board.)

On the A2A7 schematic:

• A2A7C12 - Add capacitor C12 (22p) in parallel with Y1 and pins 4 and 5 of U4.





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+5V C

GND Z

65



SERIAL PREFIX: 2234A













Figure 8-704. P/O A2A7 Microprocessor Block Diagrams

Service

Model 8663A





Zig 8-706 Sht 1094 AZAT MICROPROCESSOR (08663-60332)













Figure 8-706. P/O A2A7 Microprocessor Schematic 8-707/708







Figure 8-708. A2A8 RAM/ROM Component Locator

2537A and Above



A2A8 component locator:

• <u>A2A8</u> - Use the component locator "Figure 8-708. A2A8 RAM/ROM Component Locator (2537A)" on page 8-710.3.

A2A8 schematic:

• <u>A2A8</u> - Use the schematic "Figure 8-709. A2A8 RAM/ROM Schematic (2537A)" on page 8-710.5









Figure 8-708. A2A8 RAM/ROM Component Locator (2537A)





J-y8-709 Skt 345

i



- 1 - 1 -









J-ig 8-709a Stt 2075

J-g 8-709a Stt 30/5





Jig 8-709a Skt 5 g 5

INTEGRATED CIRCUIT

REFERENCE	PART NUMBERS		
U1 U2 U3 U5 U6 U7 U8 U9,16 U10~13 U14 U15	NGMSERS 08663-80005 08663-80006 08663-80009 08663-80009 08663-80009 08663-80010 1820-1197 1820-1216 1820-1201 1818-0443 1820-1201		
017 018,19 020	1820-1917 1820-2024 1820-2075		

NOTES

- 1. REFER TO TABLE 8-102 FOR SCHEMATIC DIAGRAM NOTES.
- 2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.
- ADDRESS OATA IS TRANFERRED POSITIVE TRUE FROM THE MICROPROCESSOR ON THE ADDRESS BUS (AØ-A15).
- 4. DATA IS TRANSFERRED POSITIVE TRUE FROM ROM OR RAM MEMORY TO THE MICROPROCESSOR ON THE DATA BUS (DØ-07).

INTEGRATED CIRCUIT Voltage and Ground connections					
REFERENCE DESIGNATIONS	PIN NUMBERS				
UI~6	+5V - 21,24				
U8,15	+5V - 16 +5V - 8				
U7,9,14, 16	+5V - 14 ☆ - 7				
UI0~13	+5V - 18				
U17-20	+5V - 20 ☆ - i0				

LOGIC		ECL .	LEUL	CMOS
HIGH(1)	≥27	≥+4.0V	≥+4.9V	⇒ VDD
LOW(O)	≤0.8V	≤+3.5V	≤+4,4V	≤0.IV
SEE EQUAL TO OR MORE NEGATIVE THAN				
≥= EQUAL TO OR MORE POSITIVE THAN				
INPUT	TTL	ECL	EECL	CMOS
GROUND	LOW(0)	HIGH())	HIGH()	LOW(0)
OPEN	HIGH()	LOW(O)	LOW(0)	X
GROUND - OV: X= UNDEFINED				

CIRCUIT BOARD TOP VIEW

013

	REFERENCE DESIGNATIONS				
Γ	A2A8	A2A11			
	CI-15 RI UI-20	XA2AB			

2A8U7, CIRCUIT ,14,16 BDARD P VIEW TOP VIEW	CIRCUIT A2A8U8-15 BOARD T <u>OP_VIE</u> W TOP_VIEW	A2A8U1-6 T <u>OP_VIE</u> W	CIRCUIT BOARD Top view
0 14 1 0 014			1 □ 024 (EY ≠0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
CIRCUIT BOARD PVIEW TOPVIEW OUD 18 10 018 D KEYTO 0	CIRCUIT BOARD TOP VIEW I OV 120 I D 020 KEY-YO 0		0 0 0 0 120 0 13
		А2 Тор 12345 Соммон АВСО	ABRI VIEW 678910 EFGHJ



8-711/712














Jig 8-712 Sht 18/5



SERIAL PREFIX: 2234A

7-12 8-712 Sht 2055



Jig 8-712 Stt 3 g5 ROM 13 ROM 15 - ROM 17 -(NOTE 3) 11 11 (D000-D7FF) (E000-E7FF) (F000-F7FF) X-AØ X-----AØ X 8 AÊ 8 7 AL 7 2 2 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 6 2 3 4 5 6 7 8 8 6 2 6 4 5 8 4 A3 A4 5 4 8 5 4 3 2 -23 22 -8 43 8 16 AØ-2047 16 4 AØ-2047 A5 A6 A7 4 3 2 64 1 23 256 22 512 19 1024 16 AØ-2047 32 23 264 23 22 256 22 512 19 102 32 64 128 256 AB AIØ 512 1024 20 A 1024 20 ∧ 18 ∧ √ 20 ⊾ ______18 ⊾ \$13 515 8 8 +F1 E I \$17 8 U4 Rom UB Rom DATA ENABLE UI Rom DØ DI 9 DØ 9 DØ 10 11 13 14 15 16 A. 11 9 ОØ D1 D2 D3 D4 D5 D6 07 A. 1 D1 D2 D3 10 A,1 10 A,1 11 D2 D3 D4 D5 <u>Å. [[]</u> A₁I A, 1 13 A, 1 14 A, 1 15 A, 1 15 A, 1 16 A, 1 17 A, 1 17 13 A, 1 DØ 04 05 14 A, 1 15 A, 1 16 A, 1 17 D6 D7 **D**1 D6 07 A.T <u>D2</u> A. J D3 5 3 D4 6 8 8 D5 Π 11 11 <u>D6</u> 11 D7 10 11 ROM 12 -ROM 14 - ROM 16 -- ROM 18 -1 (C800-CFFF) (DBOO-DFFF) (F800-EFFF) X->Y (FBOD-FFFF) X-+Y X-+ AØ 8 AC 8 8 7 2 5 4 5 8 X---Y AØ ε A A A 7 6 AR 7 AI A1 Ā A2 6 4 A2 5 4 A2 A3 A4 A5 A6 A7 A8 A9 A1B 6 5 5 A3 A3 B 8 A A A A A A 4 3 2 1 23 22 19 4 58 4164 332 64 128 23256 2256 2256 191024 A4 A5 A6 A7 A8 16 AØ-2047 16 AØ-2047 A4 A5 A6 A7 A8 A9 A1Ø 4 16 AØ-2047 3 2 1 23 22 19 32 AØ-2047 32 64 128 2 32 64 128 64 128 256 256 AU 22 22 256 512 AID 512 512 1024 1024 20 N 1024 512 20 ⊾ 18 ⊾ \$14 _____<u>18</u>\ ______ 20 1 S16 a + FI 518 a ∔FI 8 +F1 U3 Rom UIO ROM U2 ROM U7 Rom 9 DØ A DØ Α,Ι 9 DØ <u>A | 10</u> A, 1 15 A, 1 16 A, 1 17 A. 10 A. 1 () A, 1 9 A, 1 10 A, 1 11 9 DØ DI D I D I A, I A | 11 A | 13 A | 13 A | 14 A | 15 A | 16 A | 16 Α,Ι D2 D3 A, 1 13 A, 1 13 A, 1 14 A, 1 15 A, 1 16 A, 1 17 A, 1 13 A. 1 D3 A, | 14 A, | 15 A, | 15 D3 D4 D5 2 4 5 5 5 2 2 0 0 0 0 0 D4 D5 D6 A, | A, | A, | 16 17 Α, Ι D6 D7 17 07 A. J. A. [1 7 B 8 8



J-ig 8-712 Skt5g5

- 1. REFER TO TABLE 8-102 FOR SCHEMATIC DIAGRAM NOTES.
- 2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL WEASURED VALUES. YOUR MEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.
- 3. UI (ROM 7) IS NOT CURRENTLY SUPPLIED. 1HE CORRESPONDING SOCKET IS RESERVED FOR FUTURE USE.

P/O AZALI DCU MOTHERBOARD

REFERENCE	DESIGNATIONS
A2A9	A2A11
CI-15 RI-10 UI-20	XA2A9



LOGIC	LEVELS		
	TTL		RE
HIGH	>+2¥		DES
LOW	<+0.8V		111-
< IS MORE > IS MORE	NEG.THAN POS.THAN		
OPEN	HIGH		
GROUND	LOW		1117
INTEGR	ATED CIRCU	IT	013
PAR	T NUMBERS		l n re

INTEGRATED CIRCUIT PART NUMBERS			
REFERENCE	PART		
DESIGNATIONS	NUMBERS		
UI-12	1818-0851		
UI3,14,17	5081-2483		
UI5,18,19,	1820-2024		
20	1820-1205		

INTEGRATE VOLTAG GROUND CD	D CIR SE ANU NNECT	CUII) IONS	r S
REFERENCE DESIGNATIONS	PIN NUMBERS		
UI-12	+5v ⊄		24 12
UI3,14,17	+5v ▽	-	16 8
U15,18,19, 20	+5v ⊄	-	20 10
U16	+5v ⊄	-	14 7

CIRCUIT BOARD TOP VIEW COULT 2 COULT 2	CIRCUIT A2A9UI5, 18, 19, 20 BOARD TOP VIEW I C O II8 I C O I8 C D KEY-C O C D O O O O O C D O O O O C D O O O O O	A2A9UI3, [4, 17 BOARD TOP VIEW TOP VIEW I C OU 16 C O I C OU 16 C O I C OU 16 O I C OU I O O I C OU I O O I C OU I O O I O O O I O O O I O O O I O O O I O O O I O O O I O O O I O O O I O O O I O O O I O O O I O O O I O I O O I O O O I O O O I O O O I O O
		A2A9Ui6 TOP VIEW I C OU 14 ID 014 C D KEY ² O 0 C D 0 0 0 0 C D 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0



NOTES





Figure 8-713. P/O A2A3 Peripheral RAM Block Diagrams

Service

Model 8663A







Fig 8-715 Sht 2014 1 (08663-60328) --- ADDRESS BUFFERS -40 18 CB1 - ADDRESS DECODER -PAØ 2 PAØ PAI 3 RSØ 14 ¢] PAI 13 PA2 4 4 RSØ PA2 5 PA3 7 RSI PA3 £2 6 PA3 7 PA4 PA4 12 F PA5 8 9 9 PA6 PA7 ر ک PA6 υı 5 PAT A3/ ÷2 22 24 CSØ 3 A4 -2 +5V · 18 PRW 23 CS2 2 9 RSØ 36 6 RSØ 4 35 RS | RSI 8 Ĝ <u>PH2</u> 16 . I PRW 21 15 나머 프 티 \downarrow READ 21 WRITE 14 13 X-12 2008 06 I UIB 16 11 2009 DEMUX 26 9 NC 36 NC 19 A2 LT F2 U19]3-STATE[12 25 $\dot{\nabla}$ ENABLE 2 150 A3 lG 2 34 A12 13 7 ⊐⊳ F2 3 RST NC A4 5 (MOS) A | 3 9 ⊐ Þ F2 12 UIBB 12 e 01 A14 8 ⊐⊳ F١ 14 AID 13 6 ⊲⊓ FI 5 15 ⊐⊳ F2 2 18 ⊐⊳ FI 3 17 $\Box \triangleright$ F١ PH2 16 4 모〉 F2 JN LATCH -----EXT X2 LATCH PAD-3 7 EXTERNAL DC JBLER B +5V 1 2 UI8A 6 4 8 76 +5¥]1 19 19 14 LT F2 UI7]3-STATE _____F2 U ______]3-\$____TE RIA RIB RIC RIE RIH RIF RII RIG \$4700 \$4700 \$4700 \$4700 \$4700 \$4700 \$4700 \$4700 2 3 4 6 9 7 10 8 12 EX2 PAØ 6 14 ₽ø 2 18 DØ 묘▷ ۴I $\Box \triangleright$ FΙ 14 EX3 ΕI 3 DI PAI 17 13 $\Box \triangleright$ ⊐⊳ F2 F2 12 D2 16 EX4 PA2 E2 16 15 Б ⊐⊳ ₽⊳ FΙ F2 D3 14 18 EX5 E3 PA3 17 3 6 ₽⊳ FΙ ₽⊳ F2 D4 9 EX6 7 EDB I E4 E4 11 9 3 ⊲⊳ ⊐⊳ F2 F2 7 EX7 E5 б DÐ 12 EDB2 15 E5 8 ⊲⊓ ⊲⊳ F2 F١ 5 E6 Ш 9 D6 2 18 ⊐⊳ ⊐⊳ F2 +5V -F١ 12 07, 3 4 16 Ē7 B ⊐⊳ ₽Þ FI FI 2 - POWER SUPPLY DECOUPLING •+5∨ $\begin{array}{c} - U5 - 16 \\ - U5 - 10 \\ - U5 - 10$ - UI-I6 🛉 - U2-16 + - U16-20 +5V. ± ± C4 0.14 CP 51

Jig 8-715 Stl 3 0/4



J-ig 8-715 Sht 484







Figure 8-716. P/O A2A3 Peripheral RAM Block Diagrams





_____ All serial prefixes

CALLER CONTRACTOR OF THE OWNER

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CHANGES

On the A2A3 schematic:

• A2A3 - Modify the schematic as shown in the partial schematic "P/O Figure 8-718. Peripheral RAM Schematic" on page 8-722.3.



SS	51	
8-7	22.	1





P/O Figure 8-718. Peripheral RAM Schematic



Jig 8-718 Sht 18/3

P/0 A2A3 PERIPHERAL RAM(08663-60328)



SERIAL PREFIX: 2234A





8-723/724















Jig 8.721 Sht 30/5







Model 8663A





Service





P/O AZAS FREQUENCY CONTROL (08663-60341) Jug 8-724 Sht 10/3



SERIAL PREFIX: 2234A

Jig 8.724 Stt 243







Figure 8-725. A2A10 Level Control Assembly Block Diagrams

Service

Model 8663A







SERIAL PREFIX: 2234A

Fig 8-727 Sht 2015



Fig 8-727 Stet 3 95


Fig 8-727 Stt 4 85





MICROPROCESSOR

46,47 A2A7















Y.	0 70	· · · ·	
<u>></u>	8-130 Stt 3 95		
		<u>}</u>	
		/ 2	7/
		+'	8
PSFLA LA	FM LEVEL CONTROL MONITOR	8	/
			/
		AM MODE CONTROLS	AN NODE CONTROL MONITOR
20 3 D2 FF 2 MBO		RSELIIDGI	
21 4 D2 FF 5 MBI		RMC 11 > 1C2	θ F2_
22 7 D2 FF 6 MB2	4 IT > F1 16 D2		
13 8 D2 FF 9 MB3	2 FI B		
<u>)4 13 D2 FF 12 M84</u>			
5 14 D2 FF 15 MB5			
16 17 D2 FF 16 MB6			
127 18 D2 FF 19 MB7		D4 13 D2 FF 12 AMEXIA	
	MDDULATION FREQUENCY	D5 14 D2 FF 15 AMHL	
CONTROLS	CONTROL MONITOR	D6 17 D2 FF 16	NCA4-2 13 J > F2 7
<u>61</u>	MSEL3 IN LT FI	D7 18 D2 FF 19	
3 D2 FF 2 ACFØ			1 '
4 D2 FF 5 ACF			
7 D2 FF 6 ACF2			
B D2 FF 9 ACF3			1
14 13 D2 FF 12 ACF4		FREQUENCY CONTROLS	FREQUENCY CONTROL MONITOR
5 14 D2 FF 15 ACF5			
6 17 D2 FF 16 ACF6		KSEL2 INGI	
.7 18 DD FF 19 ACF7			
- FM & PM MODE CONTROL	FM & PM MODE MONITOR	D1 4 D2 FF 5 ACF9	
SELO GI	MSELØ IN IT FI	D2 7 D2 FF 6 ARØ	
		D3 B D2 FF 9 AR	
C 3 D2 FF 2 FM		D4 13 D2 FF 12 INT	
4 02 FF 5 FMI0		05 14 D2 FF 15 HILOZ	
2 7 D2 FF 6 FMEXT		D6 17 D2 FF 16	
3 B D2 FF 9 FMAC	2 7 5 18 03	D7 18 D2 FF 19	
4 13 02 FF 12 PMMODE			
5 14 02 FF 15 PMINT		<u> </u>	5
6 17 DO FF 16 PMEXT			
18 FF 19 PMAC			
		16	





NOTES

1. REFER TO TABLE 8-102 FOR SCHEMATIC DIAGRAM NOTES.

2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.

REFERENCE DESIGNATIONS

NO PREFIX	
W80,84,85	
A2A4	A2A11
C1-6 R1-3 V1-18	XA2A4A XA2A4B J1,2,6,8

INTEGRATED CIRCUIT PART NUMBERS

41110 - 114
PART NUMBERS
1820-1216 1820-2024 1820-1204 1820-1858 1820-2075

INTEGRATED CIRCUIT

LOGIC	LOGIC LEVELS				
	TTL				
HIGH	>+2.0V				
LOW	<+0.8V				
< IS MORE > IS MORE	NEG.THAN Pos.Than				
OPEN	HIGH				
GROUND	LOW				

VOLTAGE AND GROUND CONNECTIONS				
REFERENCE	PIN NUMBERS			
U18	+5¥	-	20 10	
U2-8	+5v ▽	-	20 10	
UI1-17	+5¥ ▽	-	20 10	
VI,10	+5V ▽	-	16 8	
ÜÐ	+5v ⊄	-	14 7	

.....

A2A R2,	4 3 TOF	VIE	W E	
	2 3) #	3	<u> </u>
COMMON	A	в с	Ð	E
		16 KE 9	CII BI TOF Y-7000000000000000000000000000000000000	CUIT DARD VIEW O 16 O O O O O O O O O O O O O O O O O O O

U2-8,11	- 8	BOA				
	р 20 2 кі	iΩ εγ-⊀ο	020	A2A4 U 9 Top view	CIR BO TOP	CUIT ARD VIEW
ומחחחחם		0000000	00000000		1000000	
10 d	ри	10 0	011	́чР°	. U	00

0 7 0 0 U 7 T

A2A4 55

Figure 8-730. A2A4 Modulation Assembly Schematic 8-739/740



Figure 8-731. P/O A2A2 Sweep Control Assembly Block Diagrams

Service

Service

Model 8663A













Model 8663A



Figure 8-734. P/O A2A2 Sweep Control Assembly Block Diagrams

Service

Model 8663A



Figure 8-735. P/O A2A2 Sweep Assembly Component Locator

Fig 8-736 Sht 643



SERIAL PREFIX: 2234A

F-g-8-736 Stt 2 0/2





SERVICE SHEET **57** P/O A2A2 **57** Figure 8-736. P/O A2A2 Sweep Control Assembly Schematic 8-747/748



Figure 8-737. A2A6 HP-IB Assembly Block Diagrams

Service

Model 8663A



Figure 8-738. A2A6 HP-IB Assembly Component Locator

8-750



SERIAL PREFIX: 2234A

Fig 8-739 Sht 20/5



Fig 8-239 Stil 30/5







Figure 8-739. A2A6 HP-IB Assembly Schematic 8-751/752

:



Figure 8-740. A1A3 Main Keyboard Block Diagrams

Jig 8.741 Sht 1 of 2



(ON CIRCUIT SIDE OF BOARD) Fig 8-741 Sht 2 of 2



Figure 8-741. A1A3 Main Keyboard Component Locator

Jug 8-742 Stt 10/3 P/O AIA3 MAIN KEYBOARD ASSEMBLY (08663-60339)



Fig 8-142 Sht 20/5



EXT AC DST A R7 261 27-2 +5V EXT DC DS8 A R8 261 7-3

+57

Jig 8-742 Sht 30/5







Jig 8-742 She 5 af 5





Figure 8-743. A1A2 Sweep Keyboard Block Diagrams
Service







7-ig 8-745 Sht 203



)168)





Fig P-745 Skt 30/3

- I. REFER TO TABLE 8-102 FOR SCHEMATIC DIAGRAM NOTES.
- 2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MIGHT BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.
- 3. BLUE KEY FUNCTIONS ARE INDICATED IN PARENTHESES.

REFERENCE DE	SIGNATIONS
NO PREFIX	A2A10
W77	31
A1A2]
DSI-20 JI RI-20 SI-23	

AIA2 LEFT KEYBOARD					
	KEY		KEY CODE DISPLAY		
			8421	HĖX	
	START FREQ	100	1000	48	
EEP	STOP FREQ	100	1001	49	
N.S.	SPAN FREQ	110	1110	6E	
	LIN 100	110	1001	69	
s	LIN 1000	110	1010	6A	
TEP	SET SIZE	110	1011	69	
ŝ	L06 10%	110	1100	6C	
	LOG 1%	110	1101	6D	
	0.5 m#	110	0100	64	
۵.	lma	110	0101	65	
STE	2ms	110	0110	66	
Ϋ́Ψ	l Oma	110	0111	67	
II	looms	110	1000	68	
	OFF	110	0000	60	
뜅	AUTO	110	0001	61	
Ŵ	MANUAL	110	0010	62	
	SINGLE	110	0011	63	
	1	100	1010	4A	
	2	100	1011	4B	
ERS	3	100	1100	4C	
ARK	4	100	1101	4D	
×	5	100	1110	4E	
	OFF (ALL OFF)	100	1111	4F	

SERVICE SHEET 60

Figure 8-745. A1A2 Sweep Keyboard Schematic 8-759/760

















Fig 8-748 Sht 3095



7-9 8-748 Skt 495



4



Fig 8-748 Sht 50/5



Figure 8-748. P/O A2A1 Keycode Assembly Schematic 8-763/764 Model 8663A



Figure 8-749. P/O A2A1 Keycode Assembly Block Diagrams









SERIAL PREFIX: 2234A





8-767/768





Figure 8-752. P/O A1A1 Display Assembly Block Diagrams



Figure 8-753. P/O A1A1 Display Assembly Component Locator

All serial prefixes



On the A1A1 component locator:

• $\frac{A1A1C6}{J1}$ - Add C6 to the right of and slightly below connector $\frac{A1A1C6}{J1}$.

On the AlAl schematic:

 <u>A1A1C6</u> - In the lower left portion of the schematic, in the circuitry labeled POWER SUPPLY DECOUPLING, add capacitor C6 (2200p) from +5V to ground.



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fig 8-754 SAt # 045 \mathbf{D} FREQUENCY ANNUNCIATORS MODULATION FREQUENCY, MODULATION LEVEL, AMPLITUDE, AND ANNUNCIATOR DISPLAY DATA CA 64. REF ND B T 9 DS 10 DS 1E H5V GHz W75 C1-DS1 J1 R1-U1-FREQUENCY/SWEEP ANNUNCIATOR LATCHES 5 6 12 5 F +5V MHZ 11 02 DSTB7 U24 FI REG khz 4 03 13 05 14 DS 16 45V 3 STATE 5 GHz D2 FF 14 15 MHz D2 FF 13 D2 FF 12 kHz HZ 1 051A 16 051H R 1F 68 H5Y 9 Hz D2 FF 2 CF 02 Ē۶ ANNUNCIATORS 7 6 AF D2 FF 100 19 START 8 FF D2 1 2 3 4 BS2B +5V 17 16 STOP D2 FF ΔF €8 2 3 ↓ 4 DS3A ↓ 5V ATAIDS START 3 4 5 6 6 +5V A C STOP 1 1 2 3 4 4 +5V

fig 8-754 5ht 5 of 5

NOTES

I. SEE TABLE 8- FOR SCHEMATIC DIAGRAM NOTES.

2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.

REFERENCE D	ESIGNATIONS
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CI-5 DSI-14 JI RI-11 UI-31	

AIA1U19,24,25 29-31 TOP VIEW		CIRCUIT BOARD TOP VIEW	
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INTEGRATED CIRCUIT VOLTAGE AND GROUND CONNECTIONS				
REFERENCE DESIGNATIONS	PIN NUMBERS			
UI-1.1,13-18,	+5V	-	7	
21-23	\Diamond	-	6	
U12,26-28	+5V		16	
	\Diamond	-	8	
U19,24,25,	+5∀	-	10	
29-31	$\dot{\nabla}$	-	20	
U20 .	+ 5γ	-	7	
↓ - 14				
<u> </u>				

INTEGRATED CIRCUIT PART NUMBERS			
REFERENCE	PART NUMBERS		
UI-II, [3-18 21-23	1990-0330		
U12,26-28	1820-1216		
U19,24,25 29,30	1820~1997		
020	1990-0399		
U3 I	1820-2102		



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Figure 8-754. P/OA1A1 Display Assembly Schematic

8-771/772



+5V

+5V

+5V

- +5V

+5V

- +5V

Model 8663A



Figure 8-755. P/O A1A1 Display Assembly Block Diagrams



Figure 8-756. P/O A1A1 Display Assembly Component Locator

CHANGES

On the A1A1 schematic:

• <u>A1A1</u> - Under MODULATION MODE ANNUNCIATORS, change R7G to R1G.

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Jig 8-757 Sht 1 09 5

P/O AIAI DISPLAY (08663-60338)

















Figure 8-757. P/O A1A1 Display Assembly Schematic 8-775/776 SERVICE SHEET 65 A7A3 AND A7A4 POWER SUPPLY MOTHERBOARD AND INVERTER

REFERENCE BLOCK DIAGRAM 10

Table 4-1. Recommended Performance Tests After Adjustments or Repairs. Table 5-2. Post-Repair Adjustment Procedures.

PRINCIPLES OF OPERATION

General

Circuitry on the Power Supply Motherboard and Inverter Board rectify and invert the ac line voltage prior to regulation. Before reaching these boards, the line voltage is passed through a filter assembly (A10) and a voltage selection switch (S2), which allow operation at 115 Vac or 220 Vac. Power entering the Supply Motherboard via pins 7-9 is rectified and coarsely filtered to produce ± 160 Vdc. In the case of 115 Vac operation, voltage doubling is implemented during rectification. The ± 160 Vdc is then chopped by inverter/regulator switches Q3 and Q4 to drive the main power supply transformer (A7A3T3).

The ac voltages at the multiple secondary taps of the transformer are rectified, filtered, and sent to the Linear Regulator Board (Service Sheet 66). The +5.2 Vdc line is regulated exclusively by the switching action of the supply. Fast and slow sense lines, as well as foldback and feedback sense lines, are derived from various points along the +5.2 Vdc line. These sense lines are sent to the Control Board (Service Sheet 67) which uses the information they provide to generate two pulse-width modulated signals (180° out of phase) that are fed back to drive the inverter/regulator circuitry (formed by Q1, Q2, Q3, and Q4) on the Inverter Assembly.

The output voltage of the 30 Vac transformer (T1) is rectified, filtered, and regulated to supply the dc oven heater voltage for the 10 MHz reference crystal oscillator and to power sections of the Linear Regulator and Control Boards. Circuitry within the auxiliary regulator section on the Control Board (Service Sheet 67) prevents the instrument from being operated when the line voltage falls below 80 Vac (for 115 Vac operation), or 160 Vac (for 220 Vac operation).

Rectifier, Filter, and Voltage Doubler Circuitry (A7A4)

With the input voltage selection switch in the 220 Vac position, input voltage is full-wave rectified by the bridge formed by CR1, CR2, CR3 and CR4 and is coarsely filtered by capacitors C2 and C3 to produce \pm 160 Vdc. When the selection switch is in the 115 Vac position, CR2 and CR3 form a bridge rectifier and voltage doubler. Here again, the result is \pm 160 Vdc. CR5 and CR6 serve to protect capacitors C2 and C3 from reverse voltage generated during foldback current limiting.

Overvoltage Crowbar

The overvoltage protection circuitry (crowbar) disables the instrument in the event that the rectified dc voltage exceeds <u>+</u>185 Vdc. At this point Q1 fires and switches 5 ohms across the input line, blowing line fuse F1.

Switch Drivers and Inverter/Regulator Switches (A7A3)

In order to drive T3, pulses from the pulse-width modulation circuitry (duty-cycle control logic) on the Control Board are transformer coupled (via A7A3T1 and A7A3T2) to the inverter/regulator switch driver circuitry formed by Q1 and Q2. Q1 and Q2 are turned alternately ON and OFF to drive Q3 and Q4 180° out of phase. This action switches the ± 160 Vdc to produce a 20 kHz alternating drive current through the primary of T3 (see Figure 8-901 below). Q3 and Q4 are turned on by about 350 mA of current at their bases. To reduce the turn-off time of the two transistors, however, two amperes of reverse current is required to turn them off.



Figure 8-901. Switching Waveform at the primary of T3 (See also Figure 8-218, Simplified Power Supply Block Diagram)

A resistive-capacitive divider formed by R1, R2, C1, and C2 indirectly references the primary of T3 to ground, isolating the high voltage section from chassis ground and preventing T3 from overheating due to dc currents caused by imbalances in the switching circuits that drive T3. The circuit formed by R11, R12, and C5 serves to lower the Q of the inductance associated with T3, cutting down on ringing at the emitter of Q3 and the collector of Q4.

Secondary Isolation/Stepdown Transformer (T3) Outputs

Voltages at the secondary taps of T3 are referenced to the +5.2 Vdc supply ground. They are full-wave rectified, coarsely filtered, preregulated, and sent to the Linear Regulator Assembly for final regulation. These unregulated voltages are +23 volts, -13 volts, and - 45 volts.

The fast and slow sense lines, as well as the foldback and feedback sense lines, are derived from the +5.2 Vdc supply line. The duty cycle control logic on the Control Board regulates the +5.2 Vdc supply by varying the duty cycle of the signals which drive inverter/regulator switches Q3 and Q4. Regulation of the +5.2 Vdc supply is accomplished exclusively through the switching action of the supply; no linear regulators are used on this line.

TROUBLESHOOTING

When a power supply problem has been traced through the block diagram troubleshooting procedure to this assembly, use the following procedure to isolate the cause of the problem.

CAUTION

If the LINE fuse is blown, do not insert a new fuse until the cause of the failure has been determined. Inserting a new fuse could cause additional damage if the problem is a shorted diode or transistor on the Power Supply Inverter Assembly (A7A3). Perform steps 1 through 6 before inserting a new FUSE.

- 1. Disconnect the line cord from the rear panel of the Generator.
- 2. Remove the top cover of the instrument.
- 3. Remove the top cover of the power supply (at left-rear of instrument).
- 4. Pull out the A7A3 Inverter Assembly.
- 5. Use an ohm meter or continuity checker to check the following components for shorts:

CR5	CR9	CR13
CR6	CR10	Q3
CR7	CR11	Q4
CR8	CR12	

- 6. If none of these components is shorted, it is safe to install another LINE fuse and continue normal troubleshooting.
- 7. Disconnect the line cord from the rear panel of the Generator.

WARNING

When the A7A3 Inverter Assembly is mounted on its extender board, +160V and -160V are exposed on the traces on this board. Use extreme care.

CAUTION

Removing and installing power supply boards with the line cord plugged in can damage these boards because high voltage is present whenever the line cord is plugged in. Remove the A7A3 Inverter Assembly from the power supply. Insert the extender board for A7A3 (set the switch on the extender board to the "in" position to allow high voltage to appear on the top edge connector of the extender board). Plug in the line cord. Measure the high voltage at the edge connector of the extender board. Use a DVM with the common lead connected to the chassis.

> Pin 5 V = -160 + 20 Vdc Pin 7 V = +160 + 20 Vdc

These values are for 115 VAC line voltage. Higher or lower line voltages produce proportionally more or less dc voltage. If this voltage is not correct, the problem is with the high voltage rectifiers and associated circuitry on A7A4. Troubleshoot this circuitry to find the cause. Otherwise, the problem is on the A7A3 assembly so continue troubleshooting with Step 8.

8. Look at the waveform at pin 3 of transformer T3. This is the output of the switching transistors. The waveform should be as shown in the Figure 8-902, below.



Figure 8-902. Waveform at Pin 2 of T3

If this waveform is normal, the problem is on the secondary side of transformer T3 so continue troubleshooting with step 10 below. If this waveform is not correct, the problem is with the switching transistor circuitry so continue troubleshooting with step 9.

9. Disconnect the line cord from the rear panel of the Generator. Set the switch on the A7A3 extender board to the "out" position. With the switch in this position, no high voltage reaches the A7A3 assembly and the drive signals to the switching transistors can be observed. Compare the actual waveforms to those in Figure 8-903 below.



Figure 8-903. Switching Transistor Drive Signals
Model 8663A

10. Compare the waveforms at the outputs of the rectifiers on the secondary of transformer T3 to those shown in Figure 8-904 below.



Figure 8-904. Transformer (T3) Secondary Waveforms

If any waveform is different, there is a problem with rectifier diodes or the transformer. The magnitude and duty cycle of the pulses will vary with line voltage, but the pulse width at the output of all rectifiers should be the same.

11. Measure the outputs of the rectifier filters at the bottom edge connector of A7A3. Normal values are:

A7A3 Connector Pin	DVM DC Reading	DVM AC Reading
21	>+22.5	<0.05
22	More Negative than -12.50	<0.20
20	More Negative than -44.00	<0.20
17	>+5.40	<0.040
14, 15, 16	<u>></u> +5.20	<0.025

Rectifier	Filter	Outputs
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Figure 8-905. Power Supply Inverter, Motherboard & Line Filter Block Diagrams

Service

Model 8663A





2342A and Above

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2651A and Above

2545A and Above

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221220000 2510A and Above

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On the A7A3 schematic:

• A7A3 - Modify the schematic as shown in the partial schematic "P/O Figure 8-908. A7A3 Power Supply Inverter, Motherboard & Line Filter Schematic (2342A)" on page 8-908.3.

On the A7A3 schematic:

• A7A3 - Modify the schematic as shown in the partial schematic "P/O Figure 8-908. A7A3 Power Supply Inverter, Motherboard & Line Filter Schematic (2510A)" on page 8-908.4.

On the A7A4 schematic:

• A7A4 - Change the part number of the A7A4 Assembly to 08662-60376.

WARNING

If replacing the old A7A4 Power Supply Motherboard with the new board (08662-60376), extreme care should be exercised as the connections are arranged in a different order on the 08662-60376 board. For information regarding installing a new A7A4 Assembly (08662-60376) in an older instrument, refer to Installation Instructions -- HP Part Number 08662-90069. The wiring codes and board labels shown on the schematic are correct.

On the A7A1 schematic:

A7A1 - Change the part number of the A7A1 Assembly to 08662-60347.

 $\underline{A7A1}$ - Change the part number of Q4, Q6, Q8 to 1884-0330 in the table "Transistors and Integrated Circuit Part Numbers."

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Model 8663A

INSTALLATION INSTRUCTIONS

November 14, 1985

HP MODEL 8662A/8663A SYNTHESIZED SIGNAL GENERATORS

Serial Prefix 2537A and Below

INSTRUCTIONS FOR INSTALLING NEW STYLE A7A4 POWER SUPPLY MOTHERBOARD HP PART NUMBER 08662-60376 (REPLACES 08662-60156)

WARNING

Due to wiring configuration changes, product damage or shock hazard could result if the following installation procedure is not followed. Before attempting this procedure, remove the power cord from the signal generator.

PROCEDURE

The input power wiring configuration for HP Part Number 08662-60156 (Serial Prefixes 2537A and below) is different from the wiring configuration of HP Part Number 08662-60376 (Serial Prefixes 2545A and above). Figure 1 (below) shows the proper wiring configuration when installing the new A7A4 Power Supply Motherboard, HP part number 08662-60376.



Figure 1. Wiring Connections for 08662-60376 Power Supply Motherboard (A7A4)

Printed in U.S.A. HP Part Number 08662-90069



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CHANGES

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)	2342A to 2846A	On the A7A3 schematic:
		• A7A3 - Modify the schematic as shown in the partial schematic "P/O Figure 8-908, A7A3 Power Supply Inverter, Motherboard & Line Filter Schematic (2342A to 2846A)" on page 8–908.5
	2510A to 2846A	On the A7A3 schematic:
		• <u>A7A3</u> - Modify the schematic as shown in the partial schematic "P/O Figure 8-908, A7A3 Power Supply Inverter, Motherboard & Line Filter Schematic (2510A to 2846A)" on page 8-908.6
	2545A and above	On the A7A4 schematic:
		• <u>08662-60376</u> - Change the part number of the A7A4 Assembly to 08662-60376.
		NOTE If replacing the old A7A4 power supply motherboard with the new board (08662-60376), extreme care should be excercised as the connections are arranged in a different order on the 08662-60376 board. For information regarding installing a new A7A4 Assembly (08662-60376) in an older instrument, refer to Installation Instructions on page 8-908.1 – HP Part number 08662-90069. The wiring codes and board labels shown on the schematic are correct.
	2651A and above	 On the A7A1 schematic: <u>08662-60347</u> - Change the part number of the A7A1 Assembly to 08662-60347. <u>Q4, Q6, Q8</u> - Change the part number of Q4, Q6, and Q8 to 1884-0330 in the "Table of Transistor and Integrated Circuit Part Numbers."
	2846A and below	 On the schematic: <u>B1</u> - This assembly is not individually backwards compatible except as a total kit – the Fan Replacement Kit is available as HP part number 08662-60383. If your rear panel frame is damaged and your fan needs to be replaced, order the Rear Panel Kit, HP part number 08662-60384. See section 7 for further information.

Service

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CHANGES

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2918A to 3003A	On the A7A3 schematic:
	• <u>B1</u> - Use the partial schematic on page 8-908.7.
	• <u>A7A3</u> - In A7A3 INVERTER, under FULL-WAVE RECTIFIERS AND RIPPLE FIL/TERS, draw an on-page connector extending upward from the node of C9. Label it "A" +23 V. "A" connects to the partial schematic on page 8-908.7.
3017A and above	On the A7A3 schematic:
	• A7A3C1, C2 - Change the value of A7A3C1 and C2 to 22uf.

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A7A3 Schematic Partial, (2342A to 2510A).

NOTE

The circuitry shown on this page for the B1 Fan Motor is no longer accurate. For the current circuitry, see page 8–908.7, 2918A and above. See section 7 for more details.

rev.16JUN89

SS65 8–908.5



Schematic Partial, (RT2 was added). See section 7 (2510A and above).

NOTE

The circuitry shown on this page for the B1 Fan Motor is no longer accurate. For the current circuitry, see page 8–908.7, 2918A and above. See section 7 for more details.

SS65 rev.16JUN89







Partial Schematic for the B1 Fan Change (2918A and above).

Fig 8-908 Sht 1 98 5





Fig 8-908 Skt 3 of 5

A7A3 INVERTER (08662-60289)



Jig 8-908 She 4 of 5



NOTES

- I. REFER TO TABLE 8-102 FOR SCHEMATIC DIAGRAM NOTES.
- 2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.
- 3. THE EQUIVALENT CIRCUIT FOR ATA4Q2 IS SHOWN BELOW (A SILICON UNILATERAL SWITCH).
- 4. IF REPLACING TRANSISTOR SWITCH ATA3Q3 OR Q4, ALSO CHECK THE RECTIFIERS AT THE SECONDARY OF T3.

A7A4Q2 EQIVALENT CIRCUIT



TRANSISTOR PART NUMBERS

A7XP1 REAR VIEW

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ENCIRCLED NUMBERS INDICATE WIRING COLOR CODE

NUMBERS

1853-0442

1884-0268 1884-0091

(05)

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DESIGNATIONS

A7A3 Q1,2 Q3,4

A7A4 Q1 Q2

REFERENCE D	ESIGNATIONS	
NO PREFIX	A7A4	
BI FL2 J2 PI,2 S2 TI W56,57	CI-7 CRI-6 KI QI.2 RI-13 RTI VRI.2 XA7A3	
A7A3	A10	
C1-6,8-13 CR1-14	CI,2 RI	
Q1-4	A7	
Ri-13,15,16 RT1 TI-3 ₩1,2	XPI	
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DANGER HIGH VOLTAGE WARNING

PLUS AND MINUS 160VDC AND LINE VOLTAGE ARE PRESENT WHENEVER THE POWER CABLE IS PLUGGED IN. THIS HIGH VOLTAGE EXISTS ON THE RED HEAT SINK ON OTHER PORTIONS OF THE INVERTER BOARD, AND ON THE MOTHERBOARD. BE EXTREMELY CARE-FUL WHEN WORKING IN THESE AREAS.

BEFORE REMOVING OR INSERTING POWER SUPPLY PLUG-IN BOARDS, DISCONNECT THE AC POWER CABLE AND ALLOW 30 SECONDS FOR THE FILTER CAPACITORS TO DISCHARGE.

FAILURE TO OBSERVE THESE PRECAUTIONS MAY RESULT IN INJURY TO PERSONNEL OR DAMAGE TO THE EQUIPMENT.

CAUTION

DO NOT REPLACE A DAMAGED 'LINE' FUSE UNTIL THE RESISTANCE CHECKS DESCRIBED IN TROUBLESHOOTING HAVE BEEN MADE. TO DO SO COULD CAUSE ADDITIONAL DAMAGE.



Figure 8-908. A7A3 Power Supply Inverter, Motherboard & Line Filter Schematic

8-909/910

SERVICE SHEET 66 A7A1 LINEAR REGULATOR ASSEMBLY

REFERENCE BLOCK DIAGRAM 10 Table 4-1. Recommended Performance Tests After Adjustments or Repairs. Table 5-2. Post-Repair Adjustment Procedures.

PRINCIPLES OF OPERATION

General

The Linear Regulator board regulates the rectified and coarsely filtered dc voltages sent from the Inverter Board (Service Sheet 65). There are three series regulator circuits on this board, the outputs of which are +20 Vdc (at 2.5 amperes), -10 Vdc (at 2.5 amperes), and -40 Vdc (at 0.5 amperes). Additional features incorporated into the regulator circuits are foldback current limiting, transient and high input voltage shutdown, and bi-directional crowbar protection.

Linear Regulator Circuits

Each of the series regulators is designed around an integrated comparator amplifier (regulator). Ul and U2 are LM204 equivalents; U3 is a LM305 equivalent. A compound PNP/NPN transistor pair is used in each circuit to boost its current handling capability. Potentiometers R18, R20, and R39 are adjusted to trim the output voltages of the -40 Vdc, -10 Vdc, and +20 Vdc lines, respectively. Transistors Q11 and Q10 control the current limiting action of U2 and U1 in the -10 Vdc and -40 Vdc regulator circuits. Current limiting for the +20 Vdc line is sensed by a resistive divider formed by R23, R24, R27, and R28. Each regulator circuit has a green LED lamp which indicates that voltage is present at its output.

Bi-Directional Crowbar Protection

A crowbar circuit shunts the output of each of the three linear regulators found on this assembly. Each crowbar protects the circuitry fed by its respective regulator should the regulator fail. When the output of a regulation circuit exceeds its nominal output voltage by more than 3 to 4 volts, or falls more than one volt below ground (one volt above ground for the negative supplies), a triac is turned ON, shorting the output to ground. This puts the supply in a current-limit mode.

High Input Voltage Shutdown Circuitry

The high input voltage detector circuitry protects the regulators against excessive input voltage resulting from transients, open circuit conditions, or malfunctions in the switching-regulator circuitry. If the input line voltage even momentarily exceeds a preset value, the high input-voltage shutdown lamp will latch ON and a signal will be sent to the inverter-drive circuitry, shutting down the power supply.

TROUBLESHOOTING

When a power supply problem has been traced through the block diagram troubleshooting procedure to this assembly, use the procedure below to isolate the cause of the problem.

There are two basic types of problems that are covered by this procedure:

- 1. If red LED in the upper left hand corner of the board is lit indicating the input voltage from the A7A3 Inverter Assembly was too high, and the supply was shut down, it is most likely a problem with the regulator not drawing enough current.
- 2. The input voltage is normal, but the output voltage is not regulated.

Inverter Input Voltage High

1. Disconnect the line cord form the rear panel of the 8663A.

CAUTION

Removing and installing power supply boards with the line cord plugged in can damage these boards because high voltage is present whenever the line cord is plugged in.

Install the A7A1 Linear Regulator Assembly on its extender board. Then plug in the line cord.

If the red LED in the upper left hand corner lights when the POWER switch is turned on, continue troubleshooting with step 2. Otherwise, continue troubleshooting with step 3.

2. Connect a short jumper between the collector of Q16 and ground. This will allow the control board to turn on. Turn the line switch to ON and measure the voltage at the test points in the table below, to identify which supply is malfunctioning.

Measure	Normal Voltage	
TP4	<+30.0	
TP5	<-16.0*	
TP6	<-55.0*	

Linear	Regulator	Inputs
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*The sign < in this case means lower magnitude or more positive than.

When the malfunctioning supply is identified, troubleshoot the regulator circuitry to find the cause of the problem.

Inverter Input Voltage Normal

3. Turn the POWER switch to ON. To determine if any of the supplies are in current limit, measure the voltage across the current sensing resistors specified in the table below.

Resistor	Max Reading (VDC)	Supply
R27, 28	1.25	+20V
R36	0.60	-10V
R37	0.60	-40V

Current	Limiting	Check
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If any reading is above the maximum value given in the table, that supply is current limited so continue troubleshooting with step 4. If the readings are normal, there is a problem with the regulator circuitry so troubleshoot to find the cause.

4. The supply could be in current limit because the load is drawing too much current or because the overvoltage crowbar has triggered. Turn the POWER switch to STANDBY and monitor the output of the supply being tested with an oscilloscope. Set the scope to trigger when the supply turns on and watch how high the voltage rises when the line switch is turned on. Compare this value to the numbers in the table below.

Crowbar Trigger Voltage

Supply	Crowbar Trigger Voltage	
+20V	23.7V	
-10V	11.0V	
-40V	46.4V	

If the actual supply voltage exceeded the trigger voltage, the regulator is defective so troubleshoot the regulator circuitry to find the cause of the problem. Otherwise, the load is drawing too much current so look for the problem in one of the other sections of the instrument.

Model 8663A



Figure 8-909. A7A1 Power Supply Linear Regulator Block Diagrams

Service

Model 8663A

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Figure 8-910. A7A1 Power Supply Linear Regulator Component Locator



On the schematic:

• <u>A7A1U1</u> - In the table of Transistor and Integrated Circuit Part Numbers, change the part number for U1 to 1826-0016.

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SS66 8-916.1 Model 8663A

Service

SERVICE SHEET 67 A7A2 CONTROL ASSEMBLY

REFERENCE BLOCK DIAGRAM 10 Table 4-1. Recommended Performance Tests After Adjustments or Repairs. Table 5-2. Post-Repair Adjustment Procedures.

PRINCIPLES OF OPERATION

General

The Control Board contains the 40 kHz oscillator circuitry, duty-cycle control logic, inverter/regulator drivers, malfunction-disable circuitry, and auxiliary supply regulators.

Oscillator Circuitry

The switching frequency of the supply is established by a free-running multivibrator (U2) which generates a 40 kHz asymmetrical waveform having a 4:1 duty cycle (20 us to 5 us). The 2.5 MHz clock formed by U1 provides update clocking for flip-flops U4A and U4B. The 40 kHz generator is locked to a sub-multiple of the 2.5 MHz oscillator frequency by trigger pulses which reach pin 6 via C21. This assures synchronous operation of the two oscillators, eliminating spurs at the operating frequency of the synthesizer.

Within the pulse-width modulator (duty-cycle control logic formed by U6A and U6B), the 5 us "off" period of the 40 kHz oscillator serves two functions. First, it alternates routing of turn-on trigger pulses from the turn-on level comparator (formed by Q15 and Q18) to the switching transistors. Second, it establishes a 5 us "safety band" which disables both drive signals, assuring that both switching transistors are never turned ON at the same time. Any time during the 20 us period, a drive signal can be started by a turn-on trigger pulse, but it will always be terminated at the next 5 us period. The period of the drive signals depends on the position of the turn-on trigger pulse in the 20 us frame. If it occurs early in the frame, the drive pulses will be marrow.

Duty Cycle Control Logic

The duty cycle control logic generates the pulse-width modulated signals that ultimately drive A7A3T3 (service sheet 65). This circuitry is composed of buffer flip-flops U4A and U4B, driver flip-flops U6A and U6B, and associated NAND gate circuitry (U5 and U7). Flip-flops U4A and U4B act as switching buffers between the duty cycle generator, loop gain amplifier, current foldback limiter, malfunction disable gate, and the driver flip-flops. They assure that control signals to the driver flip-flops are synchronized to occur at the proper times. The pulse-width modulator circuitry is set up in a quad-state logic arrangement. U4B disables the ± 160 volt driver flip-flops whenever it changes state. U4A enables one of the two driver flip-flops whenever it changes state, but never allows both flip-flops to turn on at the same time. Furthermore, no driver flip-flop is allowed to turn ON twice in a row.

Fast and Slow Sense Circuits

Whenever a change in power demand occurs, the fast and slow loops effect a change in power delivered to the input of T3 by varying the duty cycle of the 20 kHz inverter. The fast sense circuit is a direct duty cycle modulated loop which turns the level comparator (Q15 and Q18) ON and OFF for fast and slow reactions to load changes; it also helps to diminish the 120 Hz component on the +5.2 volt line.

The slow sense circuit is an analog loop which senses the output of the +5.2 volt section. The slow loop provides precise (and relatively slow) regulation of the output, while the fast loop handles faster regulation demands. (See the two following circuit descriptions.)

Turn-On Level Comparator (Duty Cycle Trigger)

The turn-on trigger pulses are generated by the level comparator formed by Q15 and Q18. This circuit compares the fast sense line at the junction of the first L-C filter on the +5.2 volt line (Service Sheet 65) with the dc reference voltage obtained from the loop gain amplifier (U9). The voltage on the +5.2 volt fast sense line has a triangular ripple component which slopes positively when a switching transistor is ON and negatively when both transistors are OFF. When the sense voltage falls slightly below the reference voltage, the comparator generates a turn-on trigger pulse. Immediately, one of the switching transistors turns ON and the output starts to rise again. If load demands increase, the sensed voltage drops more rapidly, the trigger pulses are generated earlier, and the switching transistors stay ON for longer periods to supply the increased power demand. Conversely, a reduced load lengthens the negative slope, causing the turn-on trigger pulses to occur later, and the switching transistors conduct for shorter periods.

Constant Voltage Comparator (Loop Gain Amplifier)

To improve regulation of the +5.2 volt supply, losses occurring between the fast sense point on the +5.2 volt line and the final dc output are factored into a level comparison voltage by the loop gain amplifier (U9). This circuit compares the +5.2 volt zener reference voltage with the +5.2 volt output slow loop sense line (Service Sheet 65). Any difference between the two voltages is amplified, slowly varying the dc reference to the turn-on level comparator.

Current Foldback Limiter

The current limit comparator (U8) monitors the output current of the supply by sensing the voltage drop across R12 (service sheet 65). If the load current exceeds a preset limit (due to a short-circuit condition) the circuit is energized and acts to reduce the dc reference voltage applied to the level comparator. This in turn causes the output voltage to drop to a level that holds the supply current at a maximum of 3 amperes.

Stabilizing Waveform Shaper (Exponentiator)

The exponentiator (U10) generates a limit-cycle-regulation waveform which is applied to the base of Q18 in the level comparator. If the output of U6A were HI and duty cycle control logic was producing a duty cycle of less than 50% at the 40 kHz rate, the system would become unstable and oscillator U2 could be forced to drop to 20 kHz operation (resulting in a 10 kHz switching rate). Summation of the output of the stabilizing waveform exponentiator shaper with the +5.2 volt slow loop sense line at the base of Q18 causes the trip point of the compatator to be changed just enough to make sure that comparison is made at 40 kHz rather than 20 kHz as would otherwise happen.

Malufunction Disable Gate

Signals from the overvoltage and overtemperature detector circuits are gated by U5B to disable the inverter drive circuitry whenever a shutdown condition arises. This results in power being turned off to all parts of the system except those powered by the auxiliary supply.

Auxiliary Supply

The auxiliary supply circuitry receives 45 Vac from transformer T1 (Service Sheet 65) located within the power supply on the rear panel of the Signal Generator. The ac is rectified, producing a dc voltage that powers the A8A3 Reference Oscillator oven and drives the \pm 15 volt regulators. CR1 through CR4 form a bridge rectifier which produces a positive and negative voltage with reference to ground. The front panel line switch has no effect on this circuitry and these voltages will be present whenever the Signal Generator is connected to the ac power lines.

When the POWER switch is in the STANDBY position, Q6 is ON, turning Q7 OFF. When Q7 is OFF, Q3, Q4, and Q5 are also OFF, shutting down the ± 15 volt supplies. When the POWER switch is turned ON, Q6 turns OFF and allows current (flowing through Q8) to turn ON Q7 and thus turn ON Q3, Q4, and Q5, which brings up the ± 15 volt supplies. If the ac line voltage decreases, a point will be reached where Q8 is biased OFF. This will turn OFF Q7, which shuts down the ± 15 volt supplies.



TROUBLESHOOTING

When a power supply problem has been traced through the block diagram troubleshooting procedure to this assembly, use the following procedure to find the defective component. Problem symptoms can be divided into two classes:

1. No pulses at TP6 and TP7 (shutdown).

2. +5.2V supply not regulated (too high or too low).

No Pulses at TP6 or TP7.



5.2V Supply Not Regulated

- 1. Turn the line switch to STANDBY, remove the power cord, and remove the A7A3 Inverter Assembly from the power supply.
- 2. Remove the bottom cover from the 8663A. Remove the small pc board that connects the wiring harness to the Power Supply Motherboard. This disconnects the power supply from the rest of the instrument.
- 3. Install the A7A2 Control Board on its extender board. Connect a jumper form TP8 to ground (the top [negative] leads of capacitors C3,4,9,10 in the center of the board make good ground points). Having this jumper connected is the same as turning the line switch on.
- 4. Set an adjustable power supply to +5.2 +0.1 Vdc and connect the "+" terminal to TP1 and the "-" terminal to ground on A7A2.
- 5. Plug in the power cord and follow the procedure in the following table.

Check	Normal Condition	lf Abnormal,		
1. Q17 Emitter	>5.4 Vdc	Current foldback limiter circuit is defective. No current should be flowing through the current sensing resistor on A7A4 so U8 should produce a positive output.		
2. U9 Pin 3	5.20 ± 0.02 Vdc	If the voltage is between 4.6 and 5.8 Vdc, adjust the 5.2V supply pot to bring the voltage into spec. If the voltage is not close or cannot be adjusted, troubleshoot the reference circuit to find the cause of the problem.		
3. Waveform on TP11	As shown in the figure below	There is a problem with U10 or associated circuitry.		
Exponentiator Waveform				
4. Set power supply connected to TP1 ≤5.10 Vdc				
U9 pin 6	>13.0 Vdc	Loop gain amplifier defective.		
U4 pin 5	CMOS logic high (+10 Vdc)	Comparator circuitry defective.		
5. Set power supply connected to TP1 ≥5.30 Vdc				
U9 pin 6	< 0V	Loop gain amplifier defective.		
U4 pin 5	CMOS logic low (0V)	Comparator circuitry defective.		



Figure 8-912. A7A2 Power Supply Control Board Block Diagrams

Service

Model 8663A







On the A7A2 schematic:

• A7A2R37 - Change the value of R37 to 21.5k.

On the schematic:

• <u>A7A2Q2</u> - In the Table of Transistor and Integrated Circuit Part Numbers, change the part number of Q2 to 1854-1046.

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Jig 8-911 Sht 4 of 4 Sht

- NOTES 1. SEE TABLE 8~102 FOR SCHEMATIC DIAGRAM NOTES.
- 2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MAY BE SLIGHTLY DIFFERENT THAN WHAT IS SHOWN.

1,2,14,18, 28, 29, 38, 56

REFERENCE DESIGNATIONS		
NO PREFIX	A7A4	
P2	XATAIA,B	
A7A1	Wį	
C1-19 CR1-5 DS1-4 Q1-16 R1-46 TP1-6 U1-3 VR1-6		



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REMOVING OR INSERTING POWER PLUG-IN BOARDS, DISCONNECT THE CABLE AND ALLOW 30 SECONDS FILTER CAPACITORS TO DISCHARGE.

TO OBSERVE THESE PRECAUTIONS ULT IN THE INJURY TO PERSONNEL OR TO THE EQUIPMENT.



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1,5,14,18, 8,32,42

TRANSISTOR AND INTEGRATED CIRCUIT PART NUMBERS REFERENCE PART DESIGNATIONS NUMBERS 1854-0845 1884-0217 1853-0012 1853-0007 1853-0020 1884-0201 1854-0071 Q1-3 Q4,6,8 Q5,7,9 Q10,11 Q12,13,15 Q14 Q16 U I U2 U3 1826-0473 1826-0016 1820-0247

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ATAIQI-3 BOTTON VIEW

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C (CONNECTED TO CASE)

9 F -

SERVICE SHEET A7A1

Figure 8-911. A7A1 Power Supply Linear **Regulator Schematic**

8-917/918



fig 8-914 Sht 2 of 4





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FAILURE TO OBSERVE MAY RESULT IN INJUR DAMAGE TO EQUIPMENT
Fig 8-914 Sht 4 of 4

→11>-92 OVEN VC OVEN VOLTAGE

NOTES

- I. REFER TO TABLE 8-DIAGRAM NOTES. FOR SCHEMATIC
- 2. TROUBLESHOOTING VALUES ARE TYPICAL. THEY ARE ACTUAL MEASURED VALUES. YOUR MEASUREMENTS MIGHT BE SLIGHTLY OIFFERENT THAN WHAT IS SHOWN.

REFERENCE C	ESIGNATIONS
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P2 Si W58	LI-4 QI-19 RI-57
A7A2	
C1-35 C81-19	A7A4
DSI	XATA2

LOGIC LEVELS				
	TTL	CMOS		
HIGH	>+2¥	>+3.5V		
LOW	<+0.8V	<+1.5V		
< IS MORE NEG. THAN				
> IS MORE POS. THAN				
OPEN	HIGH	LOW		
GROUND	LOW	HIGH		

TRANSISTOR AND INTEGRATED CIRCUIT PART NUMBERS			
REFERENCE	PART		
DESIGNATIONS	NUMBERS		
Q1 Q2 Q3 Q4,8,15,17 Q5-7,11,13 16,19 Q9,19 Q10,12 Q14	1853-0053 1854-0475 1853-0442 1853-0007 1854-0210 1884-0201 1854-0012		
U1	1820-1746		
U2	1826-0180		
U3	1826-0192		
U4,6	1820-1963		
U5	1820-1965		
U7	1820-1745		
U8	1826-0013		
U9,10	1826-0488		

INTEGRATED CIRCUIT Voltage and Ground Connections			
REFERENCE	PIN NUMBERS		
ÜΙ	+107 - 1		
	\\$ - 8		
U4−7	+10V - 14		
	r - ب [\]		



PLUS AND MINUS 160VOC AND LINE VOLTAGES ARE PRESENT WHENEVER THE POWER CABLE IS PLUGGED IN. THIS HIGH VOLTAGE EXISTS ON THE RED HEAT SINK, ON OTHER PORTIONS OF THE INVERTER BOARD, AND ON THE MOTHERBOARD. BE EXTREMELY CAREFUL WHEN WORKING IN THESE AREAS.

BEFORE REMOVING OR INSERTING POWER SUPPLY PLUG-IN BOARDS, DISCONNECT THE AC POWER CABLE AND ALLOW 30 SECONDS FOR THE FILTER CAPACITORS TO DISCHARGE.

FAILURE TO OBSERVE THESE PRECAUTIONS MAY RESULT IN INJURY TO PERSONNEL OR DAMAGE TO EQUIPMENT.

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Figure 8-914. A7A2 Power Supply Control Board Schematic 8-927/928