ALPHA & OMEGA SEMICONDUCTOR, LTD.

Rev 1: June 2004

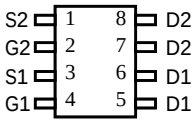
AO4612, AO4612L (Green Product)
Complementary Enhancement Mode Field Effect Transistor

General Description

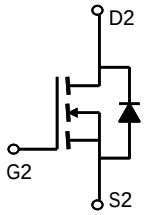
The AO4612 uses advanced trench technology MOSFETs to provide excellent $R_{DS(ON)}$ and low gate charge. The complementary MOSFETs may be used in H-bridge, Inverters and other applications. AO4612L (Green Product) is offered in a lead-free package.

Features

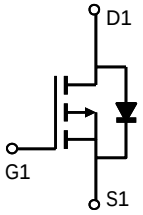
n-channel	p-channel
V_{DS} (V) = 60V	-60V
I_D = 4.5A	-3.2A
$R_{DS(ON)}$	$R_{DS(ON)}$
< 56mΩ (V_{GS}=10V)	< 105mΩ (V_{GS} = -10V)
< 77mΩ (V_{GS}=4.5V)	< 135mΩ (V_{GS} = -4.5V)



SOIC-8



n-channel



p-channel

Absolute Maximum Ratings T_A=25°C unless otherwise noted

Parameter		Symbol	Max n-channel	Max p-channel	Units
Drain-Source Voltage		V_{DS}	60	-60	V
Gate-Source Voltage		V_{GS}	±20	±20	V
Continuous Drain Current [A]	T_A=25°C	I_D	4.5	-3.2	A
	T_A=70°C		3.6	-2.6	
Pulsed Drain Current [B]		I_{DM}	20	-20	
Power Dissipation	T_A=25°C	P_D	2	2	W
	T_A=70°C		1.28	1.28	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	-55 to 150	°C

Thermal Characteristics: n-channel and p-channel

Parameter		Symbol	Device	Typ	Max	Units
Maximum Junction-to-Ambient [A]	t ≤ 10s	$R_{\theta JA}$	n-ch	48	62.5	°C/W
Maximum Junction-to-Ambient [A]	Steady-State		n-ch	74	110	°C/W
Maximum Junction-to-Lead [C]	Steady-State	$R_{\theta JL}$	n-ch	35	60	°C/W
Maximum Junction-to-Ambient [A]	t ≤ 10s	$R_{\theta JA}$	p-ch	48	62.5	°C/W
Maximum Junction-to-Ambient [A]	Steady-State		p-ch	74	110	°C/W
Maximum Junction-to-Lead [C]	Steady-State	$R_{\theta JL}$	p-ch	35	40	°C/W

N Channel Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	I_D=250µA, V_{GS}=0V	60			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=48V, V_{GS}=0V			1	µA
		T_J=55°C			5	
I_{GSS}	Gate-Body leakage current	V_{DS}=0V, V_{GS}= ±20V			100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS} I_D=250µA	1	2.1	3	V
$I_{D(ON)}$	On state drain current	V_{GS}=10V, V_{DS}=5V	20			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V, I_D=4.5A		46	56	mΩ
		T_J=125°C		79		
		V_{GS}=4.5V, I_D=3A		64	77	mΩ
g_{FS}	Forward Transconductance	V_{DS}=5V, I_D=4.5A		11		S
V_{SD}	Diode Forward Voltage	I_S=1A,V_{GS}=0V		0.74	1	V
I_S	Maximum Body-Diode Continuous Current				3	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=30V, f=1MHz		450	540	pF
C_{oss}	Output Capacitance			60		pF
C_{rss}	Reverse Transfer Capacitance			25		pF
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, f=1MHz		1.65	2	Ω
SWITCHING PARAMETERS						
Q_g(10V)	Total Gate Charge	V_{GS}=10V, V_{DS}=30V, I_D=4.5A		8.5	10.5	nC
Q_g(4.5V)	Total Gate Charge			4.3	5.5	nC
Q_{gs}	Gate Source Charge			1.6		nC
Q_{gd}	Gate Drain Charge			2.2		nC
$t_{D(on)}$	Turn-On DelayTime	V_{GS}=10V, V_{DS}=30V, R_L=6.7Ω, R_{GEN}=3Ω		4.7		ns
t_r	Turn-On Rise Time			2.3		ns
$t_{D(off)}$	Turn-Off DelayTime			15.7		ns
t_f	Turn-Off Fall Time			1.9		ns
t_{rr}	Body Diode Reverse Recovery Time	I_F=4.5A, dI/dt=100A/µs		27.5	35	ns
Q_{rr}	Body Diode Reverse Recovery Charge	I_F=4.5A, dI/dt=100A/µs		32		nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any a given application depends on the user's specific board design. The current rating is based on the t≤ 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C. The $R_{\theta JA}$ is the sum of the thermal impedence from junction to lead $R_{\theta JL}$ and lead to ambient.

D. The static characteristics in Figures 1 to 6 are obtained using 80µs pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: N-CHANNEL

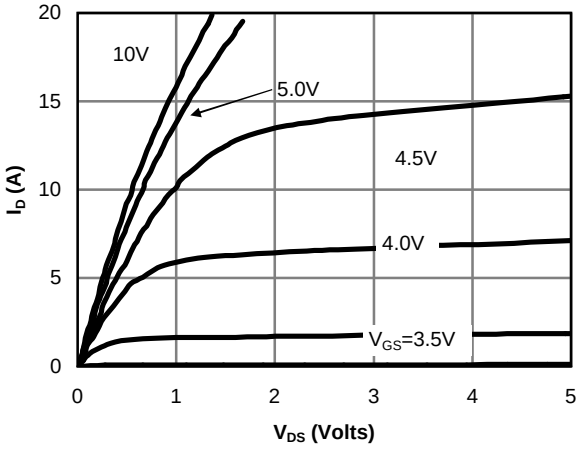


Fig 1: On-Region Characteristics

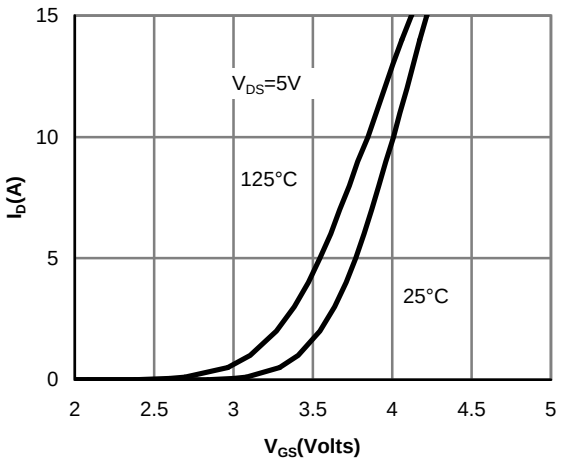


Figure 2: Transfer Characteristics

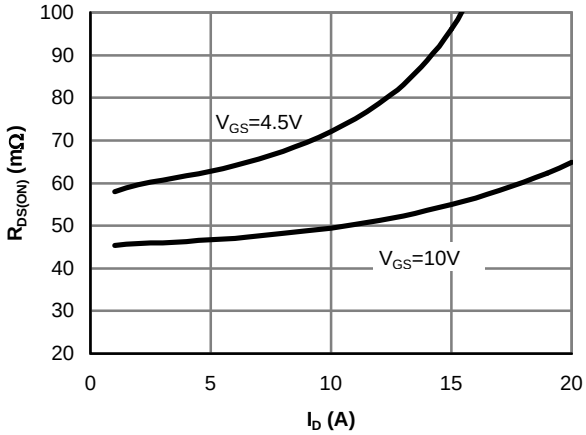


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

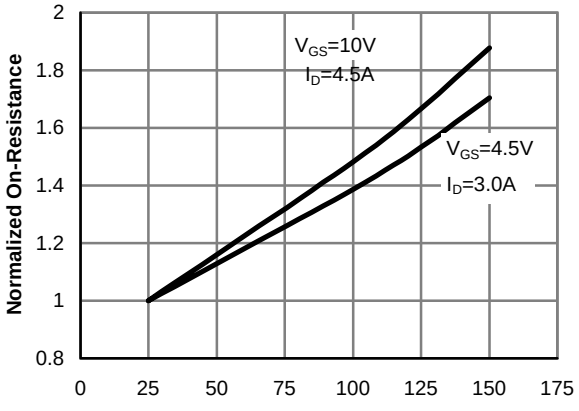


Figure 4: On-Resistance vs. Junction Temperature

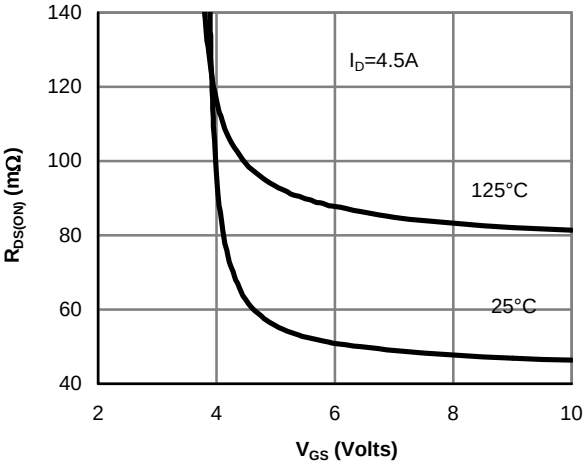


Figure 5: On-Resistance vs. Gate-Source Voltage

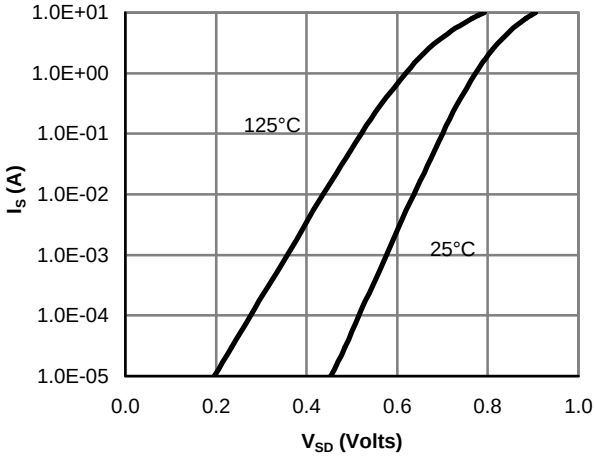


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: N-CHANNEL

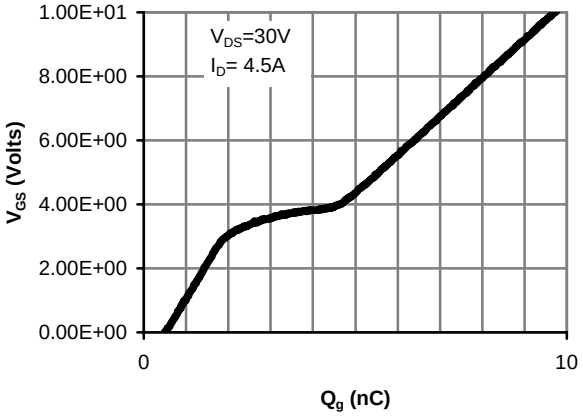


Figure 7: Gate-Charge Characteristics

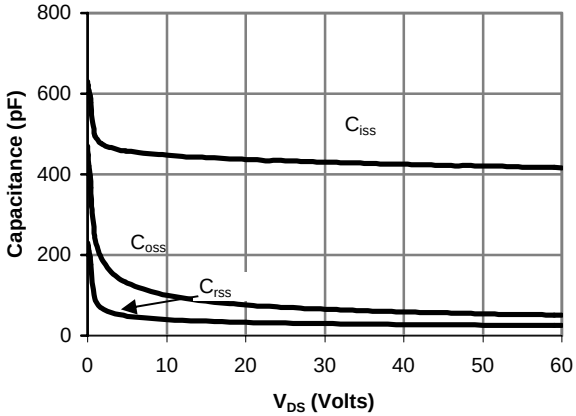


Figure 8: Capacitance Characteristics

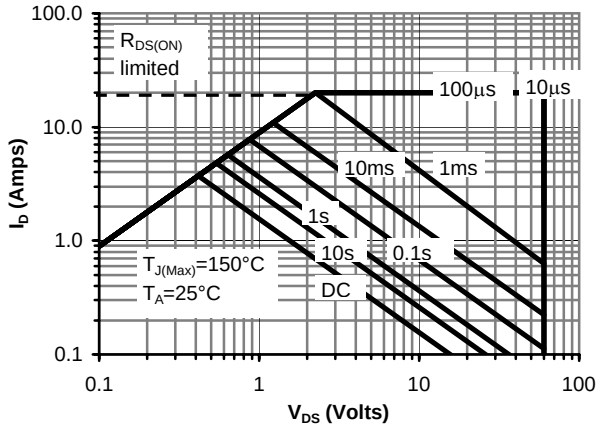


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

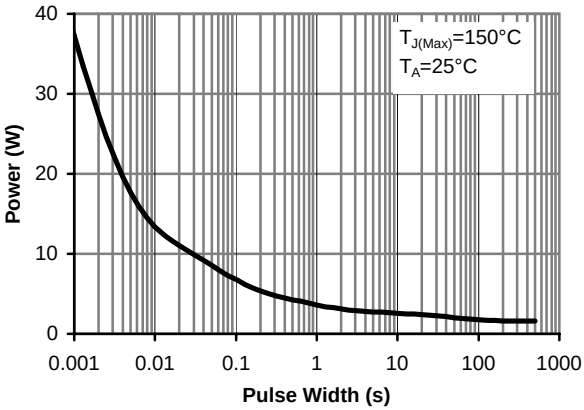


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

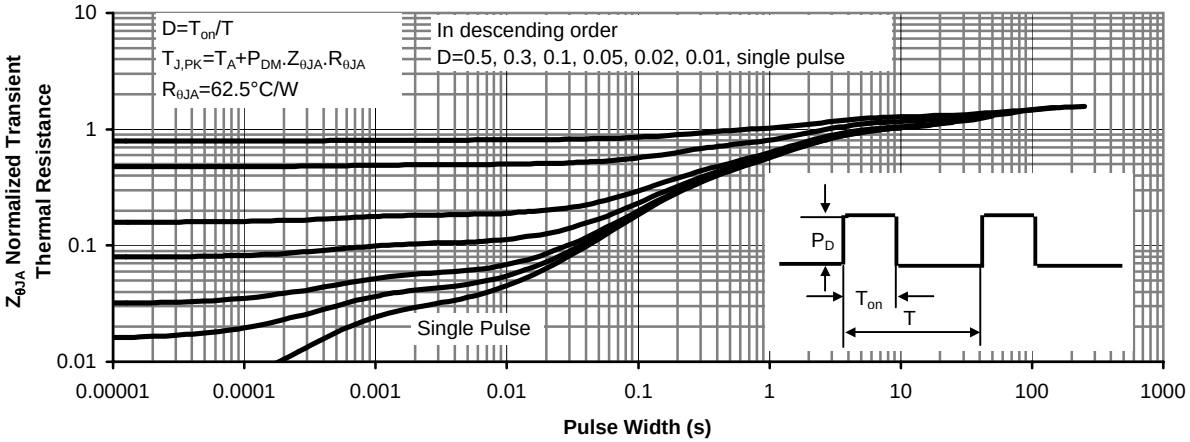


Figure 11: Normalized Maximum Transient Thermal Impedance

P-Channel Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	I_D=-250µA, V_{GS}=0V	-60			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=-48V, V_{GS}=0V			-1	µA
		T_J=55°C			-5	
I_{GSS}	Gate-Body leakage current	V_{DS}=0V, V_{GS}=±20V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS} I_D=-250µA	-1	-2.1	-3	V
$I_{D(ON)}$	On state drain current	V_{GS}=-10V, V_{DS}=-5V	-20			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-10V, I_D=-3.2A		84	105	mΩ
		T_J=125°C		145		
		V_{GS}=-4.5V, I_D=-2.8A		106	135	mΩ
g_{FS}	Forward Transconductance	V_{DS}=-5V, I_D=-3.2A		9		S
V_{SD}	Diode Forward Voltage	I_S=-1A,V_{GS}=0V		-0.73	-1	V
I_S	Maximum Body-Diode Continuous Current				-3	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=-30V, f=1MHz		930	1120	pF
C_{oss}	Output Capacitance			85		pF
C_{rss}	Reverse Transfer Capacitance			35		pF
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, f=1MHz		7.2	9	Ω
SWITCHING PARAMETERS						
Q_g(10V)	Total Gate Charge (10V)	V_{GS}=-10V, V_{DS}=-30V, I_D=-3.2A		16	20	nC
Q_g(4.5V)	Total Gate Charge (4.5V)			8	10	nC
Q_{gs}	Gate Source Charge			2.5		nC
Q_{gd}	Gate Drain Charge			3.2		nC
$t_{D(on)}$	Turn-On DelayTime	V_{GS}=-10V, V_{DS}=-30V, R_L=9.4Ω, R_{GEN}=3Ω		8		ns
t_r	Turn-On Rise Time			3.8		ns
$t_{D(off)}$	Turn-Off DelayTime			31.5		ns
t_f	Turn-Off Fall Time			7.5		ns
t_{rr}	Body Diode Reverse Recovery Time	I_F=-3.2A, dI/dt=100A/µs		27	35	ns
Q_{rr}	Body Diode Reverse Recovery Charge	I_F=-3.2A, dI/dt=100A/µs		32		nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The value in any a given application depends on the user's specific board design. The current rating is based on the t≤ 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C. The $R_{\theta JA}$ is the sum of the thermal impedence from junction to lead $R_{\theta JL}$ and lead to ambient.

D. The static characteristics in Figures 1 to 6,12,14 are obtained using 80µs pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: P-CHANNEL

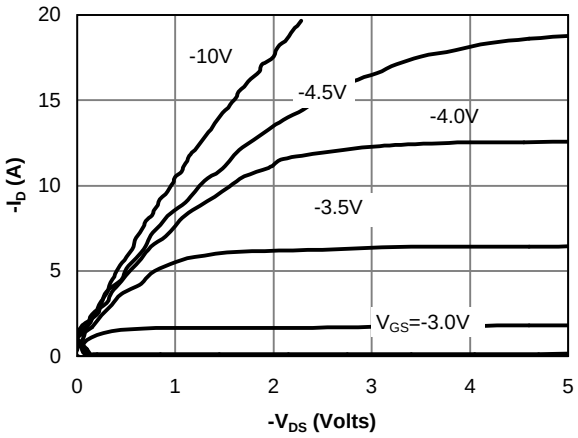


Fig 1: On-Region Characteristics

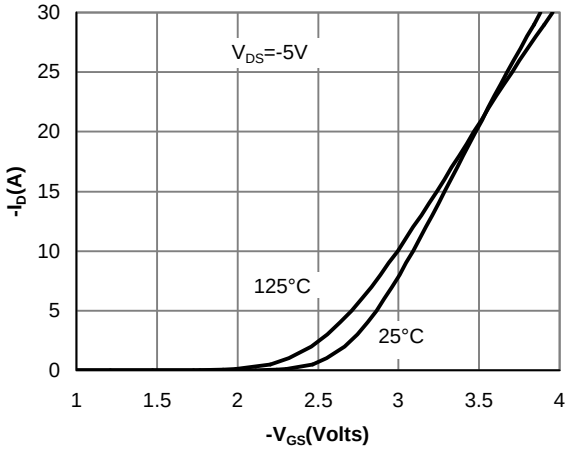


Figure 2: Transfer Characteristics

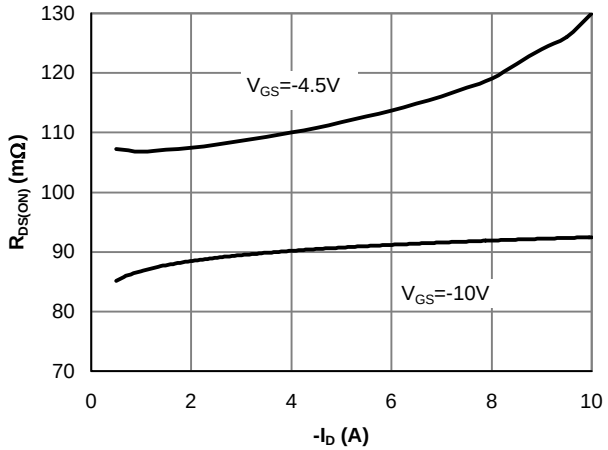


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

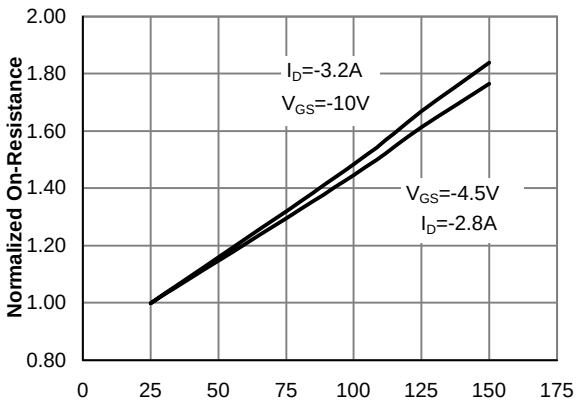


Figure 4: On-Resistance vs. Junction Temperature

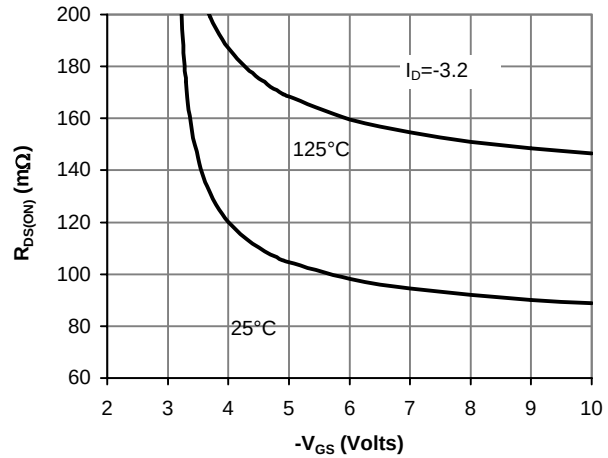


Figure 5: On-Resistance vs. Gate-Source Voltage

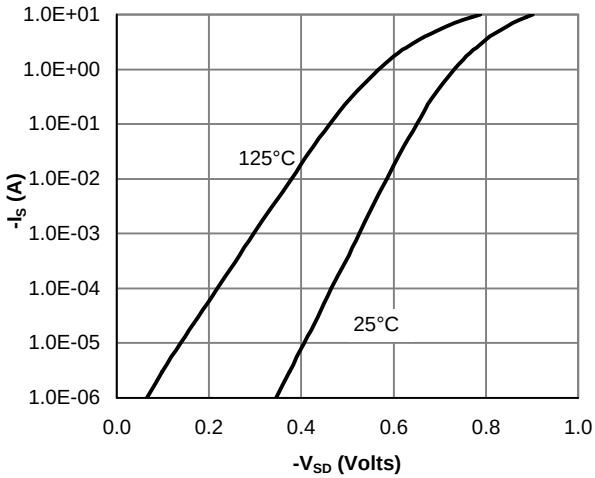


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: P-CHANNEL

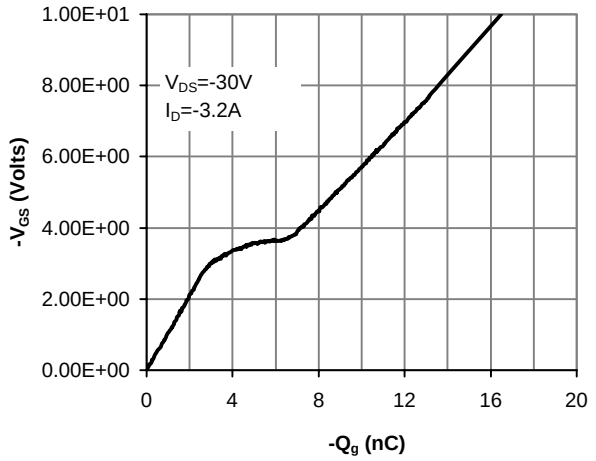
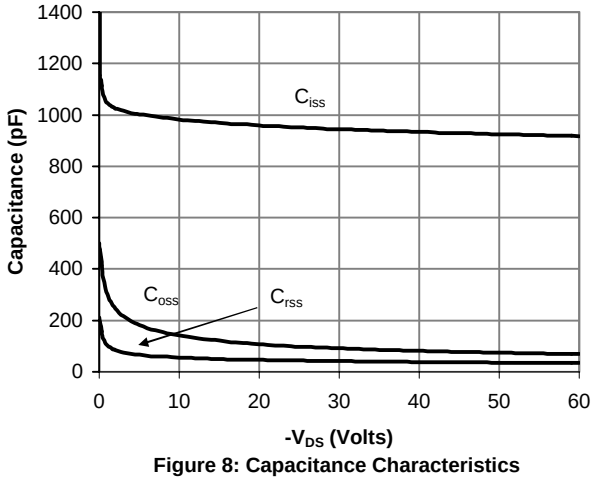


Figure 7: Gate-Charge Characteristics



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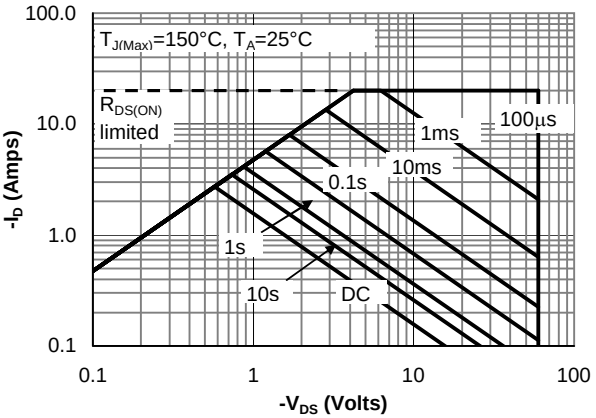


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

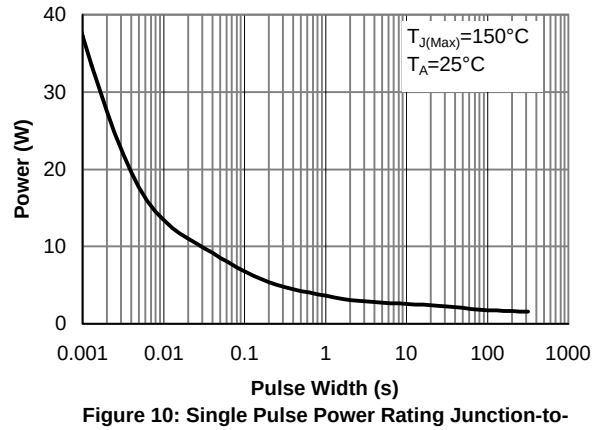


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

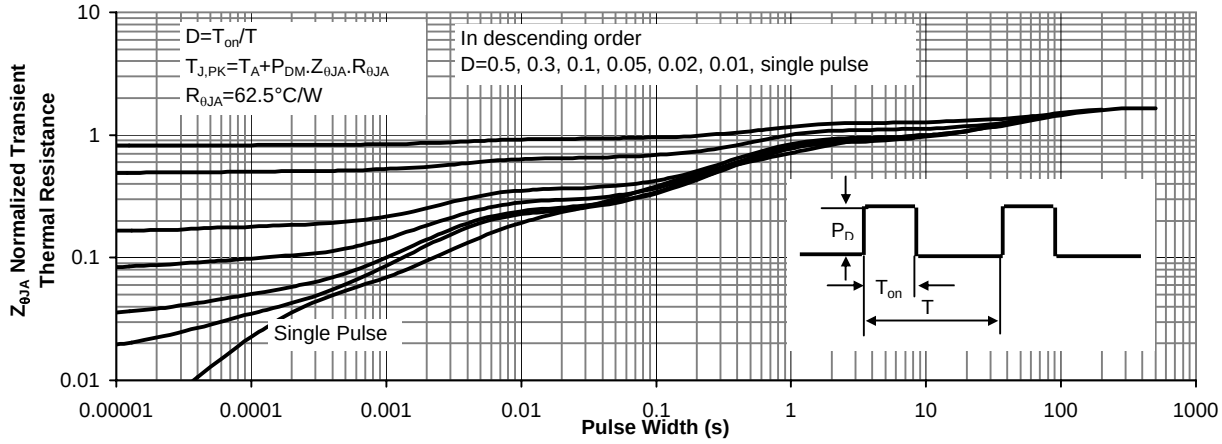
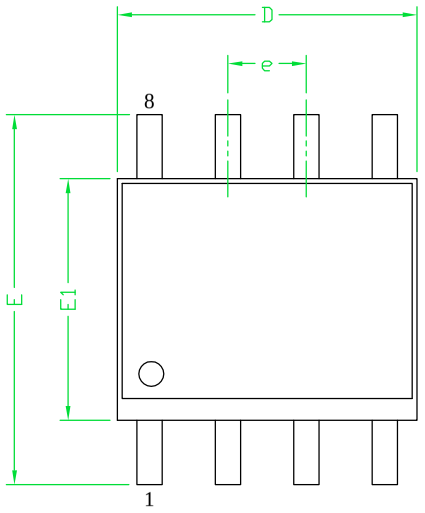
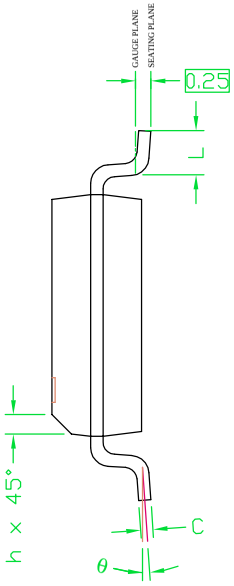
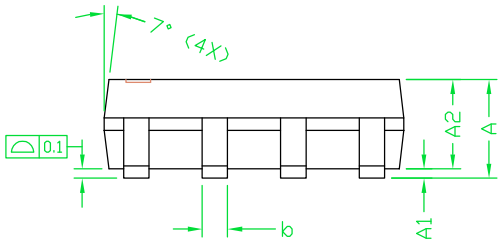


Figure 11: Normalized Maximum Transient Thermal Impedance



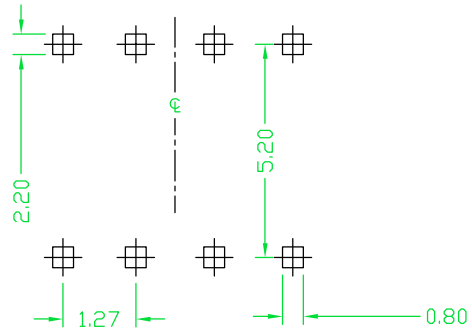




NOTE
1. ALL DIMENSIONS ARE IN MILLMETERS.
2.DIMENSIONS ARE INCLUSIVE OF PLATING.
3.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
4. DIMENSION L IS MEASURED IN GAUGE PLANE.
5. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.35	1.65	1.75	0.053	0.065	0.069
A1	0.10	——	0.25	0.004	——	0.010
A2	1.25	1.50	1.65	0.049	0.059	0.065
b	0.31	——	0.51	0.012	——	0.020
c	0.17	——	0.25	0.007	——	0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	1.27 BSC			0.050 BSC		
E	5.80	6.00	6.20	0.228	0.236	0.244
h	0.25	——	0.50	0.010	——	0.020
L	0.40	——	1.27	0.016	——	0.050
θ	0°	——	8°	0°	——	8°

RECOMMENDED LAND PATTERN



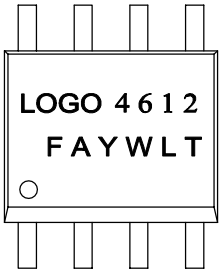
UNIT: mm

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES DECIMAL X.X ± X.XX ± X.XXX ± ANGULAR ± INTERPRET DIM AND TOL PER ASME Y14.5M – 1994 PRINTING IS SCALED TO FIT DO NOT SCALE DRAWING	THIRD ANGLE PROJECTION	ALPHA & OMEGA SEMICONDUCTOR, LTD.
	Document No.	PD–00004
	Version	rev C
	Title	SO–8 PACKAGE OUTLINE

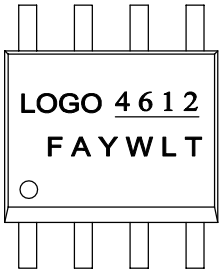
Document No.	PD-00238
Version	rev B
Title	AO4612 Marking Description

ALPHA & OMEGA
SEMICONDUCTOR, LTD.

SO-8 PACKAGE MARKING DESCRIPTION



Standard product



Green product

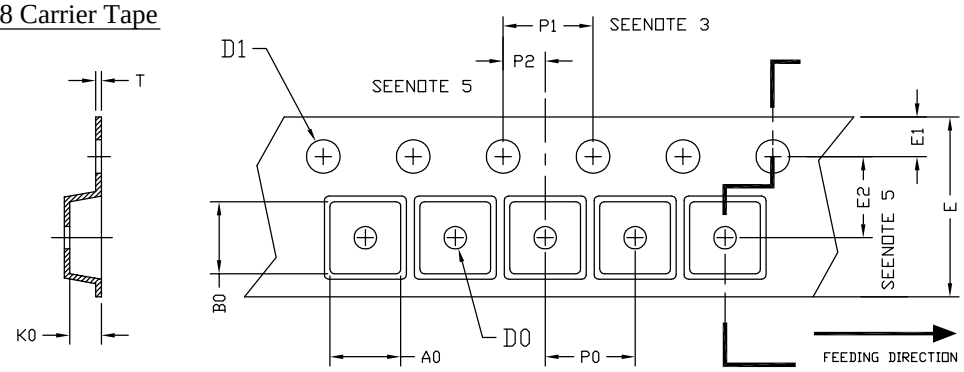
NOTE:
LOGO - AOS LOGO
4612 - PART NUMBER CODE.
F&A - FOUNDRY AND ASSEMBLY LOCATION
Y - YEAR CODE
W - WEEK CODE.
L T - ASSEMBLY LOT CODE

PART NO.	DESCRIPTION	CODE
AO4612	Standard product	4612
AO4612L	Green product	4612

ALPHA & OMEGA
SEMICONDUCTOR, LTD.

SOP-8 Tape and Reel Data

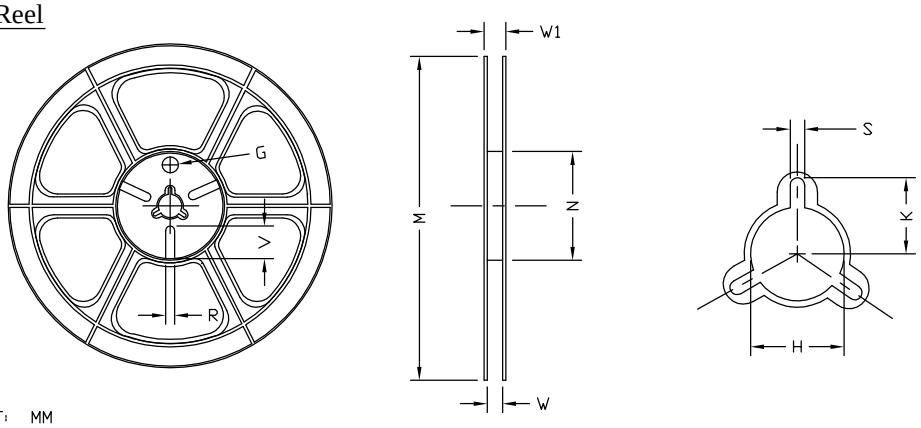
SOP-8 Carrier Tape



UNIT: MM

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SO-8 (12 mm)	6.40 ±0.10	5.20 ±0.10	2.10 ±0.10	1.60 ±0.10	1.50 +0.10	12.00 ±0.30	1.75 ±0.10	5.50 ±0.05	8.00 ±0.10	4.00 ±0.10	2.00 ±0.05	0.25 ±0.05

SOP-8 Reel



UNIT: MM

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	ø330	ø330.00 ±0.50	ø97.00 ±0.10	13.00 ±0.30	17.40 ±1.00	ø13.00 +0.50 −0.20	10.60	2.00 ±0.50	---	---	---

SOP-8 Tape

Leader / Trailer & Orientation