# TYPES SN7406, SN7416, SN5406, SN5416 HEX INVERTER BUFFERS/DRIVERS WITH OPEN-COLLECTOR HIGH-VOLTAGE OUTPUTS

DECEMBER 1983 - REVISED MARCH 1988

- . Converts TTL Voltage Levels to MOS Levels
- · High Sink-Current Capability
- Input Clamping Diodes Simplify System Design
- Open-Collector Driver for Indicator Lamps and Relays
- Inputs Fully Compatible with Most TTL Circuits
- Package Options Include Standard Plastic (N) and Ceramic (J) 300-mil Dual-In-Line Packages and Plastic Small Outline (D) Packages.

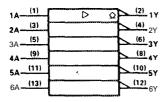
SN5406, SN5416 . . . J PACKAGE SN7406. SN7416 . . . D OR N PACKAGE (TOP VIEW)

	,
1A[[1	U14D VCC
17 □2	13 🗖 6A
$_{2A}\square$ 3	12 O 6Y
2Y 🗖 4	11 5A
3 A <b>□</b> 5	10 <b>∑</b> 5Y
3Y 🗖 6	9 🗖 4 A
SND 🗖 7	8 🗖 4Y

### description

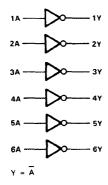
These monolithic TTL hex inverter **buffers/drivers** feature high-voltage open-collector **outputs** for **interfacing** with high-level circuits (such **as** MOS), **or** for driving high-current **loads** (such as **lamps orrelays**), and are also characterized for use as inverter **buffers** for driving TTL inputs. The SN5406 and SN7406 have **minimum** breakdown **voltages** of **30 volts** and **the** SN5416 and SN7416 have **minimum** breakdown voltages of 15 **volts**. The maximum sink **current** is 30 milliamperes for **the** SN5406 and SN5416, **and** 40 milliamperes for the SN7406 and SN7416.

# logic symbol†

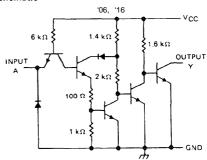


This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

#### logic diagram (positive logic)



#### schematic



Resistor values shown are nominal

# TYPES SN7406, SN7416, SN5406, SN541 6 HEX INVERTERBUFFERS/DRIVERS WITH OPEN-COLLECTOR HIGH-VOLTAGE OUTPUTS

#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply	voltage,	VCC (see	Note 1		7 v-
Input	voltage	(see Note	1)		. 5.5 v
Output vo	Itage (see Notes 1	and 2): SN54	6. SN7406	Circuits	30 v
		S N 5 4 1	6, SN741	6 Circuits	15v
Operating f	ree air temperatui	re range: SN <b>5</b> 4	<b>06</b> , SN5416	6 Circuits	55°C to 125°C
		SN740	6. SN7416	Circuits	0°C to 70°C
Storage	temperature	ra	nge		- 65°C to 150°C

NOTES 1. Voltage values are with respect to network ground terminal. This is the maximum voltage which should be applied to any output when it is in the off state.

#### recommended operating conditions

			SN5406			SN7406		
			S N 5 4	1 6		SN7416	i	
		MIN	NOM	MAX	MIN	NOM	MAX	HNIT
V <sub>CC</sub> Supply voltage		4.5	5	5.5	4.75	5	5.25	V
VIH High-levelinput voltage		2			2			٧
VIL Low-level input voltage				0.8			0.8	٧
VOH High-leveloutput voltage	'06			30			30	· v
*OHTHSH-EVELOGEDOL VOITage	'16			15			15	٠
IOL Low-level output current				30			40	m A
TA Operating free-air temperature	•	- 55		125	0		70	С

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS <sup>†</sup>			SN5406 SN5416			SN7406 SN7416			UNIT	
					MIN	TYP#	MAX	MIN	TYP #	MAX	
VIK	VCC = MIN,	I <sub>I</sub> = - 12 mA					- 1.5			- 1.5	V
ГОН	VCC = MIN,	V <sub>IL</sub> = 0.8 V,	V <sub>OH</sub> = \$				0.25			0.25	mA
V	VCC = MIN.	V <sub>IH</sub> = 2 V		10L = 16 mA			0.4			0.4	V
VOL	VCC - 101114,	VIH - 2 V		10L = 4			0.7			0.7	1 °
11	VCC = MAX.	V <sub>1</sub> = 5.5 V		•			1			1	mA
чн	VCC = MAX,	V <sub>IH</sub> = 2.4 V			-		40	T		40	μА
'IL	VCC = MAX.	V <sub>IL</sub> = 0.4 V					- 1.6	<b></b> -		- 1.6	mA
<sup>1</sup> ссн	V <sub>CC</sub> = MAX					30	48		30	48	mA
CCL	V <sub>CC</sub> = MAX					32	51		32	51	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

# switching characteristics, VCC = 5 V, TA = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN TYP	MAX	UNIT
'PLH	^	٧	R <sub>1</sub> = 110 Ω CL= 15 pF	10	15	ns
'PHL	A	•	ME = 110 M	15	23	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1



<sup>1</sup> All typical values are at  $V_{CC}$  = 5 V,  $T_A$  = 25°C.  $$V_{OH}$  = 30 V for '06 and 15 V for '16.  $$1_{OL}$  = 30 mA for SN54' and 40 mA for SN74'.

D3518, MAY 1990

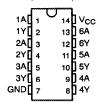
- Converts TTL Voltage Levels to MOS Levels
- · High Sink-Current Capability
- Input Clamping Diodes Simplify System Design
- Open-Collector Driver for Indicator Lamps and Relays
- Package Options Include Standard Plastic (N) and Ceramic (J) 300-mil Dual-In-Line Packages, Plastic Small Outline (D) and Ceramic Chip Carrier (FK) Package

# description

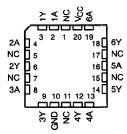
These monolithic hex inverter buffers/drivers feature high-voltage open-collector outputs to interface with high-level circuits (such as MOS), or for driving high-current loads, and are also characterized for use as inverter buffers for driving TTL inputs. The 'LS06 has a rated output voltage of 30 V. The maximum sink current for the SN54LS06 is 30 mA and for the SN74LS06 it is 40 mA.

These circuits are compatible with most TTL families. Input are diode-clamped to minimize transmission-effects, which simplifies design. Typical power dissipation is 175 mW and average propagation delay time is 8 ns.

#### SN54LS06 ... J PACKAGE SN74LS06 ... D OR N PACKAGE (TOP VIEW)



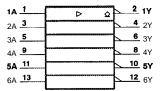
SN54LS06 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

The SN54LS06 is characterized over the full military temperature range of -55°C to 125°C. The SN74LS06 is characterized for Operation from 0°C to 70°C.

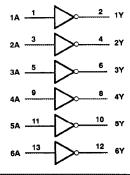
# logic symbol<sup>†</sup>



<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1964 and IEC Publication 617-12.

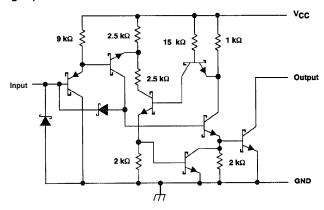
Pin numbers shown are for D, J, and N packages.

# logic diagram (positive logic)





# schematic (each gate)



# absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, Vcc	7 v
Input voltage, V <sub>I</sub> (see Note 1)	5.5 v
Output voltage, Vo (see Notes 1 and 2): SN54LS06, SN74LS06	30 V
Operating free-air temperature range: SN54LS06,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	- 55°C to 125°C
SN74LS06,,,,,	., 0°C to 70°C
Storage temperature range	65°C to 150°C

<sup>†</sup> Stresses beyond those listed **under** "absolute maximum ratings" may **cause** permanent darnage to the device. This are stress ratings **only**, and **functional** Operation of the device at these or any other conditions beyond those indicated **under** 'recommended operating conditions" is not **implied**. Exposure to absolute-maximum-rated conditions for extended **periods** may **affect** device **reliability**.

NOTES: 1. Voltage values are with respect to network ground terminal.

# recommended operating conditions

			SN54LS06			SN74LS06			UNIT
			MIN	NOM	MAX	MIN	NOM	MAX	
Vcc	Supply voltage		4.5	5	5.5	4.75	5	5.25	٧
VIH	High-level input voltage		2	4		2			V
VIL	Low-level input voltage			NA	0.8			0.8	V
VOH	High-level output voltage	'LS06	8	OF THE W	30			30	٧
ĪOL	Low-level output current			-	30			40	mA
TA	Operating free-air temperature		- 55		125	0		70	°C

<sup>2.</sup> This is the maximum voltage that should be applied to any output when it is in the off state.

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS†			S	N54LS06	3	s	UNIT		
TARAMETER	1	1201 00110111		MIN	TYP‡	MAX	MIN	TYP‡	MAX	
VIK	V <sub>CC</sub> = MIN,	l <sub>l</sub> = – 12 mA		- 1.5			- 1.5			V
ЮН	VCC - MIN,	V <sub>IL</sub> = 0.8 V	'LS06, V <sub>OH</sub> = 30 V			0.25			0.25	mA
	V <sub>CC</sub> = MIN,		I <sub>OL</sub> = 16 mA		0.25	0.4		0.25	0.4	
Vol		V <sub>IH</sub> = 2 V	I <sub>OL</sub> = 30 mA	10 Mill		0.7				٧
	"		I <sub>OL</sub> = 40 mA		OLLIER OLLIER				0.7	
l <sub>l</sub>	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 7 V		ক্	6K	1			1	mA
1 <sub>IH</sub>	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 2.4 V			•	20			20	μΑ
ΊL	V <sub>CC</sub> =MAX,	V <sub>I</sub> = 0.4 V				0.2			- 0.2	mA
Іссн	V <sub>CC</sub> = MAX					18			18	mA
Icci	V <sub>CC</sub> = MAX					60			60	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

# switching characteristics, VCC = 5 V, TA = 25°C (see Note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CO	MIN	TYP	MAX	UNIT	
tpLH	Δ	· v	RL = 110 Ω,	Cı = 15 pF		7	15	ns
tPHL	A	Ť	ri <u>r</u> = 110 sz,	OL = 10 pt		10	20	110

NOTE 3: Load circuit and voltage waveforms are shown in Section 1.

<sup>‡</sup> All typical values are at VCC = 5 V, and TA = 25°C.