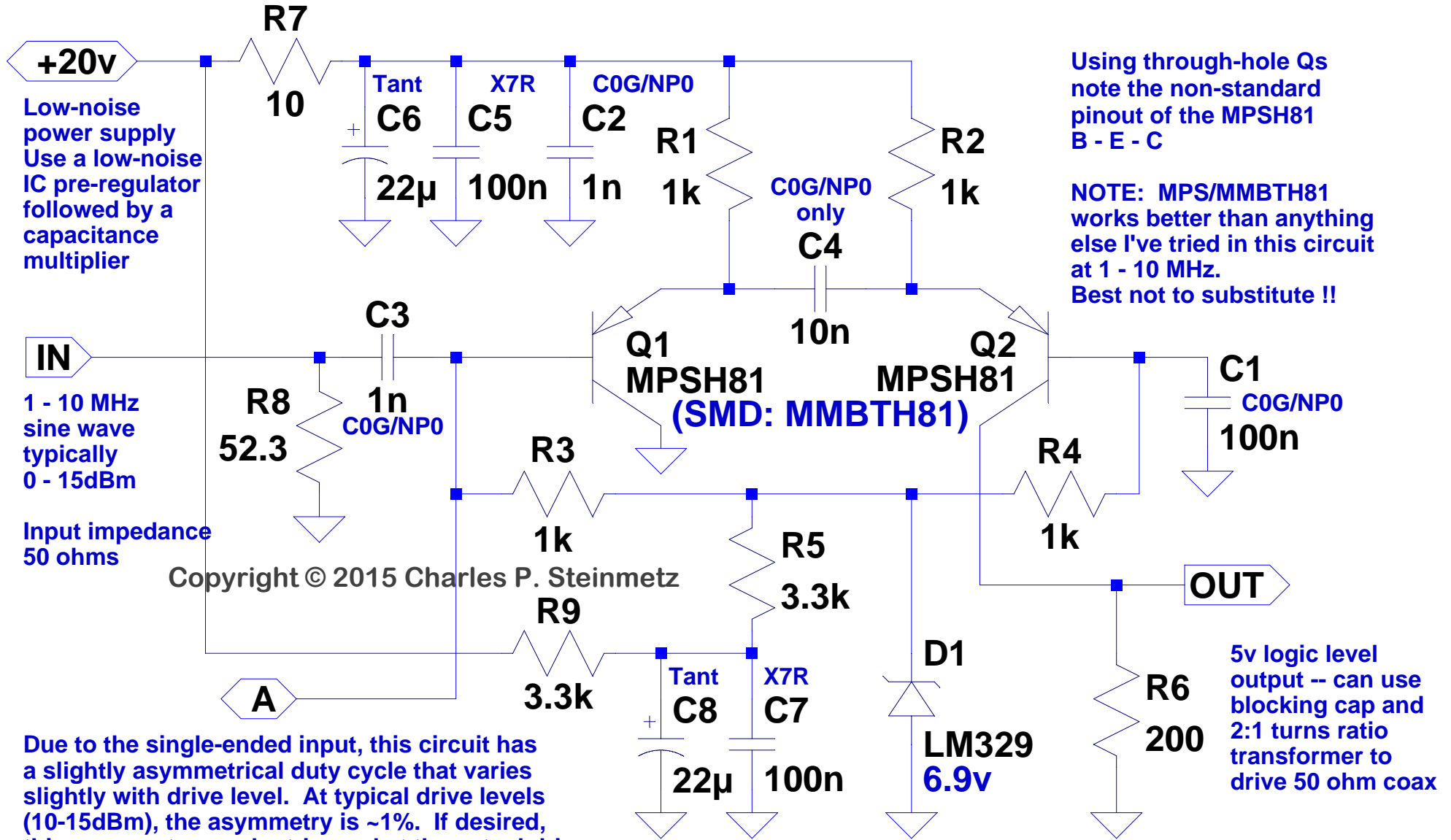


# Optimized Wenzel-type Squarer for 1 - 10 MHz Sine Waves



Due to the single-ended input, this circuit has a slightly asymmetrical duty cycle that varies slightly with drive level. At typical drive levels (10-15dBm), the asymmetry is ~1%. If desired, this asymmetry can be trimmed at the actual drive level by adding a resistor (~10k) to ground from Point A or by making R1 slightly less than R2.