MAX4599

Low-Voltage, Single-Supply, SPDT Analog Switch in SC70

General Description

The MAX4599 single-pole/double-throw (SPDT) switch operates from a +2.0V to +5.5V single supply. It offers 60Ω max on-resistance (R_{ON}) at +5V and fast switching times (t_{ON} = 30ns max, t_{OFF} = 25ns max).

The MAX4599 features excellent R_{ON} flatness (4 Ω max) and matching (1 Ω max) between channels. This device also offers 5pC max charge injection.

The MAX4599 is available in tiny 6-pin SC70, μ DFN, and SOT23 packages.

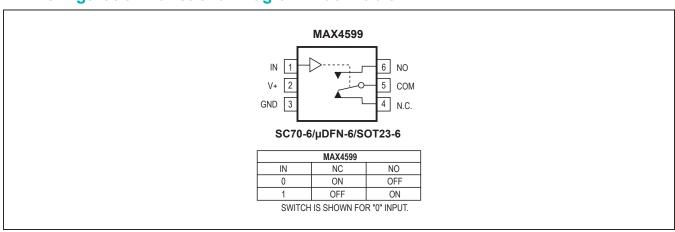
Applications

- Battery-Operated Equipment
- Audio and Video Signal Routing
- Cellular Phones
- Low-Voltage Data-Acquisition Systems
- Sample-and-Hold Circuits
- Communications Circuits

Features

- Available in 6-Pin SC70 Package
- 60Ω max (40Ω typ) On-Resistance
- 1Ω max (0.2Ω typ) R_{ON} Matching Between Channels
- 4Ω max (2.5Ω typ) R_{ON} Flatness
- Fast Switching: t_{ON} = 30ns (max)
 t_{OFF} = 25ns (max)
- Guaranteed 5pC max Charge Injection
- +2.0V to +5.5V Single-Supply Operation
- 200MHz -3dB Bandwidth
- Low ±0.5nA Leakage Current at +25°C
- Break-Before-Make Switching
- TTL/CMOS-Logic Compatible
- -76dB Off-Isolation at 1MHz
- 0.12% Total Harmonic Distortion

Pin Configuration/Functional Diagram/Truth Table





Absolute Maximum Ratings

Voltage Referenced to GND	
V+	0.3V to +6V
IN, COM, NO, NC (Note 1)	0.3V to (V+ + 0.3V)
Continuous Current (any terminal)	±20mA
Peak Current, COM, NO, N.C.	
(pulsed at 1ms, 10% duty cycle)	±40mA

Continuous Power Dissipation (T _A = +70°C)
6-Pin SC70-6 (derate 3.1mW/°C above +70°C)245mW
6-Pin µDFN-6 (derate 2.1mW/°C above +70°C)168mW
6-Pin SOT23-6 (derate 7.1mW/°C above +70°C)571mW
Operating Temperature Range
MAX4599E_T40°C to +85°C
Storage Temperature Range65°C to +150°C
Lead Temperature (soldering, 10s)+300°C

Note 1: Signals on NO, NC, COM, or IN exceeding V+ or GND are clamped by internal diodes. Limit forward-diode current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Electrical Characteristics—Single +5V Supply

(V+ = +4.5V to +5.5V, V_{INH} = +2.4V, V_{INL} = +0.8V, T_A = T_{MIN} to T_{MAX} , unless otherwise noted.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
ANALOG SWITCH							,
Analog Signal Range	V _{COM} , V _{NO} , V _{NC}			0		V+	V
On-Resistance	Da	V+ = 4.5V, I _{COM} = 1mA,	T _A = +25°C		40	60	Ω
OTH (COISTAILOG	R _{ON}	V_{NO} or $V_{NC} = 3.5V$	$T_A = T_{MIN}$ to T_{MAX}			65	32
On-Resistance Match	ΔRON	V+ = 4.5V, I _{COM} = 1A,	T _A = +25°C		0.2	1	
Between Channels (Note 4)	ΔKON	V_{NO} or $V_{NC} = 3.5V$	$T_A = T_{MIN}$ to T_{MAX}			2	Ω
On-Resistance Flatness		V+ = 4.5V; I _{COM} = 1mA;	T _A = +25°C		2.5	4	
(Note 5)	R _{FLAT(ON)}	V_{NO} or $V_{NC} = 1V$, 2.0V, 3.5V	$T_A = T_{MIN}$ to T_{MAX}			5	Ω
NO, NC Off-Leakage Current	I _{NO(OFF)} , I _{NC(OFF)}	V+ = 5.5V; V _{COM} = 1V, 4.5V; V _{NO} or V _{NC} = 4.5V, 1V	T _A = +25°C	-0.5	0.01	0.5	- nA
(Note 6)			$T_A = T_{MIN}$ to T_{MAX}	-5		5	
COM On-Leakage Current		V+ = 5.5V; V _{COM} = 1V,	T _A = +25°C	-1	0.01	1	
(Note 6)	I _{COM(ON)}	$ A(ON) $ 4.5V; V_{NO} or $V_{NC} = 1V$, 4.5V, or floating	$T_A = T_{MIN}$ to T_{MAX}	-10		10	- nA
DIGITAL I/O							
Input Logic High	V _{IH}			2.4			V
Input Logic Low	V _{IL}					0.8	V
DYNAMIC							
	1	V _{NO} , V _{NC} = 3V;	T _A = +25°C		25	30	
Turn-On Time	ton	$R_L = 1k\Omega$; $C_L = 35pF$; Figure 2 $T_A = T_{MIN}$ to	$T_A = T_{MIN}$ to T_{MAX}			40	ns

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Electrical Characteristics—Single +5V Supply (continued)

(V+ = +4.5V to +5.5V, V_{INH} = +2.4V, V_{INL} = +0.8V, T_A = T_{MIN} to T_{MAX} , unless otherwise noted.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS		
Turn-Off Time	t	$V_{NO}, V_{NC} = 3V;$ $R_1 = 1k\Omega; C_1 = 35pF;$	T _A = +25°C		20	25	ns		
Turn-Oil Time	t _{OFF}	Figure 2	$T_A = T_{MIN}$ to T_{MAX}			30	115		
Break-Before-Make	4	V _{NO} , V _{NC} = 3V;	T _A = +25°C		10		no		
Dieak-Deloie-Iviake	t _{BBM}	R_L = 1kΩ; C_L = 35pF; Figure 3	$T_A = T_{MIN}$ to T_{MAX}	1			ns		
On-Channel -3dB Bandwidth	BW	Signal = 0dBm, 50Ω in and out, Figure 5	T _A = +25°C		200		MHz		
Off-Isolation (Note 7)	V _{ISO}	C_L = 5pF; R_L = 50 Ω ; f = 1MHz; V_{NO} , V_{NC} = 1 V_{RMS} ; Figure 5	T _A = +25°C		-76		dB		
Charge Injection (Note 6)	Q	V _{GEN} = 0, R _{GEN} = 0, C _L = 1.0nF, Figure 4	T _A = +25°C		3	5	рС		
NO, NC Off-Capacitance	C _{NO(OFF)} , C _{NC(OFF)}	V _{NO} , V _{NC} = GND; f = 1MHz; Figure 6	T _A = +25°C		8		pF		
COM Off-Capacitance	C _{COM(OFF)}	V _{COM} = GND, f = 1MHz, Figure 6	T _A = +25°C		8		pF		
Switch On-Capacitance	C _(ON)	$V_{COM} = V_{NO},$ $V_{NC} = GND, f = 1MHz,$ Figure 6	T _A = +25°C		20		pF		
Total Harmonic Distortion	THD	$R_L = 600\Omega$, $V_{IN} = 5Vp-p$, $f = 20Hz$ to $20kHz$	T _A = +25°C		0.12		%		
SUPPLY	SUPPLY								
Power-Supply Range	V+			2.0		5.5	V		
Positive Supply Current	l+	V+ = 5.5V, V _{IN} = 0 or V+		-1	0.001	1	μA		

Electrical Characteristics—Single +3V Supply

(V+ = +2.7V to +3.6V, V_{INH} = +2.0V, V_{INL} = +0.8V, T_A = T_{MIN} to T_{MAX} , unless otherwise noted.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS		
ANALOG SWITCH	ANALOG SWITCH								
Analog Signal Range	V _{COM} , V _{NO} , V _{NC}			0		V+	V		
On-Resistance	В	V+ = 2.7V, I _{COM} = 1mA,	T _A = +25°C		60	95	Ω		
	R _{ON}	$V+ = 2.7V$, $I_{COM} = 1mA$, V_{NO} or $V_{NC} = 1V$	$T_A = T_{MIN}$ to T_{MAX}			105			
DIGITAL I/O									
Input Logic High	V _{IH}			2.0			V		
Input Logic Low	V _{IL}					0.8	V		

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Electrical Characteristics—Single +3V Supply (continued)

 $(V + = +2.7V \text{ to } +3.6V, V_{INH} = +2.0V, V_{INL} = +0.8V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted.})$ (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS	
DYNAMIC	PYNAMIC							
Turn-On Time	4	V _{NO} , V _{NC} = 2V;	T _A = +25°C		40	45	200	
Turn-On Time	ton	$R_L = 1k\Omega$; $C_L = 35pF$; Figure 2	$T_A = T_{MIN}$ to T_{MAX}			55	ns	
Turn-Off Time		V _{NO} , V _{NC} = 2V;	T _A = +25°C		30	35	ns	
Turr-On Time	tOFF	R_L = 1kΩ; C_L = 35pF; Figure 2	$T_A = T_{MIN}$ to T_{MAX}			40		
Break-Before-Make	t	V _{NO} , V _{NC} = 2V;	T _A = +25°C		13		no	
Dieak-Deloie-Wake	t _{BBM}	R_L = 1kΩ; C_L = 35pF; Figure 3	$T_A = T_{MIN}$ to T_{MAX}	1			ns	
Charge Injection (Note 6)	Q	V _{GEN} = 0, R _{GEN} = 0, C _L = 1.0nF, Figure 4	T _A = +25°C		2	5	рС	
SUPPLY								
Positive Supply Current	l+	V+ = 3.6V, V _{IN} = 0 or V+		-1	0.001	1	μA	

Electrical Characteristics—Single +2.5V Supply

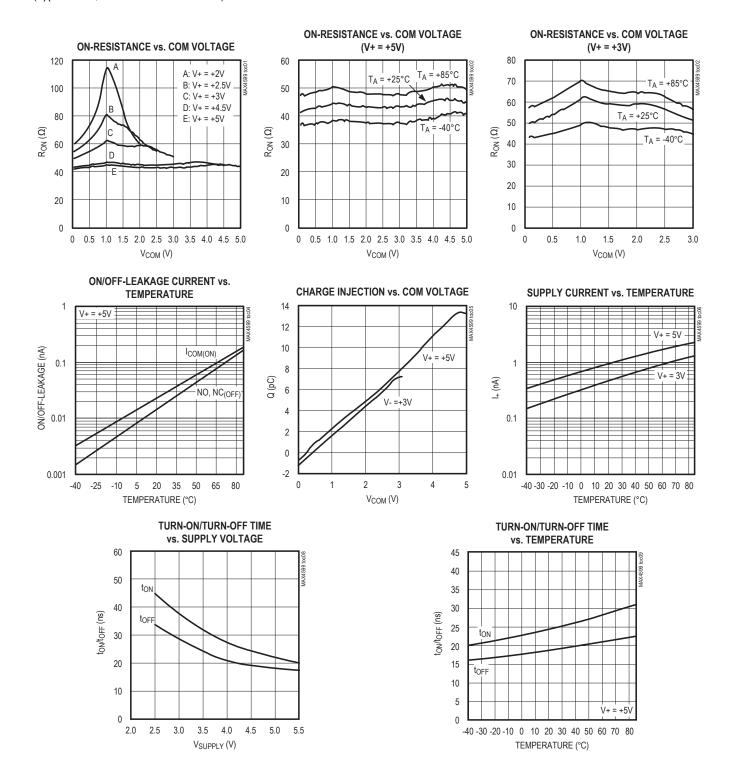
(V+ = +2.5V, V_{INH} = +2.0V, V_{INL} = +0.6V, T_A = T_{MIN} to T_{MAX} , unless otherwise noted.) (Notes 2, 3)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{COM} , V _{NO} , V _{NC}			0		V+	V
On-Resistance	Da	V+ = 2.5V, I _{COM} = 1mA,	T _A = +25°C		65	110	Ω
OII-Resistance	R _{ON}	V_{NO} or $V_{NC} = 1V$	$T_A = T_{MIN}$ to T_{MAX}			120	22
Input Logic High	V _{IH}			2.0			V
Input Logic Low	V _{IL}					0.6	V
DYNAMIC							
Turn-On Time	4	V _{NO} , V _{NC} = 2V;	T _A = +25°C		45	50	20
Turr-On Time	ton	R_L = 1kΩ; C_L = 35pF; Figure 3	$T_A = T_{MIN}$ to T_{MAX}			60	ns
Turn Off Time	4	V _{NO} , V _{NC} = 3V;	T _A = +25°C		30	35	20
Turn-Off Time	toff	R_L = 1kΩ; C_L = 35pF; Figure 3	$T_A = T_{MIN}$ to T_{MAX}			45	ns

- Note 2: Parameters are 100% tested at +25°C only and guaranteed by correlation at the full rated temperature.
- Note 3: The algebraic convention, where the most negative value is a minimum and the most positive value a maximum, is used in this data sheet.
- Note 4: $\Delta R_{ON} = R_{ON(MAX)} R_{ON(MIN)}$
- **Note 5:** Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured over the specified analog signal ranges.
- Note 6: Guaranteed by design.
- Note 7: Off-Isolation = $20log_{10}$ (V_{COM} / V_{NO}), V_{COM} = output, V_{NO} = input to off switch.

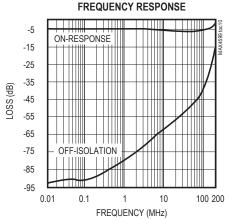
Typical Operating Characteristics

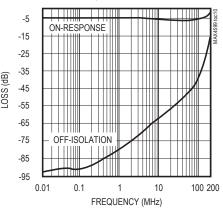
 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$



Typical Operating Characteristics (continued)

 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$





Pin Description

PIN	NAME	FUNCTION
1	IN	Digital Control Input
2	V+	Positive Supply Voltage
3	GND	Ground
4	N.C.	Analog Switch Normally Closed
5	COM	Analog Switch Common
6	NO	Analog Switch Normally Open

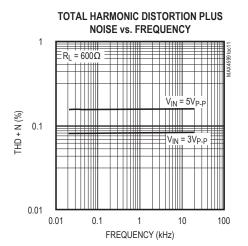
Applications Information

Analog Signal Levels

Analog signals can range over the supply voltage (V+ to GND) with on-resistance changing very little over the entire range (see Typical Operating Characteristics). The MAX4599 is bidirectional, so the NO, NC, and COM pins can be used either as inputs or outputs.

Power-Supply Sequencing and Overvoltage Protection

Proper power-supply sequencing is recommended for all CMOS devices. Always apply V+ before applying analog signals or logic inputs, especially if the analog or logic signals are not current limited. If this sequencing is not possible, and if the analog or logic inputs are not current limited to < 20mA, add a small-signal diode (D1) as shown in Figure 1. If the analog signal can dip below GND, add D2.



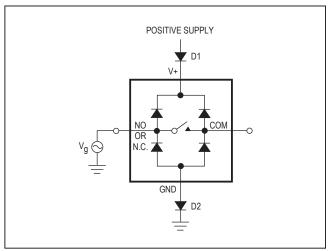


Figure 1. Overvoltage Protection Using Two External Blocking Diodes

Adding protection diodes reduces the analog signal range to a diode drop (about 0.7V) below V+ for D1 or to a diode drop above ground for D2. The addition of diodes does not affect leakage. On-resistance increases by a small amount at low supply voltages. Maximum supply voltage (V+) must not exceed 6V.

Protection diodes D1 and D2 also protect against some overvoltage situations. A fault voltage up to the absolute maximum rating at an analog signal input does not damage the device, even if the supply voltage is below the signal voltage.

Test Circuits/Timing Diagrams

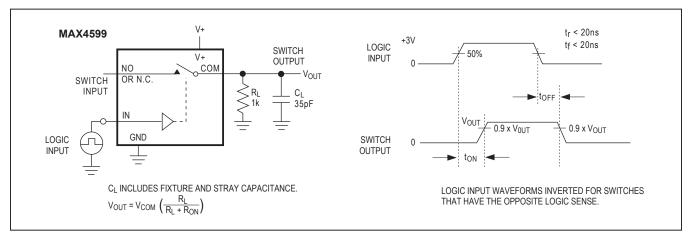


Figure 2. Switching Time

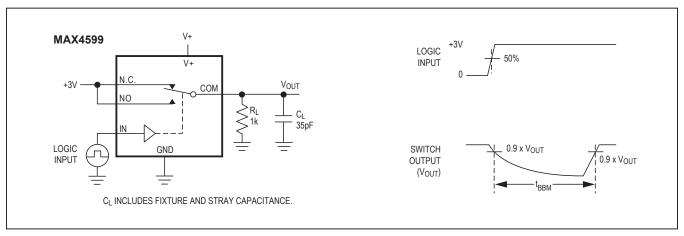


Figure 3. Break-Before-Make Interval

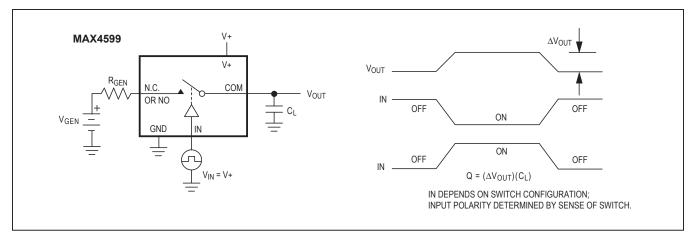


Figure 4. Charge Injection

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Test Circuits/Timing Diagrams (continued)

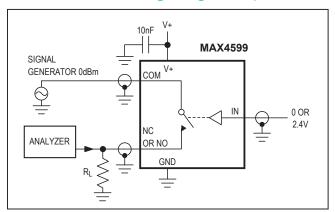


Figure 5. Off-Isolation/On-Channel Bandwidth

Ordering Information

PART	TEMP RANGE	PIN- PACKAGE	TOP MARK
MAX4599EXT+T	-40°C to +85°C	6 SC70-6	AAF
MAX4599EUT+T	-40°C to +85°C	6 SOT23-6	AAHC
MAX4599ELT+T	-40°C to +85°C	6 μDFN-6	AA

Chip Information

TRANSISTOR COUNT: 89

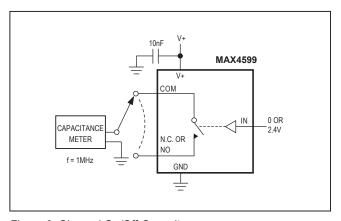


Figure 6. Channel On/Off-Capacitance

Package Information

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
6 SC70	X6SN-1	21-0077	90-0189
6 uDFN	L611-1	21-0147	90-0080
6 SOT23	U6SN+1	21-0058	90-0175

MAX4599

Low-Voltage, Single-Supply, SPDT Analog Switch in SC70

Revision History

REVISION NUMBER	DESCRIPTION		PAGES CHANGED
3	2/18	Updated Ordering Information table and created Packaging Information table	
4	2/21	Updated Package Information table for 6-SOT23.	8

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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