74LVC1G16

Single buffer

Rev. 4 — 17 January 2022

Product data sheet

1. General description

The 74LVC1G16 provides a low-power, low-voltage single buffer.

The input can be driven from either 3.3 V or 5 V devices. This feature allows the use of this device in a mixed 3.3 V and 5 V environment.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

Schmitt trigger action at all inputs makes the circuit highly tolerant of slower input rise and fall times.

2. Features and benefits

- Wide supply voltage range from 1.65 V to 5.5 V
- 5 V tolerant inputs for interfacing with 5 V logic
- High noise immunity
- ±24 mA output drive (V_{CC} = 3.0 V)
- CMOS low power consumption
- · Latch-up performance exceeds 250 mA
- Direct interface with TTL levels
- Inputs accept voltages up to 5 V
- Complies with JEDEC standard:
 - JESD8-7 (1.65 V to 1.95 V)
 - JESD8-5 (2.3 V to 2.7 V)
 - JESD8-B/JESD36 (2.7 V to 3.6 V).
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package	Package				
	Temperature range	Name	Description	Version		
74LVC1G16GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1		
74LVC1G16GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886		



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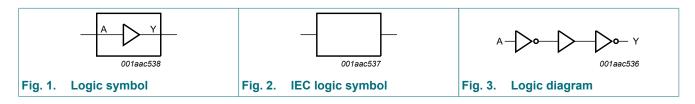
4. Marking

Table 2. Marking

Type number	Marking code[1]
74LVC1G16GW	Yr
74LVC1G16GM	Yr

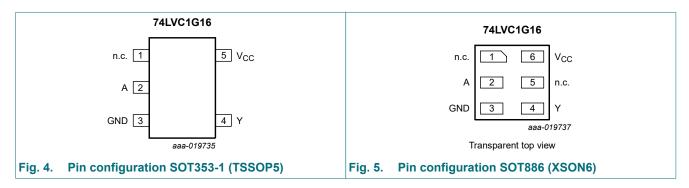
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Symbol	Pin		Description
	TSSOP5	XSON6	
n.c.	1	1	not connected
A	2	2	data input
GND	3	3	ground (0 V)
Υ	4	4	data output
n.c.	-	5	not connected
V _{CC}	5	6	supply voltage

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7. Functional description

Table 4. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$

Input	Output
A	Υ
L	L
Н	Н

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CC}	supply voltage			-0.5	+6.5	V
I _{IK}	input clamping current	V _I < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+6.5	V
I _{OK}	output clamping current	$V_O > V_{CC}$ or $V_O < 0$ V		-	±50	mA
Vo	output voltage	Active mode	[1]	-0.5	V _{CC} + 0.5	V
		Power-down mode; V _{CC} = 0 V	[1]	-0.5	+6.5	V
Io	output current	V _O = 0 V to V _{CC}		-	±50	mA
I _{CC}	supply current			-	100	mA
I _{GND}	ground current			-100	-	mA
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	[2]	-	250	mW
T _{stg}	storage temperature			-65	+150	°C

^[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		1.65	-	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	Active mode	0	-	V _{CC}	Vo
		Power-down mode; V _{CC} = 0 V	0	-	5.5	Vo
T _{amb}	ambient temperature		-40	-	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 1.65 V to 2.7 V	-	-	20	ns/V
		V _{CC} = 2.7 V to 5.5 V	-	-	10	ns/V

^[2] For SOT353-1 (TSSOP5) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C. For SOT886 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

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10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions. Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ[1]	Max	Unit
T _{amb} = -	40 °C to +85 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 1.65 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.7	-	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	V
		V _{CC} = 4.5 V to 5.5 V	0.7 × V _{CC}	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 1.65 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
	V _{CC} = 2.7 V to 3.6 V	-	-	0.8	V	
		V _{CC} = 4.5 V to 5.5 V	-	-	0.3 × V _{CC}	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}				
		I_{O} = -100 μ A; V_{CC} = 1.65 V to 5.5 V	V _{CC} - 0.1	-	-	V
		I _O = -4 mA; V _{CC} = 1.65 V	1.2	1.54	-	V
		I_{O} = -8 mA; V_{CC} = 2.3 V	1.9	2.15	-	V
		I_{O} = -12 mA; V_{CC} = 2.7 V	2.2	2.50	-	V
		I_{O} = -24 mA; V_{CC} = 3.0 V	2.3	2.62	-	V
		I_{O} = -32 mA; V_{CC} = 4.5 V	3.8	4.11	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = 100 μA; V _{CC} = 1.65 V to 5.5 V	-	-	0.10	V
		I _O = 4 mA; V _{CC} = 1.65 V	-	0.07	0.45	V
		I _O = 8 mA; V _{CC} = 2.3 V	-	0.12	0.30	V
		I _O = 12 mA; V _{CC} = 2.7 V	-	0.17	0.40	V
		$I_O = 24 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	0.33	0.55	V
		I_{O} = 32 mA; V_{CC} = 4.5 V	-	0.39	0.55	V
l _l	input leakage current	$V_{CC} = 0 \text{ V to } 5.5 \text{ V}; V_{I} = 5.5 \text{ V or GND}$ [2]	-	±0.1	±5	μΑ
I_{OFF}	power-off leakage current	$V_{CC} = 0 \text{ V}; V_{I} \text{ or } V_{O} = 5.5 \text{ V}$	-	±0.1	±10	μΑ
I _{CC}	supply current	V _{CC} = 1.65 V to 5.5 V; I _O = 0 A; V _I = 5.5 V or GND	-	0.1	10	μA
Δl _{CC}	additional supply current	$V_{CC} = 2.3 \text{ V to } 5.5 \text{ V}; V_I = V_{CC} - 0.6 \text{ V}; [2]$ $I_O = 0 \text{ A}$	-	5	500	μA
Cı	input capacitance	V_{CC} = 3.3 V; V_I = GND to V_{CC}	-	4	-	pF

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Symbol	Parameter	Conditions	Min	Typ[1]	Max	Unit
T _{amb} = -	40 °C to +125 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 1.65 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.7	-	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	V
		V _{CC} = 4.5 V to 5.5 V	0.7 × V _{CC}	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 1.65 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 2.7 V to 3.6 V	-	-	0.8	V
		V _{CC} = 4.5 V to 5.5 V	-	-	0.3 × V _{CC}	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}				
		I_{O} = -100 μ A; V_{CC} = 1.65 V to 5.5 V	V _{CC} - 0.1	-	-	V
		I _O = -4 mA; V _{CC} = 1.65 V	0.95	-	-	V
		I_{O} = -8 mA; V_{CC} = 2.3 V	1.7	-	-	V
		I_{O} = -12 mA; V_{CC} = 2.7 V	1.9	-	- V	
		I_{O} = -24 mA; V_{CC} = 3.0 V	2.0	-	-	V
		I_{O} = -32 mA; V_{CC} = 4.5 V	3.4	-	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}				
		I_{O} = 100 μ A; V_{CC} = 1.65 V to 5.5 V	-	-	0.10	V
		I _O = 4 mA; V _{CC} = 1.65 V	-	-	0.70	V
		I _O = 8 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 12 mA; V _{CC} = 2.7 V	-	-	0.60	V
		I _O = 24 mA; V _{CC} = 3.0 V	-	-	0.80	V
		I _O = 32 mA; V _{CC} = 4.5 V	-	-	0.80	V
I _I	input leakage current	V _{CC} = 0 V to 5.5 V; V _I = 5.5 V or GND	-	-	±100	μΑ
I _{OFF}	power-off leakage current	$V_{CC} = 0 \text{ V}; V_{I} \text{ or } V_{O} = 5.5 \text{ V}$	-	-	±200	μΑ
I _{CC}	supply current	V _{CC} = 1.65 V to 5.5 V; I _O = 0 A; V _I = 5.5 V or GND	-	-	200	μΑ
ΔI _{CC}	additional supply current	V_{CC} = 2.3 V to 5.5 V; V_{I} = V_{CC} - 0.6 V; I_{O} = 0 A	-	-	5000	μΑ

^[1] All typical values are measured at T_{amb} = 25 °C. [2] These typical values are measured at V_{CC} = 3.3 V.

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11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V). For test circuit see Fig. 7.

Symbol	Parameter	Conditions	-40	°C to +85	°C	-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	
t _{pd}	propagation delay	A to Y; see <u>Fig. 6</u> [2]						
		V _{CC} = 1.65 V to 1.95 V	1.0	4.0	8.6	1.0	11.0	ns
		V _{CC} = 2.3 V to 2.7 V	0.5	2.6	4.4	0.5	5.6	ns
		V _{CC} = 2.7 V	0.5	2.3	4.5	0.5	5.6	ns
		V _{CC} = 3.0 V to 3.6 V	0.5	2.0	4.1	0.5	5.2	ns
		V _{CC} = 4.5 V to 5.5 V	0.5	1.6	3.2	0.5	4.1	ns
C _{PD}	power dissipation capacitance	$V_{I} = GND \text{ to } V_{CC}; V_{CC} = 3.3 \text{ V}$ [3]	-	15	-	-	-	pF

- [1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 1.8 V, 2.5 V, 2.7 V, 3.3 V, and 5.0 V respectively.
- t_{pd} is the same as t_{PLH} and t_{PHL} . C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

 f_o = output frequency in MHz;

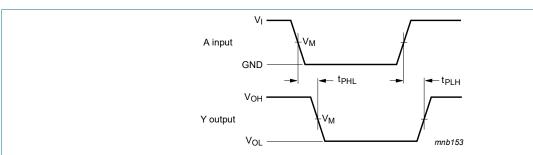
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\sum (C_L \times V_{CC}^2 \times f_0) = \text{sum of outputs.}$

11.1. Waveforms and test circuit



Measurement points are given in Table 9.

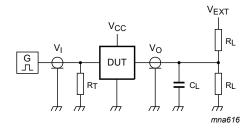
 V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

The data input (A) to output (Y) propagation delays Fig. 6.

Table 9. Measurement points

Supply voltage	Input	Output
V _{CC}	V _M	V _M
1.65 V to 1.95 V	0.5 × V _{CC}	0.5 × V _{CC}
2.3 V to 2.7 V	0.5 × V _{CC}	0.5 × V _{CC}
2.7 V	1.5 V	1.5 V
3.0 V to 3.6 V	1.5 V	1.5 V
4.5 V to 5.5 V	0.5 × V _{CC}	0.5 × V _{CC}

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Test data is given in Table 10.

Definitions for test circuit:

R_L = Load resistance;

 C_L = Load capacitance including jig and probe capacitance;

R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator;

 V_{EXT} = External voltage for measuring switching times.

Fig. 7. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Input	Input			V _{EXT}
V _{CC}	VI	t _r = t _f	CL	R _L	t _{PLH} , t _{PHL}
1.65 V to 1.95 V	V _{CC}	≤ 2.0 ns	30 pF	1 kΩ	open
2.3 V to 2.7 V	V _{CC}	≤ 2.0 ns	30 pF	500 Ω	open
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open
4.5 V to 5.5 V	V _{CC}	≤ 2.5 ns	50 pF	500 Ω	open

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12. Package outline

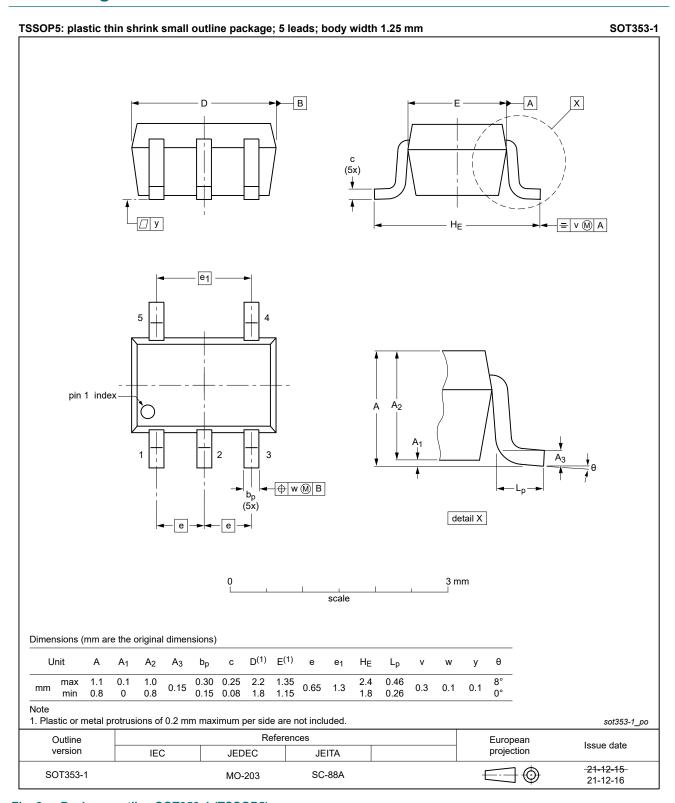


Fig. 8. Package outline SOT353-1 (TSSOP5)

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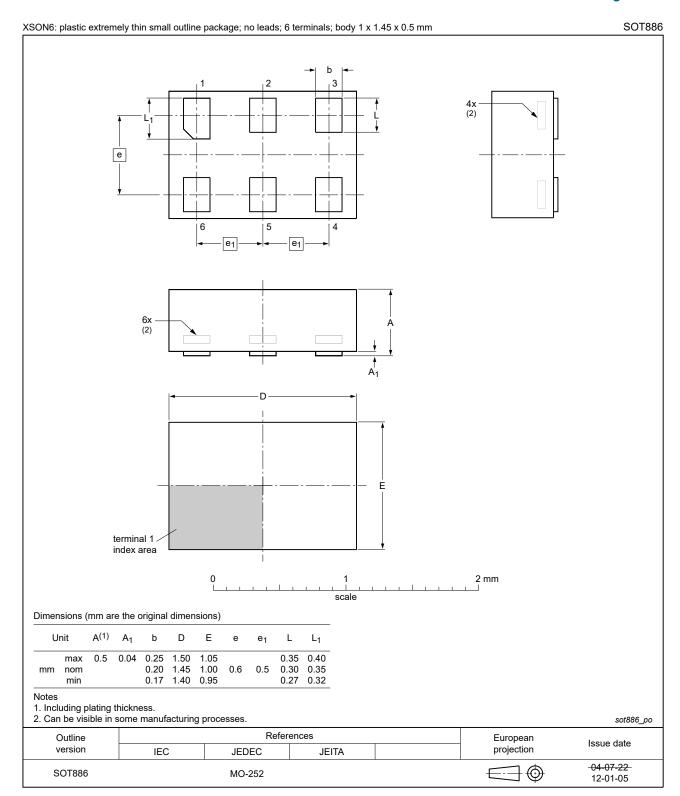


Fig. 9. Package outline SOT886 (XSON6)

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13. Abbreviations

Table 11. Abbreviations

Acronym	Description
CMOS	Complementary Metal Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74LVC1G16 v.4	20220117	Product data sheet	-	74LVC1G16 v.2	
Modifications:	Fig. 8: Package outline drawing for SOT353-1 (TSSOP5) has changed.				
74LVC1G16 v.3	20210804	Product data sheet	-	74LVC1G16 v.2	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type number 74LVC1G16GF (SOT891/XSON6) removed. Section 8: Derating values for P_{tot} total power dissipation updated. 				
74LVC1G16 v.2	20161012	Product data sheet	-	74LVC1G16 v.1	
Modifications:	 Type numbers 74LVC1G16GV, 74LVC1G16GN, 74LVC1G16GS and 74LVC1G16GX removed. 				
74LVC1G16 v.1	20151102	Product data sheet	-	-	

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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