

**Product data sheet** 

## 1. General description

PNP Darlington transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complement: BCV47

## 2. Features and benefits

- High current
- High current gain
- AEC-Q101 qualified

## 3. Applications

• For general AF applications and where high amplification is required

# 4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
I <sub>C</sub>	collector current			-	-	-500	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; I <sub>C</sub> = -1 mA; T <sub>amb</sub> = 25 °C	[1]	2000	-	-	

[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

# 5. Pinning information

Table 2. F	Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	В	base	3	ВС				
2	E	emitter						
3	С	collector						
			SOT23	aaa-034789				



# 6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
<u>BCV46</u>		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<u>SOT23</u>		

## 7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
BCV46	FE%

[1] % = placeholder for manufacturing site code

# 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-80	V
V <sub>CES</sub>	collector-emitter voltage	base short-circuited to emitter		-	-60	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-10	V
I <sub>C</sub>	collector current			-	-500	mA
I <sub>CM</sub>	peak collector current			-	-800	mA
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

# 9. Thermal characteristics

#### Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

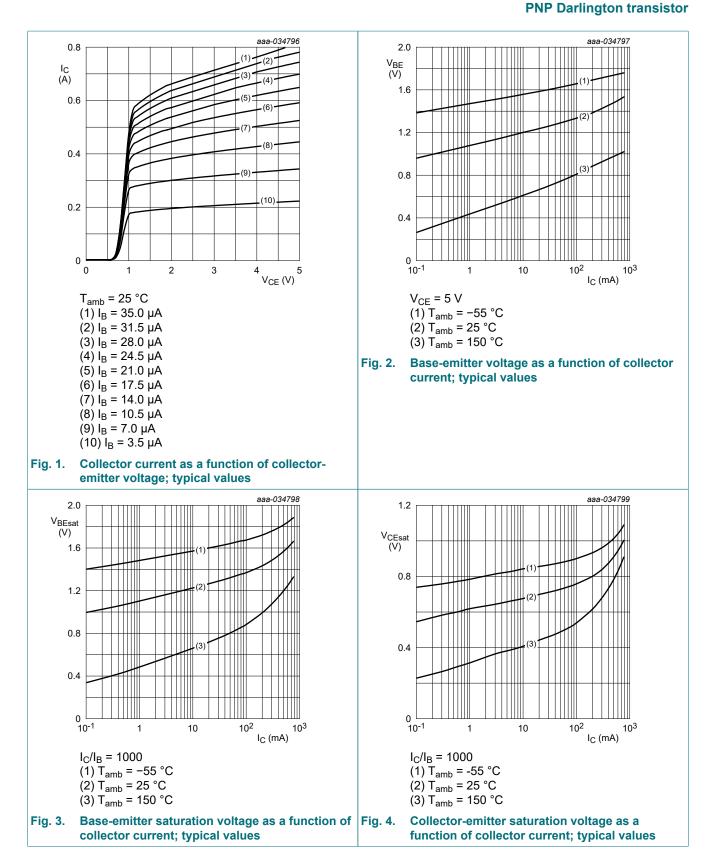
# **10. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = -100 μA; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-80	-	-	V
V <sub>(BR)CES</sub>	collector-emitter breakdown voltage	$I_{C}$ = -2 mA; $V_{BE}$ = 0 V; $T_{amb}$ = 25 °C		-60	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>C</sub> = 0 A; I <sub>E</sub> = -100 μA; T <sub>amb</sub> = 25 °C		-10	-	-	V
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = -60 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-100	nA
I <sub>CES</sub>	collector-emitter cut-off current	$V_{CE}$ = -60 V; $V_{BE}$ = 0 V; $T_{amb}$ = 25 °C		-	-	-100	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -10 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; I <sub>C</sub> = -1 mA; T <sub>amb</sub> = 25 °C	[1]	2000	-	-	
		$V_{CE}$ = -5 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C	[1]	4000	-	-	
		$V_{CE}$ = -5 V; I <sub>C</sub> = -100 mA; T <sub>amb</sub> = 25 °C	[1]	10000	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -100 mA; I <sub>B</sub> = -0.1 mA; T <sub>amb</sub> = 25 °C		-	-	-1	V
V <sub>BEsat</sub>	base-emitter saturation voltage	-		-	-	-1.5	V
V <sub>BEon</sub>	base-emitter turn-on voltage	$I_{C}$ = -10 mA; $V_{CE}$ = -5 V; $T_{amb}$ = 25 °C		-	-	-1.4	V
t <sub>d</sub>	delay time	I <sub>C</sub> = 100 mA; I <sub>Bon</sub> = 0.1 mA;		-	225	-	ns
t <sub>r</sub>	rise time	$I_{Boff}$ = -0.1 mA; $V_{CC}$ = 5 V; $T_{amb}$ = 25 °C		-	200	-	ns
t <sub>on</sub>	turn-on time			-	425	-	ns
t <sub>s</sub>	storage time			-	520	-	ns
t <sub>f</sub>	fall time			-	810	-	ns
t <sub>off</sub>	turn-off time			-	1330	-	ns

[1] Pulse test:  $t_p \le 300 \ \mu s; \delta \le 0.02$ 

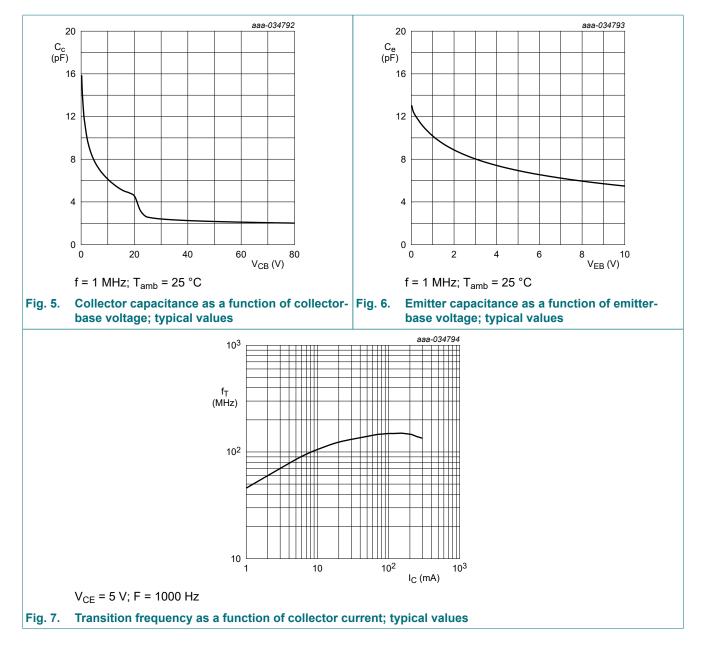
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BCV46



# PNP Darlington transistor

BCV46

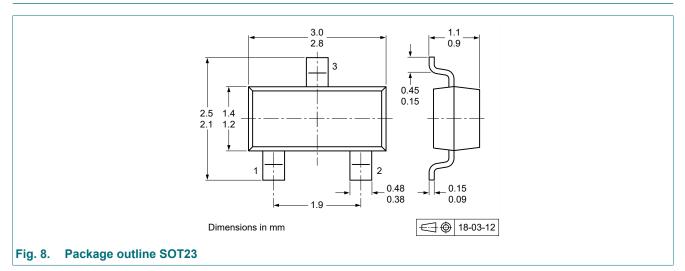


## 11. Test information

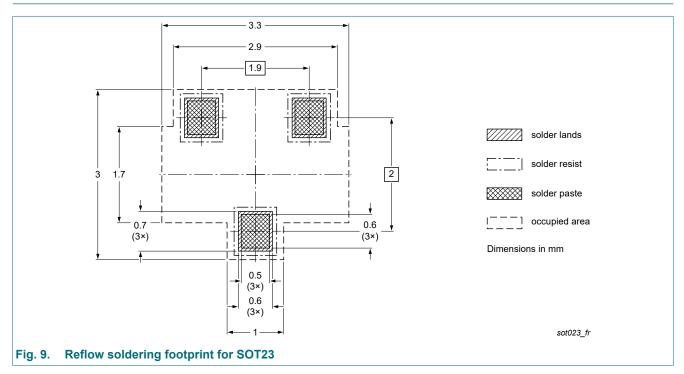
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

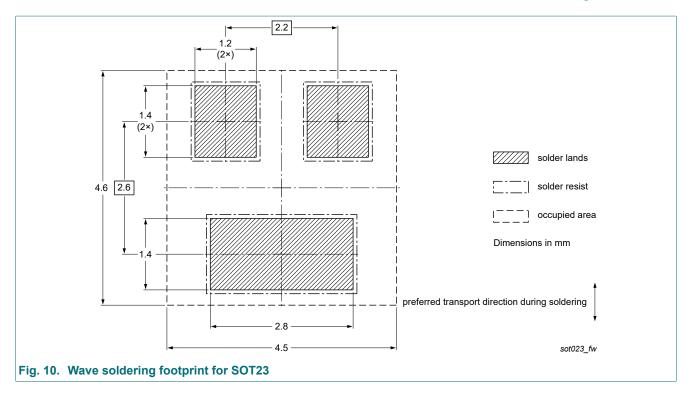
# 12. Package outline



# 13. Soldering



### **PNP Darlington transistor**



# 14. Revision history

Table 8. Revision his							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BCV46 v.3	20220512	Product data sheet	-	BCV26_BCV46 v.2			
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Family data sheet splitted to single type data sheets.</li> <li>Characteristics: Figures added</li> </ul>						
BCV26_BCV46 v.2	20040113	Product data sheet	-	BCV26_BCV46 v.1			
BCV26 BCV46 v.1	19990408	Product data sheet	_	_			

#### **PNP Darlington transistor**

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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