

100 V, 2 A low leakage current Schottky barrier rectifier4 May 2021Product data sheet

### 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Average forward current:  $I_{F(AV)} \le 2 A$
- Reverse voltage:  $V_R \le 100 \text{ V}$
- Low forward voltage: V<sub>F</sub> = 770 mV
- High power capability due to clip-bonding technology
- Extremely low leakage current I<sub>R</sub> = 40 nA
- High temperature T<sub>i</sub> ≤ 175 °C
- · Qualified according to AEC-Q101 and recommended for use in automotive applications
- Capable for reflow and wave soldering

### 3. Applications

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- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

### 4. Quick reference data

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> ≤ 160 °C	-	-	2	A
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C	-	-	100	V
V <sub>F</sub>	forward voltage	$ \begin{array}{l} I_F = 2 \; A;  t_p \leq \; 300 \; \mu s;  \delta \leq \; 0.02; \\ T_j = 25 \; ^\circ C \end{array} $	-	770	830	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 100 V; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 25 °C	-	40	150	nA

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### 5. Pinning information

Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	К	cathode[1]		K- <b>F</b> A			
2	А	anode		sym001			
			CFP3 (SOD123W)				

[1] The marking bar indicates the cathode.

### 6. Ordering information

#### Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG10020ELR-Q	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W

### 7. Marking

Table 4. Marking codes						
Type number	Marking code					
PMEG10020ELR-Q	К8					

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	100	V
l <sub>F</sub>	forward current	δ = 1; T <sub>sp</sub> = 155 °C		-	2.8	А
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; square wave; T <sub>amb</sub> ≤ 80 °C	[1]	-	2	A
		δ = 0.5; f = 20 kHz; square wave; T <sub>sp</sub> ≤ 160 °C		-	2	A
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; square wave; $T_{j(init)}$ = 25 °C		-	50	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[2]	-	680	mW
			[3]	-	1150	mW
			[1]	-	2140	mW
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 9. Thermal characteristics

#### Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance from	[1]	[1] [2]	-	-	220	K/W
	junction to ambient		[1] [3]	-	-	130	K/W
			[1] [4]	-	-	70	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[5]	-	-	18	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

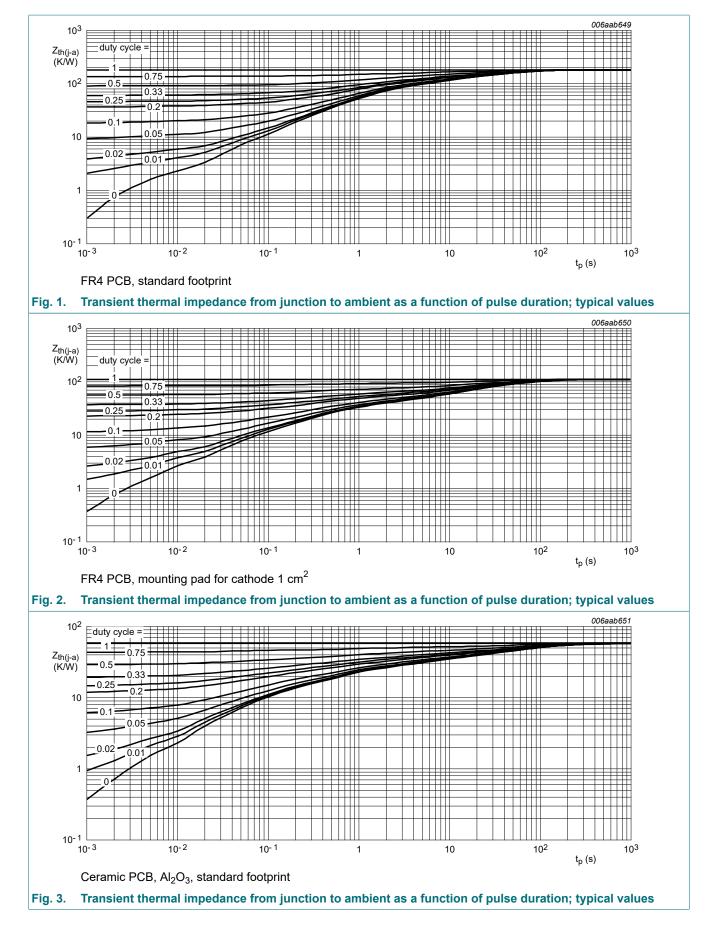
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

[4] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

[5] Soldering point of cathode tab.

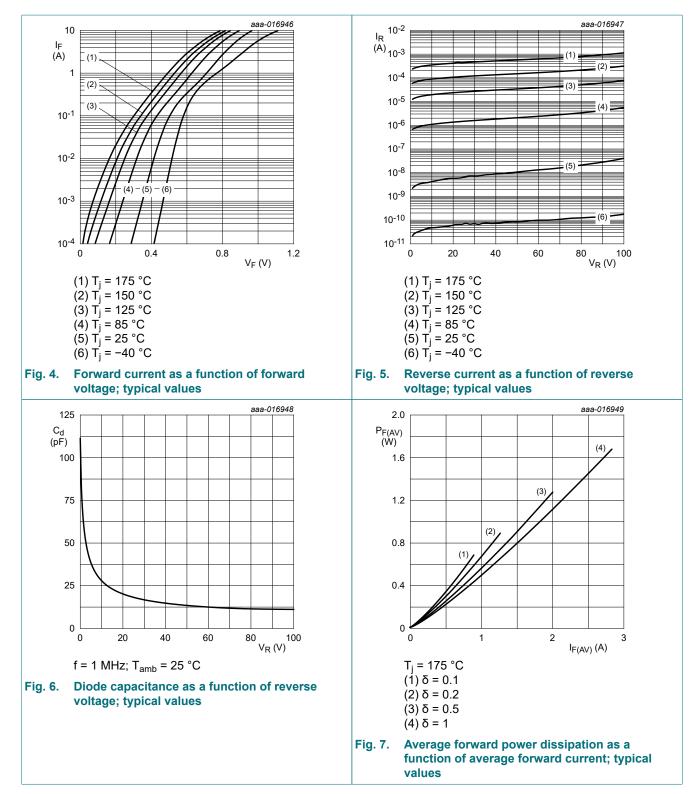
#### 100 V, 2 A low leakage current Schottky barrier rectifier



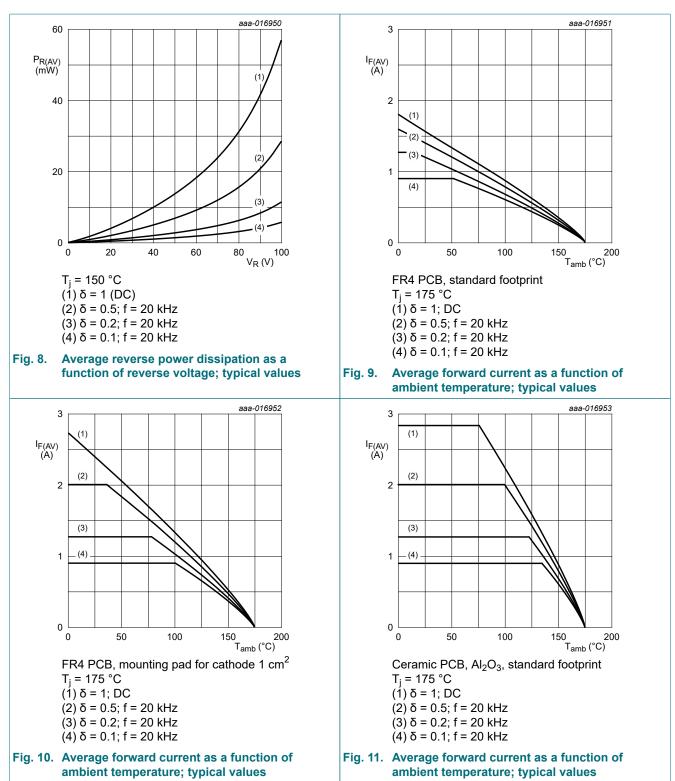
### **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)R</sub>	reverse breakdown voltage	$I_R$ = 1 mA; t <sub>p</sub> = 300 μs; δ = 0.02; T <sub>j</sub> = 25 °C	100	-	-	V
V <sub>F</sub>	forward voltage	$I_F = 0.1 \text{ A}; t_p \le 300 \text{ μs}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$	-	505	565	mV
		$I_F = 0.5 \text{ A}; t_p \le 300 \text{ μs}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$	-	640	710	mV
		$I_F = 0.7 \text{ A}; t_p \le 300 \text{ μs}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$	-	675	740	mV
		$I_F = 1 \text{ A}; t_p \le 300  \mu\text{s}; \delta \le 0.02; \\ T_j = 25 \text{ °C}$	-	710	770	mV
		$I_F$ = 1.6 A; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 25 °C	-	750	810	mV
		$I_F = 2 \text{ A}; t_p \le 300  \mu\text{s}; \delta \le 0.02; \\ T_j = 25 \text{ °C}$	-	770	830	mV
		$I_F = 2 \text{ A}; t_p \le 300  \mu\text{s}; \delta \le 0.02; \\ T_j = 125 ^\circ\text{C}$	-	635	740	mV
I <sub>R</sub>	reverse current	$V_R$ = 10 V; $t_p \le 300 \ \mu s; \delta \le 0.02;$ T <sub>j</sub> = 25 °C	-	4	-	nA
		$V_R = 60 \text{ V}; t_p \le 300  \mu\text{s}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$	-	12	-	nA
		$V_R$ = 100 V; $t_p \le 300 \ \mu s; \delta \le 0.02;$ $T_j$ = 25 °C	-	40	150	nA
		$V_R$ = 100 V; $t_p \le 300 \ \mu s; \delta \le 0.02;$ T <sub>j</sub> = 125 °C	-	70	500	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	70	-	pF
		V <sub>R</sub> = 4 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	42	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	28	-	pF
rr	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 1 \text{ A}; I_{R(meas)} = 0.25 \text{ A};$ $T_j = 25 \text{ °C}$	-	3.7	-	ns
/ <sub>FRM</sub>	peak forward recovery voltage	$I_F = 0.5 \text{ A}; \text{ d}I_F/\text{d}t = 20 \text{ A}/\mu\text{s}; T_j = 25 ^\circ\text{C}$	-	690	-	mV

#### 100 V, 2 A low leakage current Schottky barrier rectifier

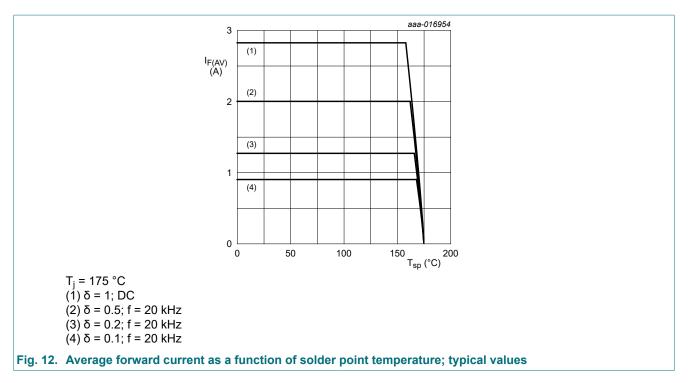




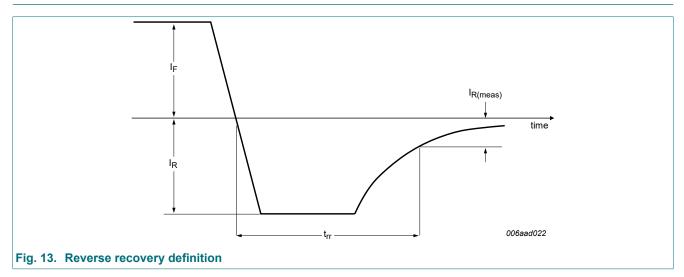


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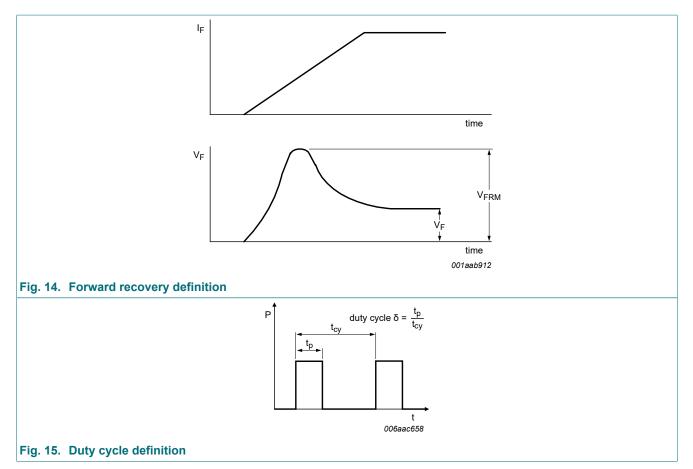
#### 100 V, 2 A low leakage current Schottky barrier rectifier



### 11. Test information



#### 100 V, 2 A low leakage current Schottky barrier rectifier

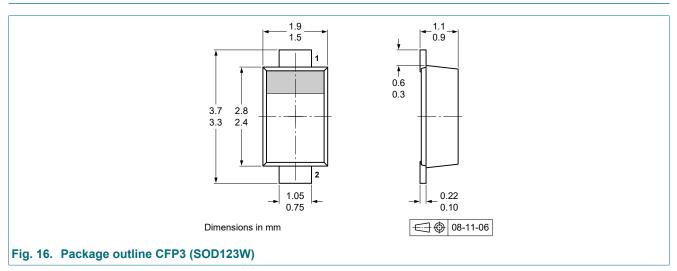


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

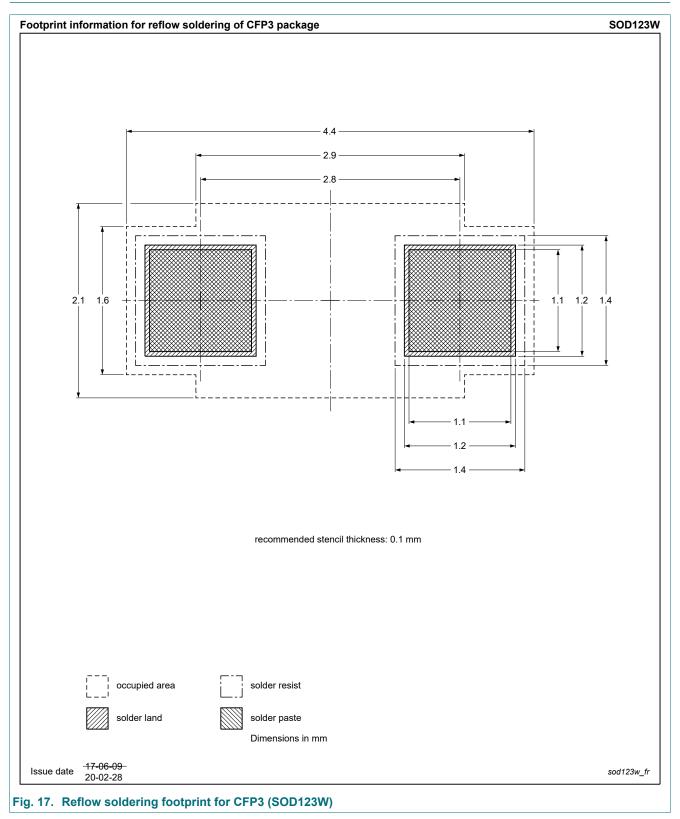
#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

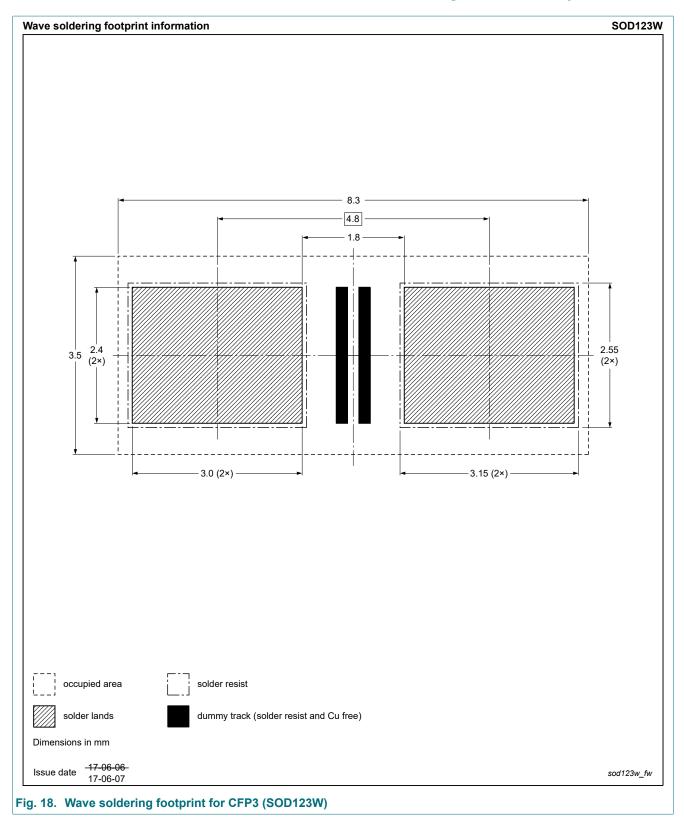
### 12. Package outline



### 13. Soldering



#### 100 V, 2 A low leakage current Schottky barrier rectifier



### 14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG10020ELR-Q v.1	20210504	Product data sheet	-	-			

PMEG10020ELR-Q

### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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