1. General description

Silicon Germanium (SiGe) rectifier encapsulated in a CFP5 (SOD128) small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

Features	Benefits				
 Low forward voltage and low Q_{rr} Extremely low leakage current Thermal stability up to 175 °C junction temperature Fast and smooth switching Low parasitic capacitance Qualified according to AEC-Q101 and recommended for use in automotive applications 	 Excellent efficiency Extraordinary safe operating area Minimal impact on Electro-Magnetic Compatibility (EMC) allowing simplified certification 				

3. Applications

- High-efficiency power conversion
 - Automotive LED lighting
 - · Engine control unit
 - Server power supply
 - Base station power supply
- Reverse polarity protection
- OR-ing

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$I_{F(AV)}$	average forward current	δ = 0.5; f = 20 kHz; square wave; $T_{sp} \le$ 160 °C		-	-	2	А
V _R	reverse voltage	T _j = 25 °C		-	-	150	V
V _F	forward voltage	I _F = 2 A; T _j = 25 °C; pulsed	[1]	-	780	850	mV
I _R	reverse current	V _R = 150 V; T _j = 25 °C; pulsed	[1]	-	0.4	30	nA
		$V_R = 150 \text{ V}; T_j = 150 ^{\circ}\text{C}; \text{ pulsed}$	[1]	-	30	300	μA

[1] Very short pulse, in order to maintain a stable junction temperature.



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode		
2	Α	anode	1 2	K A
			CFP5 (SOD128)	006aab040

6. Ordering information

Table 3. Ordering information

Type number	Package	ackage						
	Name	Description	Version					
PMEG150G20ELP-Q		plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	SOD128					

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG150G20ELP-Q	EA

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Attention: Stress above one of these maximum values may cause irreversible damage to the device.

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	150	V
I _F	forward current	δ = 1; $T_{sp} \le 155 ^{\circ}\text{C}$		-	2.8	A
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 160 °C		-	2	A
I _{FSM}	non-repetitive peak forward current	$t_p = 8.3 \text{ ms}$; half sine wave; $T_{j(init)} = 25 \text{ °C}$		-	75	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.75	W
			[2]	-	1.2	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

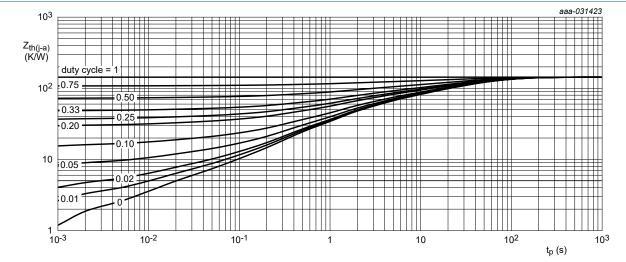
- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

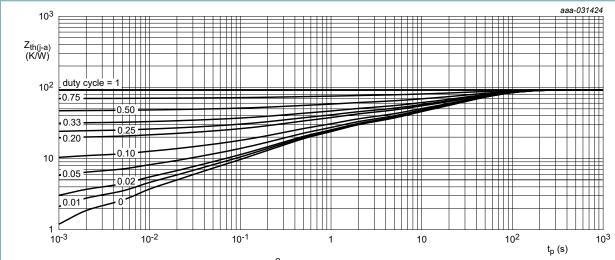
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1]	-	-	200	K/W
junction to ambient	junction to ambient		[2]	-	-	120	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[3]	-	-	12	K/W

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- Soldering point of cathode tab.



FR4 PCB, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

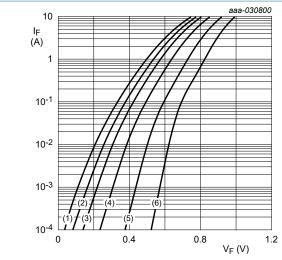
Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)R}$	reverse breakdown voltage	I_R = 1 mA; pulsed; T_j = 25 °C	[1]	150	-	-	V
V _F	forward voltage	I _F = 0.1 A; T _j = 25 °C; pulsed	[1]	-	600	680	mV
		I _F = 0.5 A; T _j = 25 °C; pulsed	[1]	-	690	770	mV
		I _F = 1 A; T _j = 25 °C; pulsed	[1]	-	730	800	mV
		I _F = 2 A; T _j = 25 °C; pulsed	[1]	-	780	850	mV
		I _F = 2 A; T _j = -40 °C; pulsed	[1]	-	870	960	mV
		I _F = 2 A; T _j = 125 °C; pulsed	[1]	-	640	740	mV
I _R	reverse current	V _R = 150 V; T _j = 25 °C; pulsed	[1]	-	0.4	30	nA
		V _R = 150 V; T _j = 125 °C; pulsed	[1]	-	5	50	μΑ
		V _R = 150 V; T _j = 150 °C; pulsed	[1]	-	30	300	μA
C _d	diode capacitance	$V_R = 1 \text{ V; } f = 1 \text{ MHz; } T_j = 25 \text{ °C}$		-	70	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	28	-	pF
t _{rr}	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 1 \text{ A}; I_{R(meas)} = 0.25 \text{ A};$ $T_j = 25 \text{ °C}$		-	8	-	ns
	reverse recovery time ramp recovery	$dI_F/dt = 100 \text{ A/}\mu\text{s}$; $I_F = 1 \text{ A}$; $V_R = 30 \text{ V}$; $T_j = 25 \text{ °C}$		-	14	-	ns
I _{RM}	peak reverse recovery current			-	0.7	-	Α
Q _{rr}	reverse recovery charge			-	7	-	nC
V_{FRM}	peak forward recovery voltage	$I_F = 0.5 \text{ A}; dI_F/dt = 20 \text{ A/}\mu\text{s}; T_j = 25 ^{\circ}\text{C}$		-	715	-	mV

^[1] Very short pulse, in order to maintain a stable junction temperature.



pulsed condition

(1) $T_i = 175 \,^{\circ}C$

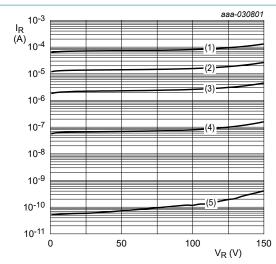
(2) $T_i = 150 °C$

 $(3) T_j = 125 °C$

 $(4) T_j = 85 °C$

(5) $T_j = 25 ^{\circ}C$ (6) $T_j = -40 ^{\circ}C$

Fig. 3. Forward current as a function of forward voltage; typical values



pulsed condition

(1) $T_i = 175 \,^{\circ}C$

(2) $T_i = 150 °C$

(3) $T_i = 125 °C$

 $(4) T_j = 85 °C$

(5) $T_j = 25 \,^{\circ}\text{C}$

Fig. 4. Reverse current as a function of reverse voltage; typical values

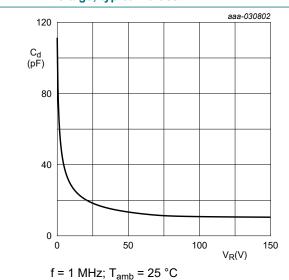
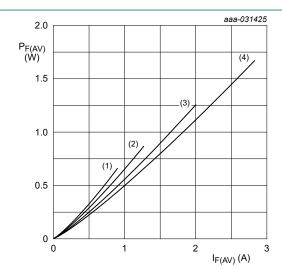


Fig. 5. Diode capacitance as a function of reverse voltage; typical values



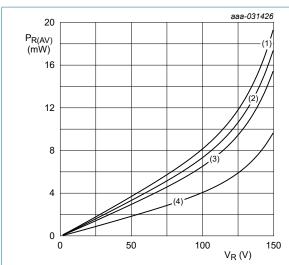
 $T_j = 175 \,^{\circ}\text{C}$ (1) $\delta = 0.1$

 $(2) \delta = 0.2$

 $(3) \delta = 0.5$

(4) δ = 1; DC

Fig. 6. Average forward power dissipation as a function of average forward current; typical values



T_i = 175 °C

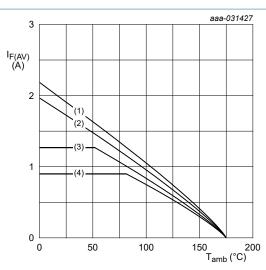
 $(1) \delta = 1$; DC

 $(2) \delta = 0.9$

 $(3) \delta = 0.8$

 $(4) \delta = 0.5$

Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

T_i = 175 °C

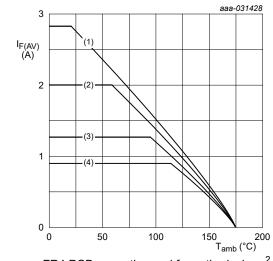
 $(1) \delta = 1; DC$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 8. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm²

T_i = 175 °C

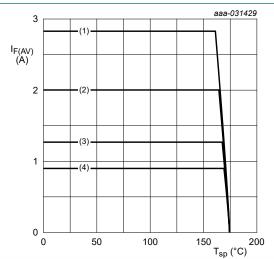
 $(1) \delta = 1$; DC

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values



T_i = 175 °C

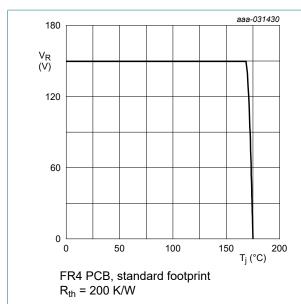
 $(1) \delta = 1; DC$

(2) δ = 0.5; f = 20 kHz

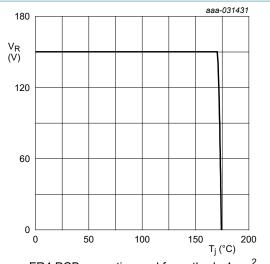
(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 10. Average forward current as a function of solder point temperature; typical values

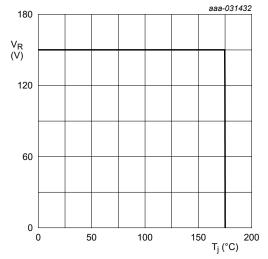


of junction temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm² $R_{th} = 120 \text{ K/W}$

Fig. 11. Derated maximum reverse voltage as a function | Fig. 12. Derated maximum reverse voltage as a function of junction temperature; typical values



Soldering point of cathode tab $R_{th} = 12 \text{ K/W}$

Fig. 13. Derated maximum reverse voltage as a function of junction temperature; typical values

11. Test information

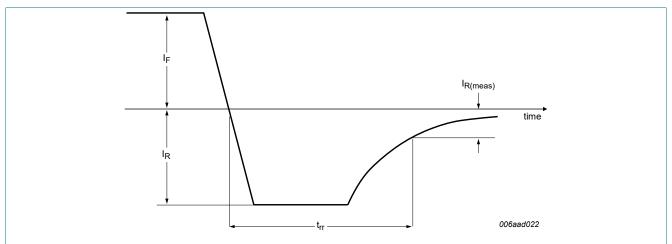


Fig. 14. Reverse recovery definition; step recovery

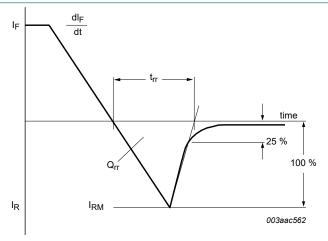


Fig. 15. Reverse recovery definition; ramp recovery

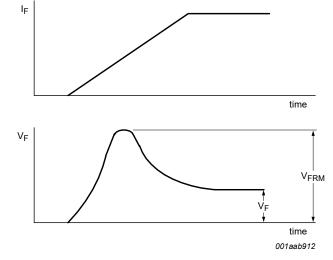
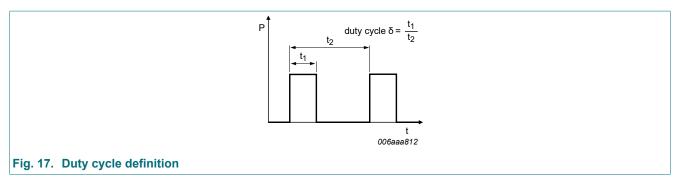


Fig. 16. Forward recovery definition



The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

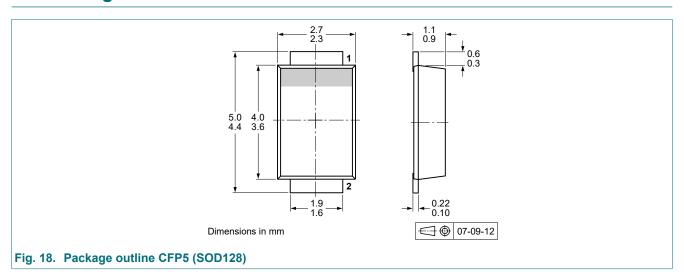
 $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$

with I_{RMS} defined as RMS current.

Quality information

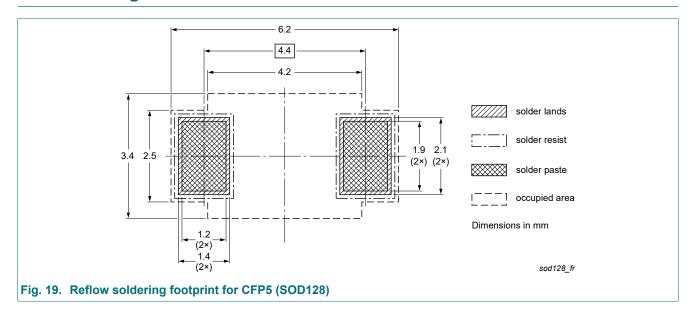
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

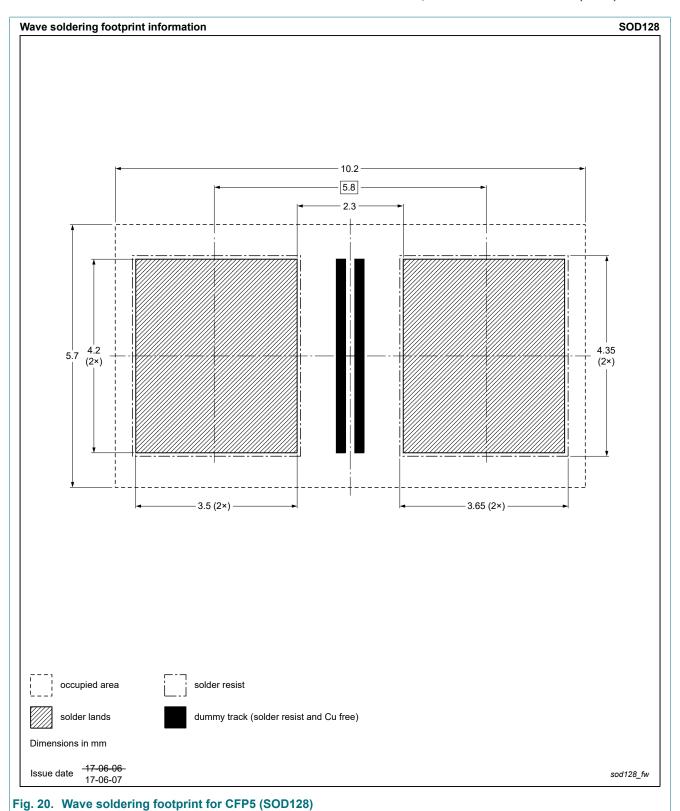
12. Package outline



9 / 14

13. Soldering





14. Mounting

This device is sensitive to Electro Static Discharge (ESD). Observe precautions for handling electrostatic sensitive devices. Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

15. Revision history

Table 8. Revision history

Table of Iteriologic inotes	•							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMEG150G20ELP-Q v.2	20210514	Product data sheet	-	PMEG150G20ELP-Q v.1				
Modifications:	 Features and benefit 	Features and benefits: added recommendation for automotive applications						
PMEG150G20ELP-Q v.1	20210210	Product data sheet	-	-				

16. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Product data sheet

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