

M68EVB912B32 Evaluation Board

Overview

The M68EVB912B32 evaluation board (EVB) is an economical tool for designing and debugging code for and evaluating the operation of the MC68HC912B32 MCU. By providing the essential MCU timing and input/output (I/O) circuitry, the EVB simplifies user evaluation of prototype hardware and software.

The EVB features a prototype area, which allows custom interfacing with the MCU's I/O and bus lines. These connections are broken out via on-board headers immediately adjacent to the MCU.

Figure 1 shows the EVB's layout and locations of the major components as viewed from the component side of the board.





M68EVB912B32 Evaluation Board

 +4.5 to +5.5 Vdc operation RS-232C interface 32 Kbytes of MCU internal FLASH EEPROM with resident D-Bug12 monitor/debugger program Four operational modes: EVB Jump-to-EEPROM BDM pod (background debug mode) Bootload Header connectors for access to the MCU's I/O and bus lines
• On-board prototype area for customized interfacing with the MCU
 Low-voltage inhibit protection Full support for either dumb terminal or host computer terminal interface IASM12 assembly language development toolkit File transfer capability from a host computer, allowing off-board code generation
The EVB is composed of three components:
 Hardware — The board is a 5.15-inch x 3.4-inch (13.08-cm x 8.63-cm) multilayer printed circuit board (PCB) that provides the platform for interface and power connections to the MC68HC912B32 MCU. The MCU is surface mounted to the PCB.
2. Firmware — D-Bug12, the EVB's firmware-resident monitor program, provides a self-contained operating environment that allows writing, evaluating, and debugging user programs.
The hardware is factory configured to execute D-Bug12 without further configuration by the user. It is ready for writing and debugging user code with an RS-232C terminal.
Commands are typed on the terminal's D-Bug12 prompt line and executed when the carriage return (ENTER) key is pressed. D-Bug12 then displays either the appropriate response to the command or an error indication.
3. Software — The ISAM12 assembly language toolset provides an integrated development environment which includes a project manager, relocatable macro-assembler, linker, librarian, Motorola S-record generator, and a variety of other tools.



User Code Generation

EVB user code can be generated by two methods:

- 1. For small programs or subroutines, D-Bug12's single-line assembler/disassembler may be used to place object code directly into the EVB's memory.
- 2. For larger programs, the IASM12 assembler may be used on a host computer to generate S-record object files, which then can be loaded into the EVB's memory using D-Bug12's FLOAD or LOAD command.

If the M68HC912B32's BDM interface serves as the user interface, the SCI (serial communication interface) port becomes available for user applications. This mode requires a background debug development tool, such as Motorola's SDITM interface, and a host computer with the appropriate interface software or another B32EVB.

At reset, the EVB can begin operation in either of four jumper-selectable modes:

- 1. In EVB mode, D-Bug12 immediately issues its command prompt on the terminal display and waits for a user entry.
- 2. In the jump-to-EEPROM mode, execution begins directly with the user code in on-chip EEPROM.
- 3. In BDM pod mode, D-Bug12 serves as a probe interface between a target system and the user via the BDM out connector.
- 4. In bootload mode, a user program may be loaded into the host EVB's byte-erasable or FLASH EEPROM memory.

Information in these tables summarizes EVB specifications and minimum host PC requirements.

Table 1. EVB Specifications				
Characteristic	Specification			
MCU	MC68HC912B32			
MCU I/O ports	HCMOS compatible			
FLASH	32 Kbytes			
EEPROM	768 bytes			
Communications ports	2 RS-232C DCE ports			
Board dimensions	5.15 inches x 3.4 inches; 13.08 cm x 8.63 cm			

1 Kbyte

Table 1 EV/D Specifications

EVB

and PC

Specifications

Requirements

SRAM

Freescale Semiconductor, Inc.

M68EVB912B32 Evaluation Board

Characteristic	Specification
Terminal	RS-232C compatible terminal
Host PC, optional	386-based PC or higher with an RS-232C serial port; requires a user-supplied communications program capable of emulating a dumb terminal
Input/output	Serial communication port
Cable	RS232C compatible, 6-pin BDM target cable
External power supply	+4.5 to +5.5 Vdc @ 100 mA maximum

Table 2. Host PC Minimum Requirements

Ordering Information

Warranty

Refer to **Table 3** for ordering information.

Table 3. Ordering Information

Evaluated MCU	EVB Part Number	Software
MC68HC912B32FU8	M68EVB912B32	IASM12, included with EVB

For a list of the Motorola sales offices and distributors, visit http://www.mcu.motsps.com/sale_off.html.

Motorola provides a 1-year limited warranty.

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document. Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters which may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

RoHS-compliant and/or Pb- free versions of Freescale products have the functionality and electrical characteristics of their non-RoHS-compliant and/or non-Pb- free counterparts. For further information, see http://www.freescale.com or contact your Freescale sales representative.

For information on Freescale.s Environmental Products program, go to http://www.freescale.com/epp.



For More Information On This Product, Go to: www.freescale.com