Freescale Semiconductor, Inc.Order this document by MC12058/D



ARCHIVED BY FREESCALE SEMICONDUCTOR, INC. 2005 **1.1 GHz Low Power Dual Modulus Prescaler**

The MC12058 is a low power ÷126/128, ÷254/256 dual modulus prescaler. Motorola's advanced Bipolar MOSAIC[™] V technology is utilized to achieve low power dissipation of 3.0 mW at a minimum supply voltage of 2.7 V. The MC12058 can be operated down to a minimum supply voltage of 2.7 V required for battery operated portable systems.

On–chip output termination provides 250 μ A (typical) output current to drive a 8.0 pF (typical) high impedance load. The Divide Ratio Control input, SW, permits selection of divide ratio as desired. A HIGH on SW selects \div 126/128; an OPEN on SW selects \div 254/256. The Modulus Control input, MC, selects the proper divide number after SW has been biased to select the desired divide ratio.

- 1.1 GHz Toggle Frequency
- Supply Voltage 2.7 to 5.5 V
- Low Power 1.1 mA Typical at V_{CC} = 3.0 V
- Operating Temperature Range of -40 to 85°C
- On-Chip Output Termination

MOSAIC V is a trademark of Motorola

FUNCTIONAL TABLE

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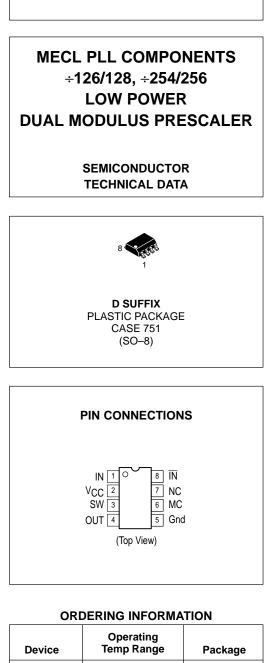
SW	MC	Divide Ratio
н	н	126
Н	L	128
L	Н	254
L	L	256

NOTES: 1. SW: H = V_{CC}, L = Open. A logic L can also be applied by grounding this pin, but this is not recommended due to increased power consumption.
2. MC: H = 2.0 V to V_{CC}, L = GND to 0.8 V.

MAXIMUM RATINGS

Characteristic	Symbol	Range	Unit
Power Supply Voltage, Pin 2	Vcc	-0.5 to 7.0	Vdc
Operating Temperature Range	TA	-40 to 85	°C
Storage Temperature Range	T _{stg}	-65 to 150	°C
Modulus Control Input, Pin 6	MC	–0.5 to V _{CC}	Vdc
Maximum Output Current, Pin 4	ΙO	4.0	mA

NOTE: ESD data available upon request.



 $T_A = -40$ to $85^{\circ}C$

MC12058

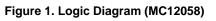
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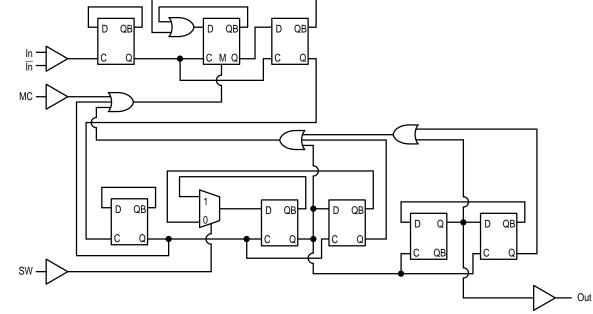
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	Symbol	Min	Тур	Max	Unit
Toggle Frequency (Sine Wave Input)	INC. 2005	0.1	1.4	1.1	GHz
Supply Current Output (Pin 2)	ICC	-	1.1	2.0	mA
Modulus Control Input HIGH (MC)	V _{IH1}	2.0	-	V _{CC} + 0.5	V
Modulus Control Input LOW (MC)	VIL1	Gnd	-	0.8	V
Divide Ratio Control Input HIGH (SW)	V _{IH2}	V _{CC} – 0.5	VCC	V _{CC} + 0.5	V
Divide Ratio Control Input LOW (SW)	V _{IH2}	Open	Open	Open	-
Output Voltage Swing (Note 1)	Vout	0.8	1.1	-	V _{pp}
Modulus Setup Time MC to OUT at 1100 MHz	t _{set}	-	11	16	ns
Input Voltage Sensitivity 250–1100 MHz 100–250 MHz	V _{in}	100 400	-	1000 1000	mVpp





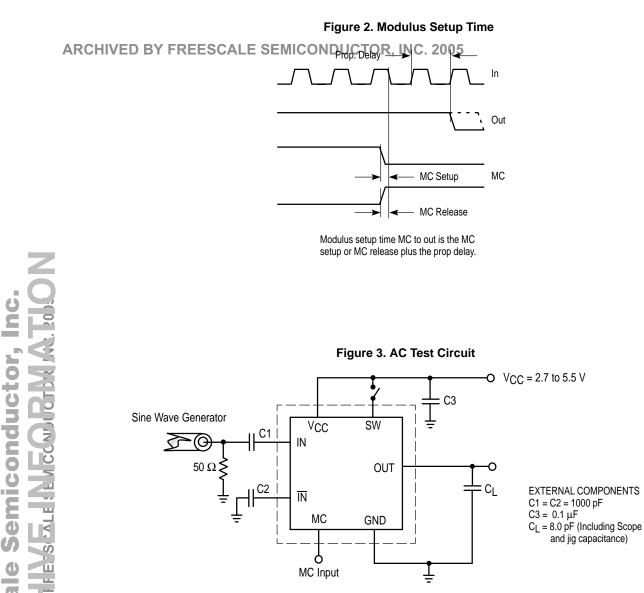


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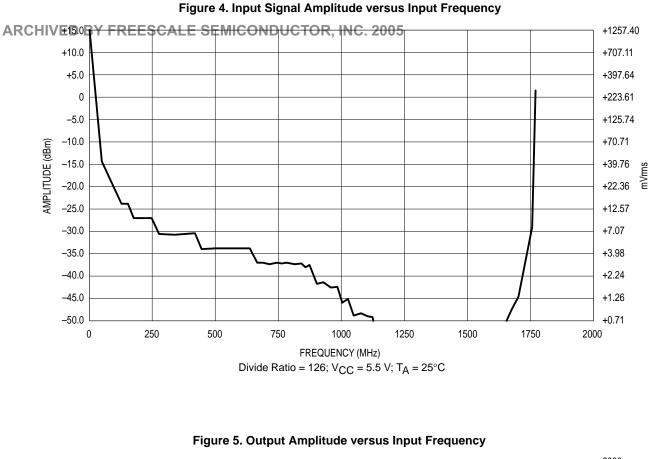
MC Input

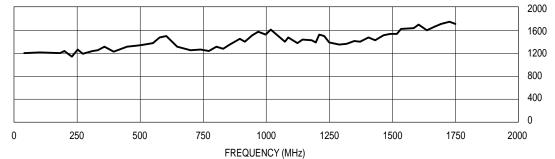
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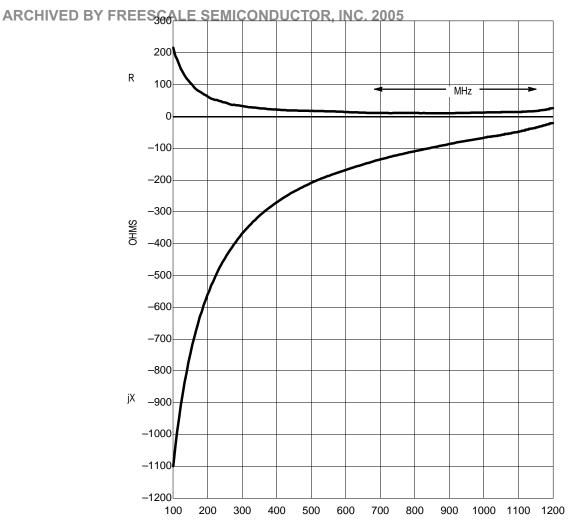


Figure 6. Typical Input Impedance versus Input Frequency

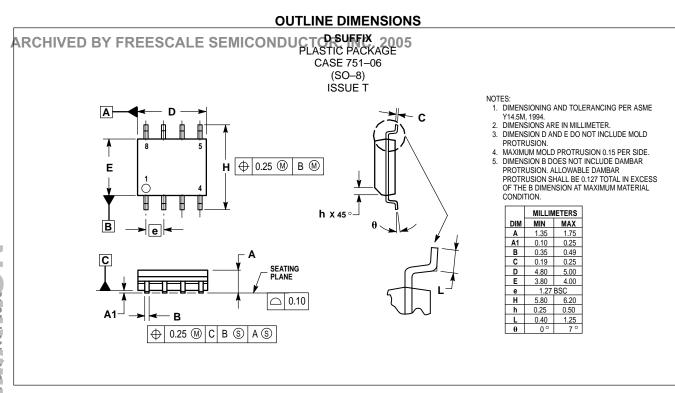


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