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Silicon Lateral FET, N-Channel **Enhancement-Mode MOSFET**

Designed for use in low voltage, moderate power amplifiers such as portable analog and digital cellular radios and PC RF modems.

- Performance Specifications at 900 MHz: Output Power = 36.5 dBm Typ Power Gain = 10.5 dB Typ Efficiency = 65% Typ
- Guaranteed Ruggedness at Load VSWR = 10:1
- New Plastic Surface Mount Package

LDMOS FET 36.5 dBm, 900 MHz

SEMICONDUCTOR **TECHNICAL DATA**

MRF9382T1

HIGH FREQUENCY

POWER TRANSISTOR

- Pin 1. Drain
 - 2. Gate
 - 3. Source
 - 4. Source



PLASTIC PACKAGE **CASE 449** (PLD-1, Tape & Reel Only)

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	28	Vdc
Drain–Gate Voltage (RGS = 1.0 M Ω)	V _{DGO}	28	Vdc
Gate–Source Voltage	VGS	±12	Vdc
Drain Current - Continuous	ID	2.2	Adc
Total Device Dissipation @ T _C = 50°C Derate above 50°C	PD	12.5 125	W mW/°C
Storage Temperature Range	T _{stg}	-65 to 150	°C
Operating Junction Temperature	TJ	150	°C

NOTE: ESD data available upon request.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	8.0	°C/W

ORDERING INFORMATION

Device	Marking	Package
MRF9382T1	9382	PLD-1
		Tape & Reel*

*1,000 Units per 12 mm, 7 inch reel.

SEMICONDUCTOR, INC.

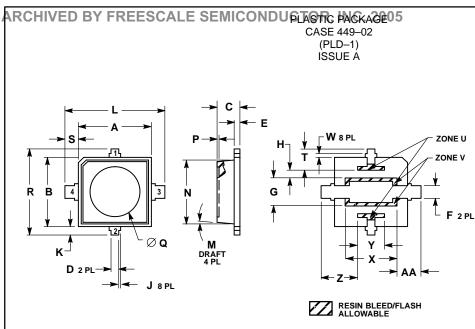
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OUTLINE DIMENSIONS



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14 5M 1982
- 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.185	0.195	4.70	4.95
В	0.175	0.185	4.44	4.70
С	0.058	0.064	1.47	1.63
D	0.017	0.023	0.43	0.58
Е	0.014	0.017	0.36	0.43
F	0.027	0.033	0.69	0.84
G	0.071	0.077	1.80	1.96
Н	0.017	0.023	0.43	0.58
J	0.000	0.007	0.00	0.18
K	0.018	0.026	0.46	0.66
L	0.253	0.263	6.43	6.68
M	5 °REF		5°REF	
N	1.75 REF		4.44 REF	
Р	0.000	0.006	0.00	0.15
Q	0.120	0.130	3.05	3.30
R	0.220	0.230	5.59	5.84
S	0.030	0.038	0.76	0.97
Т	0.050	0.060	1.27	1.52
U	0.000	0.018	0.00	0.46
٧	0.000	0.014	0.00	0.36
W	0.004	0.016	0.10	0.41
Х	0.131	0.141	3.33	3.58
Υ	0.065	0.075	1.65	1.90
Z	0.089	0.099	2.26	2.51
AA	0.056	0.066	1.42	1.67

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